BT169W

0.8A SENSITIVE SCRs

DESCRIPTION:

Highly sensitive triggering levels, the BT169 Series SCRs is suitable for all applications, where the available gate current is limited, such as capacitive discharge ignitions, motor control in kitchen aids, overvoltage crowbar protection in low power supplies...

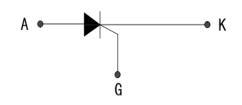
MAIN FEATURES

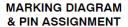
Symbol	Value	Unit
IT (RMS)	0.8	Α
VDRM/VRRM	600	V
lgт	≤ 200	μΑ

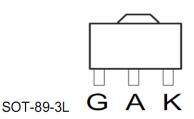


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Simplified Schematic







ABSOLUTE MAXIMUM RATINGS

Parameter			Value	Unit
Storage junction temperature range			- 40 to +150	°C
Operrating junction temperature range			- 40 to +110	°C
Repetitive Peak Off-state Voltage Tj=25℃			600	V
Repetitive Peak Reverse Voltage	Tj=25℃	VRRM	600	V
RMS on-state current (180 conduction angle)	Tc=77°C	IT(RMS)	0.8	Α
Average on-state current (180 conduction angle)	Tc=77°C	I T(AV)	0.5	А
	tp=10ms		8	Α
Non repetitive surge peak on-state current (Tj=25℃)	tp=8.3ms	I TSM	9	А
I²t Value for fusing	tp=10ms	I²t	0.32	A²s
Peak gate current tp=20us,Tj=110°C			0.2	Α
Average gate power dissipation Tj=110°C			0.1	w

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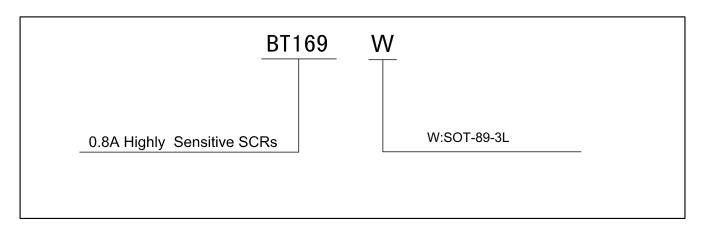
ELECTRICAL CHARACTERISTICS(Tj=25℃ unless otherwise specified)

Symbol	Test Condition		BT169W			Unit
Symbol			Min.	Тур.	Max.	
IGT	VD=6V RL=100Ω		-	40	200	μA
VGT			-	0.6	0.8	V
VGD	VD=VDRM RL=3.3KΩ RGK=1KΩ Tj =110℃		0.2	-	-	V
IL	IG=1mA RGK=1KΩ		-	-	6	mA
Ін	IT =50mA RGK=1KΩ		-	-	5	mA
VTM	IT = 1A tp=380uS Tj=25 ℃		-	1.2	1.35	V
dV/dt	VD=67%VDRM RGK=1KΩ	Tj=110 ℃	10	-	-	V/µs
IDRM	VD= VDRM RGK=1KΩ	Tj=25 ℃	-	-	5	μA
		Tj=110 ℃	-	-	0.1	mA
IRRM	VR = VRRM RGK=1KΩ	Tj=25 ℃	-	-	5	μA
		Tj=110 ℃	-	-	0.1	mA

THERMAL RESISTANCES

Symbol	Parameter	Parameter		Unit
Rth(J-C)	Junction to Case	SOT-89-3L	60	°C/W

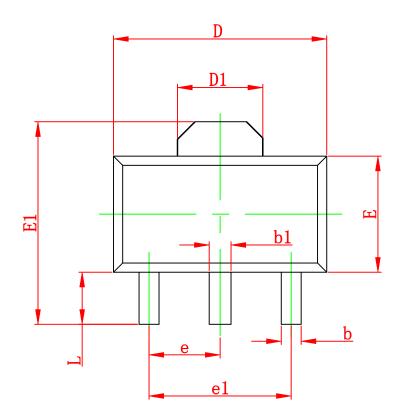
ORDERING INFORMATION

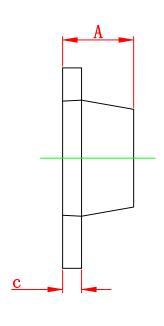


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SOT-89-3L PACKAGE OUTLINE DIMENSIONS





Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.400	1.600	0.055	0.063	
b	0.320	0.520	0.013	0.197	
b1	0.400	0.580	0.016	0.023	
С	0.350	0.440	0.014	0.017	
D	4.400	4.600	0.173	0.181	
D1	1.550 REF		0.061 REF		
E	2.300	2.600	0.091	0.102	
E1	3.940	4.250	0.155	0.167	
е	1.500 TYP		0.060TYP		
e1	3.000	3.000 TYP 0.118TYP		8TYP	
L	0.900	1.200	0.035	0.047	

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FIG.1: Maximum power dissipation versus RMS on-state current(full cycle)

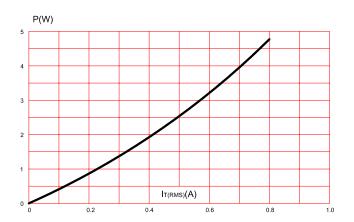


FIG.3: On-state characteristics (maximum values)

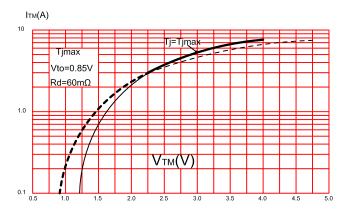


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms.

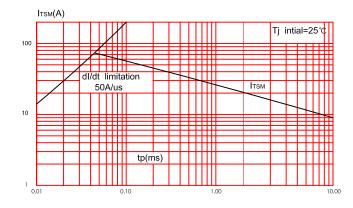


FIG.2: RMS on-state current versus case temperature(full cycle)

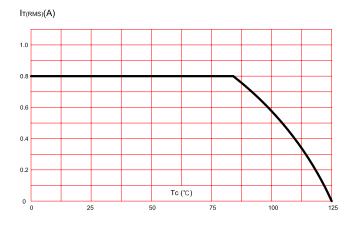


FIG.4: Surge peak on-state current versus number of cycles.

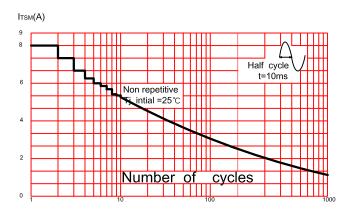
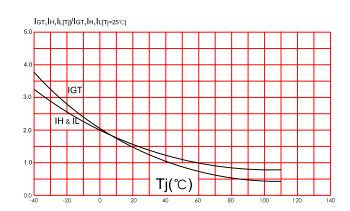


FIG.6: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values).



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