# MT10G036T

# N-Channel Enhancement Mode Field Effect Transistor

# **Product Summary**

- V<sub>DS</sub> = 100V
- $I_D = 240A$
- R DS(ON) =  $3.3 \, \text{m} \Omega \, \text{@V}_{\text{GS}} = 10 \, \text{V}$

# **Features**

- · Advanced Trench Process Technology.
- · High Density Cell Design for Ultra Low On-Resistance.
- · Lead free product is acquired.
- · RoHS Compliant.
- · TOLL Packge

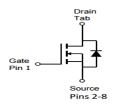
## **Applications**

- · Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

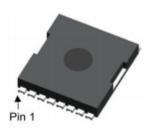
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# **Simplified Schematic**



#### MARKING DIAGRAM & PIN ASSIGNMENT



# **Absolute Maximum Ratings** (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter		Steady State	Units
V <sub>DS</sub>	Drain-Source Voltage		100	V
V <sub>G</sub> s	Gate-Source Voltage		± 20	V
I <sub>D</sub>	Continuous Drain Current <sup>1</sup>	− T <sub>C</sub> = 25°C	240	Α
lом	Pulsed Drain Current <sup>2</sup>	10 - 25 C	980	Α
Is	Continuous Source Current (Diode Conduction) 1		220	Α
E <sub>AS</sub>	Single Pulse Drain-Source Avalanche Energy <sup>3</sup>		658	mJ
$P_{D}$	Maximum Power Dissipation	T <sub>C</sub> = 25°C	270	w
TJ, TSTG	Operating Junction and Storage Temperature Range		-55~150	$^{\circ}$

#### Notes:

- 1. Surface Mounted on 1" x 1" FR4 Board, t≦10 Sec.
- 2. Pulse width limited by maximum junction temperature.
- 3. The test condition is  $T_J$  =25°C,  $V_{DD}$ =30V,  $V_{GS}$ =10V, L=0.1mH,  $R_G$ =25 $\Omega$ ,  $I_{AS}$ =50A.

# **Thermal Characteristic**

Thermal Resistance,Junction-to-Case	$R_{ heta JC}$	0.4	°C/W	
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**Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)** 

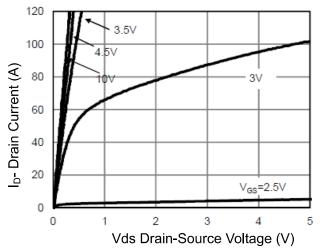
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·					
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100 V, V <sub>GS</sub> =0 V	-	-	1	uА
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	100	nA
On Characteristics	•		•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2.0	2.8	4.0	V
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10 V, I <sub>D</sub> =30A	-	3.3	4.4	mΩ
Dynamic Characteristics <sup>b</sup>			1			
Input Capacitance	C <sub>Iss</sub>	\/ -50\/\/ -0\/	-	10080	-	PF
Output Capacitance	Coss	$V_{DS}$ =50V, $V_{GS}$ =0V, F=1.0MHz	-	1890	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F = 1.0WI112	-	68	-	PF
Switching Characteristics						
Turn-on Delay Time	t <sub>d(on)</sub>		-	28	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, I_D = 90 \text{ A}$	-	98	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =3.0 $\Omega$	-	75	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	98	-	nS
Total Gate Charge	Qg	V <sub>DS</sub> =50V,I <sub>D</sub> =90A,	-	128		nC
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ - $50V$ , $I_D$ - $90A$ , $V_{GS}$ = $10V$	-	41		nC
Gate-Drain Charge	$Q_{gd}$	VGS-10V	-	28		nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =30A	-	0.75	1.4	V
Diode Forward Current	l <sub>S</sub>		-	-	220	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 90A	-	51	_	nS
Reverse Recovery Charge	Qrr	di/dt = 500A/µs	-	92	-	nC
Forward Turn-On Time	t <sub>on</sub>	t <sub>on</sub> Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

# Note:

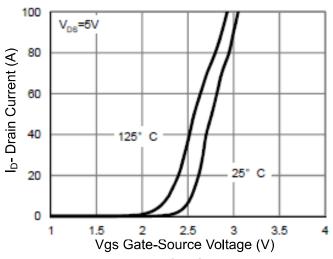
a. Pulse test; pulse width≦300μs, duty cycle≦2%.

b. Guaranteed by design, not subject to production testing.

# **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

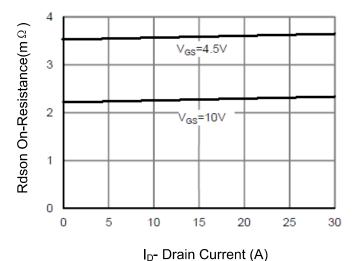


Figure 3 Rdson- Drain Current

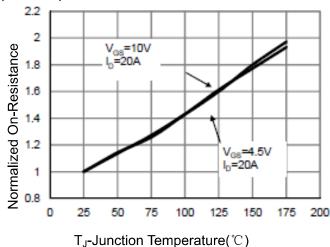


Figure 4 Rdson-JunctionTemperature

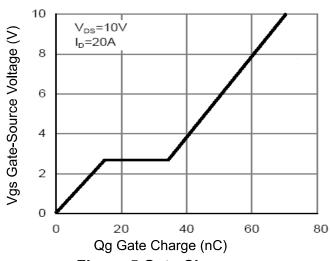


Figure 5 Gate Charge

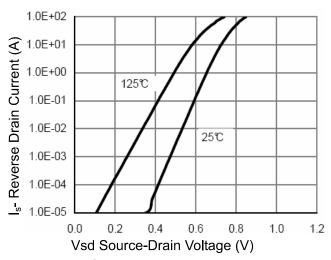


Figure 6 Source- Drain Diode Forward

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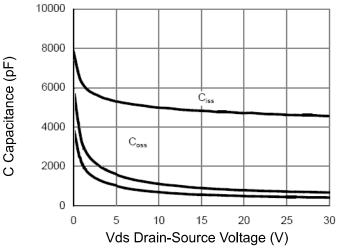
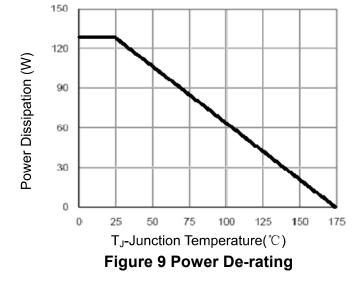


Figure 7 Capacitance vs Vds



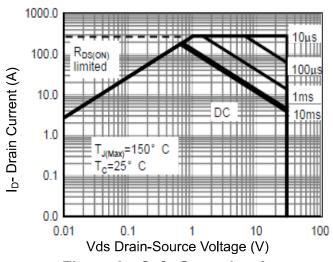


Figure 8 Safe Operation Area

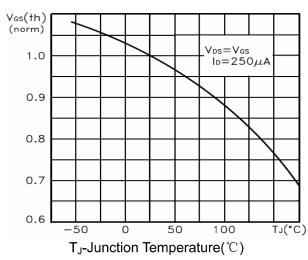
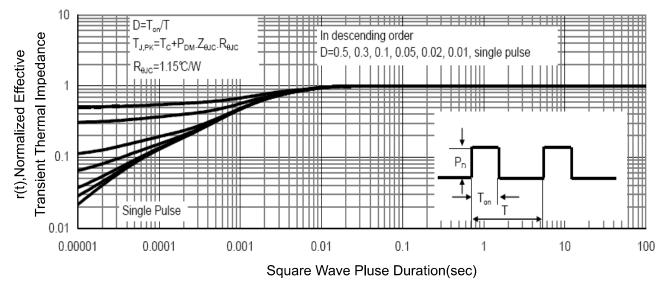


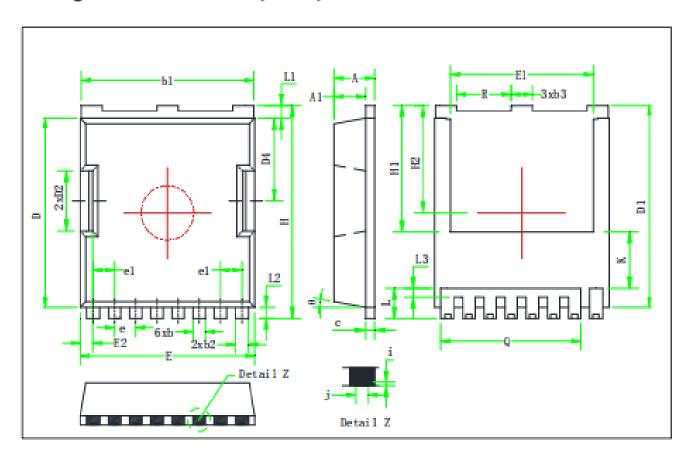
Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



**Figure 11 Normalized Maximum Transient Thermal Impedance** 

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# Package Mechanical Data(TOLL)



Symbol	Min Typ		Max	
Α	2.25	2.30	2.35	
A1	1.75	1.80	1.85	
ь	0.65	0.70	0.75	
bl	9.75	9.80	9.85	
b2	0.70	0.75	0.80	
Ь3	1.15	1.20	1.25	
c	0.45	0.50	0.55	
D	10.35	10.40	10.45	
D1	11.00	11,10	11,20	
D2	3.25	3.30	3.35	
D4	4.50	4.55	4.60	
e	1.20 BSC			
el	1.225 BSC			
E	9.85	9.90	9.95	
El	8.00	8.10	8.20	

Symbol	Min	Тур	Max	
E2	0.65	0.70	0.75	
H	11.60	11.70	11.80	
HI	6.95 BSC			
H2	5.90 BSC			
i	0.10 REF			
j	0.35 REF			
K	3.10 REF			
L	1.55 1.65 1.75			
L1	0.65	0.70	0.75	
L2	0.50	0.60	0.70	
L3	0.40	0.50	0.60	
Q	7.95 REF			
R	3.05 3.10		3.15	
θ	10°REF			

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