MT19N10

N-Channel 100V Power MOSFET

Features

- Typ $R_{DS}(on)=90m\Omega(typ)$ @ $V_{GS}=10V,I_D=8A$
- · Fast Switching Speed
- · Low Gate Charge
- · High Power and Current Handling Capability

General Description

This N-Channel MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

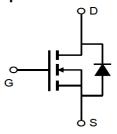
Applications

- · DC-DC primary bridge
- DC-DC Synchronous rectification
- DC FAN



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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



MOSFET Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter			Ratings	Units	
V _{DSS}	Drain to Source Voltage			100	V	
V_{GSS}	Gate to Source Voltage			±20	V	
	Drain Curren - Continuous (Silicon Limited) T _C = 25°C				15.6	
	- Continuous(Package Limited) T _C = 25°C			T _C = 25°C	8.5	Α
I _D	- Continuous			$T_C = 25^{\circ}C(Note 1a)$	45	
	- Pulsed			62.4	Α	
E _{AS}	Single Pulsed Avalanche Energy			(Note3)	25	mJ
P _D	D D: : ::		- T _C = 25°C	(Note 1a)	50	W
	Power Dissipation		- T _A = 25°C	(Note 1b)	0.4	W/°C
T _{.I} , T _{STG}	Operating and Storage Temperature Range			-55 to +150	°C	

Thermal Characteristics

Symbol	Parameter	Ratings	Units	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Note 1)	3.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	55	- C/VV

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
MT19N10	MT19N10	TO-252-2L	-	-	2500

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Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
Off Cha	aracteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA		100			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced	to 25°C		0.09		V/°C
I _{DSS}	7 0 1 1/1 1 2 1 0 1	V _{DS} = 100 V, V _{GS} = 0 V				1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = 80 V, T _C = 125°C				10	μА
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V				100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -20 V, V _{DS} = 0 V				-100	nA
On Cha	racteristics						
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		1.0		2.9	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 8A			90		mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 30 V, I _D = 7.8 A	(Note 4)		11		s
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		 		1600 800 290	pF pF
	ing Characteristics						
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 50 \text{ V}, I_{D} = 19 \text{ A},$ $R_{G} = 25 \Omega$			12	31	ns
t _r	Turn-On Rise Time				400	800	ns
t _{d(off)}	Turn-Off Delay Time	1 NG - 23 32			20	50	ns
t _f	Turn-Off Fall Time	-	(Note 4, 5)		120	250	ns
Q _g	Total Gate Charge	V _{DS} = 80 V, I _D = 19 A,			12	14	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 5 V			2.5		nC
Q _{gd}	Gate-Drain Charge	(Note 4, 5)			9.0		nC
	Source Diode Characteristics a	nd Maximum Rating	s				
I _S	Maximum Continuous Drain-Source Diode Forward Current					15.6	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current					62.4	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15.6 A				0.8	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 19 A,			80		ns
11	TREVEISE TREGOVERY THIRE	165 0 1,15 1011	(Note 4)				

 $[\]label{eq:Notes:1} \textbf{Notes:} \\ \textbf{1. Repetitive Rating: Pulse width limited by maximum junction temperature } \\ \textbf{2. L} = \textbf{1.35mH, } \\ \textbf{I}_{AB} = \textbf{15.6A}, \\ \textbf{V}_{DD} = \textbf{25V}, \\ \textbf{R}_{G} = \textbf{25} \ \Omega, \\ \textbf{Starting } \\ \textbf{T}_{J} = \textbf{25}^{\circ} \textbf{C} \\ \textbf{3. } \\ \textbf{I}_{SD} \leq \textbf{19A}, \\ \textbf{didt} \leq \textbf{300A/\mus}, \\ \textbf{V}_{DD} \leq \textbf{BV}_{DSS}, \\ \textbf{Starting } \\ \textbf{T}_{J} = \textbf{25}^{\circ} \textbf{C} \\ \textbf{4. Pulse Test: Pulse width } \leq \textbf{300\mus}, \\ \textbf{Duty cycle} \leq \textbf{2\%} \\ \textbf{5. Essentially independent of operating temperature} \\ \\ \textbf{6. Essentially independent of operating temperature} \\ \textbf{6. Essentially independent of operating temperature} \\ \textbf{7. Essentially independent of operating temperature} \\ \textbf{8. Essentially independent of operating temperature} \\ \textbf{8.$

Typical Characteristics

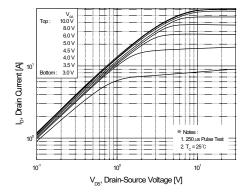


Figure 1. On-Region Characteristics

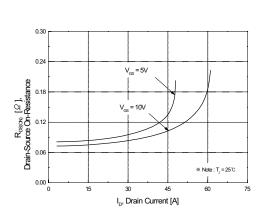


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

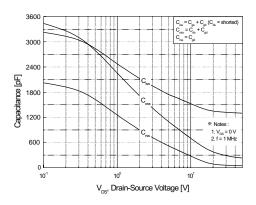


Figure 5. Capacitance Characteristics

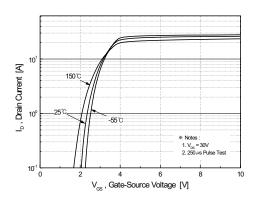


Figure 2. Transfer Characteristics

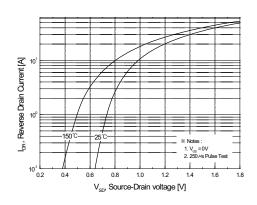


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

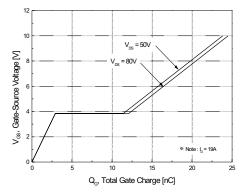


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

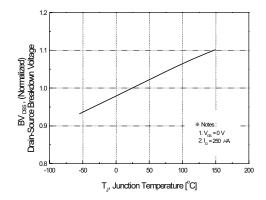


Figure 7. Breakdown Voltage Variation vs. Temperature

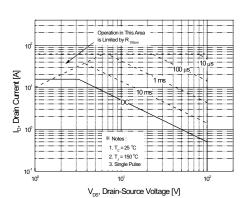


Figure 8. On-Resistance Variation vs. Temperature

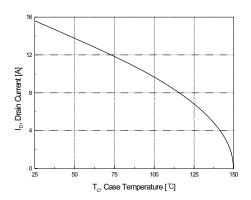


Figure 9. Maximum Safe Operating Area



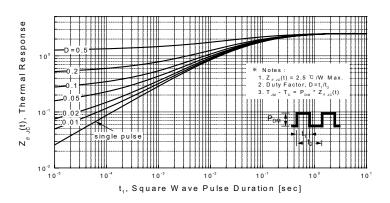
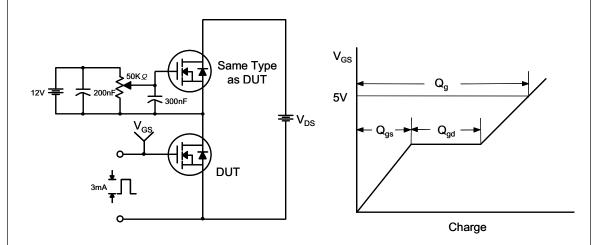


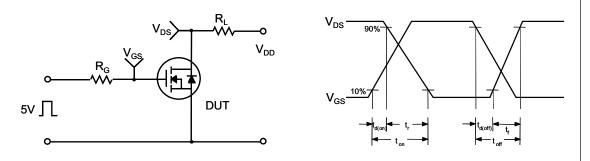
Figure 11. Transient Thermal Response Curve

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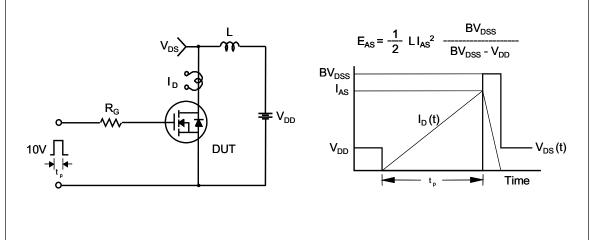
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



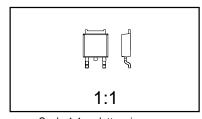
5

Peak Diode Recovery dv/dt Test Circuit & Waveforms DUT I_{SD} & Driver Same Type as DUT V_{DD} • dv/dt controlled by R_G • I_{SD} controlled by pulse period Gate Pulse Width \textbf{V}_{GS} Gate Pulse Period 10V (Driver) \mathbf{I}_{FM} , Body Diode Forward Current \mathbf{I}_{SD} di/dt (DUT) I_{RM} Body Diode Reverse Current \textbf{V}_{DS} (DUT) Body Diode Recovery dv/dt V^{DD} **Body Diode** Forward Voltage Drop

Package Dimensions

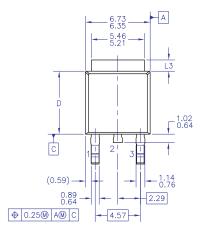
TO-252-2L



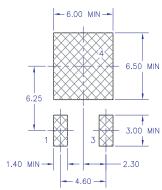


Scale 1:1 on letter size paper Dimensions shown below are in: millimeters

Part Weight per unit (gram): 0.33

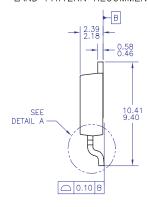


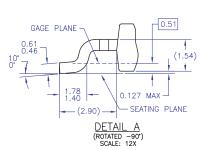
SEE NOTE D



LAND PATTERN RECOMMENDATION







- NOTES: UNLESS OTHERWISE SPECIFIED

 A) ALL DIMENSIONS ARE IN MILLIMETERS.

 B) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA & AB, DATED NOV. 1999.

 C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

 D) HEAT SINK TOP EDGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

 E) DIMENSIONS L3,D,E1&D1 TABLE:

	OPTION AA	OPTION AB
L3	0.89-1.27	1.52-2.03
D	5.97-6.22	5.33-5.59
E1	4.32 MIN	3.81 MIN
D1	5.21 MIN	4.57 MIN

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