

MT3010P

P-Channel Enhancement Mode Field Effect Transistor

Product Summary

- $V_{DS} = -30V$
- $I_D = -90A$ ($V_{GS} = -10V$)
- $R_{DS(ON)} \leq 7m\ \Omega @ V_{GS} = -10V$
- $R_{DS(ON)} \leq 10m\ \Omega @ V_{GS} = -4.5V$

Features

- Advanced Trench Process Technology.
- High Density Cell Design for Ultra Low On-Resistance.
- Lead free product is acquired.
- RoHS Compliant.

Applications

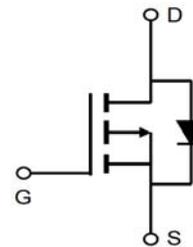
- Notebook Computer
- Portable Battery Pack



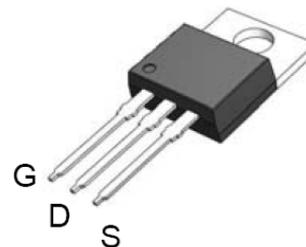
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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-220FB-3L

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings		Units
V_{DS}	Drain-Source Voltage	-30		V
V_{GS}	Gate-Source Voltage	± 20		V
I_D	Continuous Drain Current	-90		A
I_{DM}	Pulsed Drain Current	-220		A
I_S	Continuous Source Current (Diode Conduction) ¹	-2.7	-1.36	A
P_D	Maximum Power Dissipation ¹	200		W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to 150		$^\circ C$

Thermal Resistance Ratings

Symbol	Parameter	Typical	Maximum	Unit
R_{thJA}	Maximum Junction-to-Ambient ¹	$t \leq 10$ Sec	33	$^\circ C/W$
		Steady State	70	

R _{thJF}	Maximum Junction-to-Foot (Drain)	Steady State	16	21	
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Notes:

1. Surface Mounted on 1" x 1" FR4 Board.

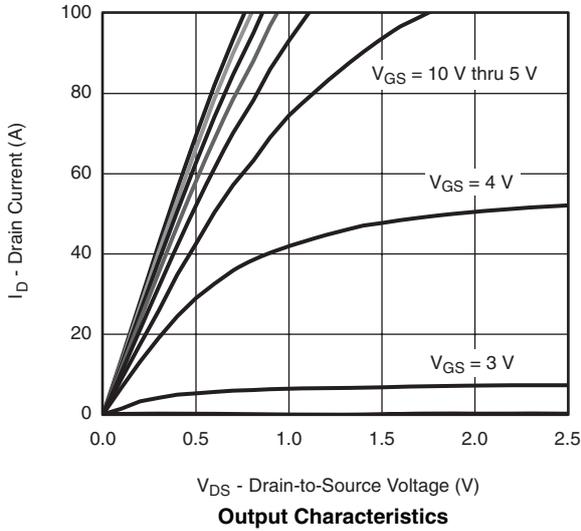
Electrical Characteristics (T_A=25°C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
● Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = -250μA	-1.0	-1.5	-3.0	V
I _{GSS}	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -24V, V _{GS} = 0V	-	-	-1	μA
		V _{DS} = -24V, V _{GS} = 0V, T _J = 70°C			-10	
R _{DS(on)}	Drain Source On State Resistance ^a	V _{GS} = -10V, I _D = -13A	-	7	8	mΩ
		V _{GS} = -4.5V, I _D = -10A	-	10	11	
g _{fs}	Forward Transconductance ^a	V _{DS} = -15V, I _D = -13A	-	40	-	S
V _{SD}	Diode Forward Voltage ^a	I _S = -2.7A, V _{GS} = 0V	-	-0.74	-1.1	V
● Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{DS} = -8V, V _{GS} = 0V, Frequency = 1MHz	-	2700.0	-	pF
C _{oss}	Output Capacitance		-	515.0	-	
C _{rss}	Reverse Transfer Capacitance		-	445.0	-	
Q _g	Total Gate Charge	V _{DS} = -15V, V _{GS} = -5V, I _D = -13A	-	60.0	-	nC
Q _{gs}	Gate-Source Charge		-	93.0	-	
Q _{gd}	Gate-Drain Charge		-	15.0	-	
t _{d(on)}	Turn-On Delay Time	V _{DD} = -15V, R _L = 1.5Ω I _D = -10A, V _{GEN} = -10V, R _G = 1Ω	-	12.0	-	nSec
t _r	Rise Time		-	11.0	-	
T _{d(off)}	Turn-Off Delay Time		-	40.0	-	
t _f	Fall Time		-	12.0	-	
R _g	Gate Resistance	V _{GS} = 0, V _{DS} = 0, f = 1MHz	-	3.4	-	Ω
t _{rr}	Source-Drain Reverse Recovery Time	I _F = -2.1A, di/dt = 100A/μs	-	60	100	nSec

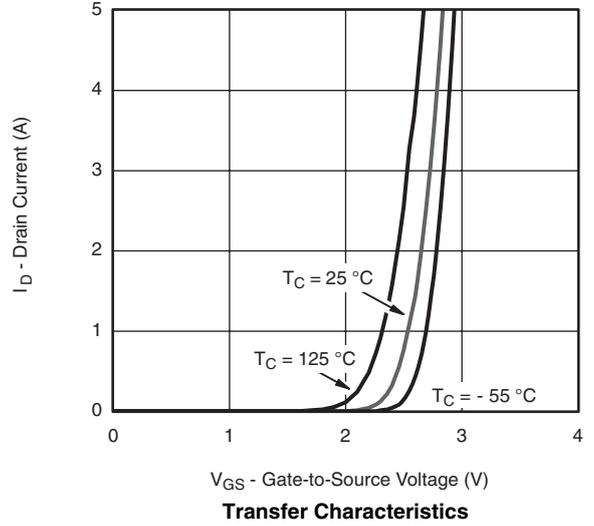
Note:

- a. Pulse test; pulse width ≤ 300μs, duty cycle ≤ 2%.
- b. Guaranteed by design, not subject to production testing.

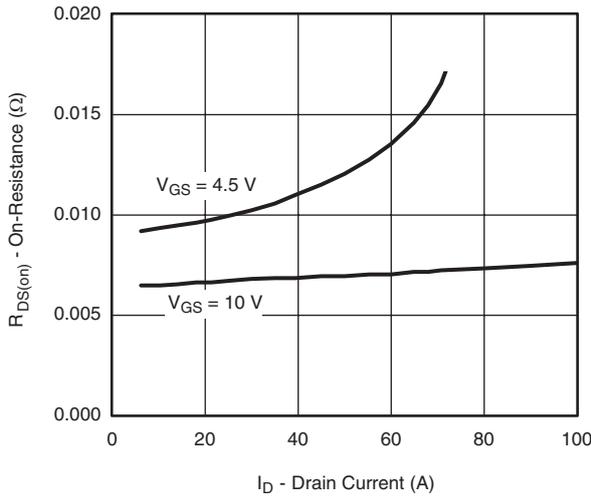
Characteristics Curve



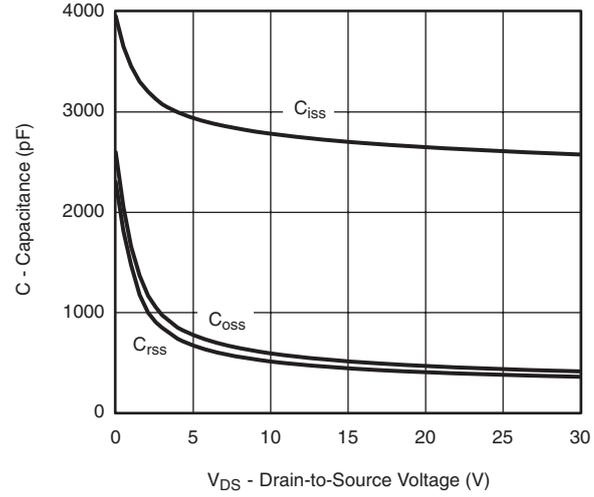
Output Characteristics



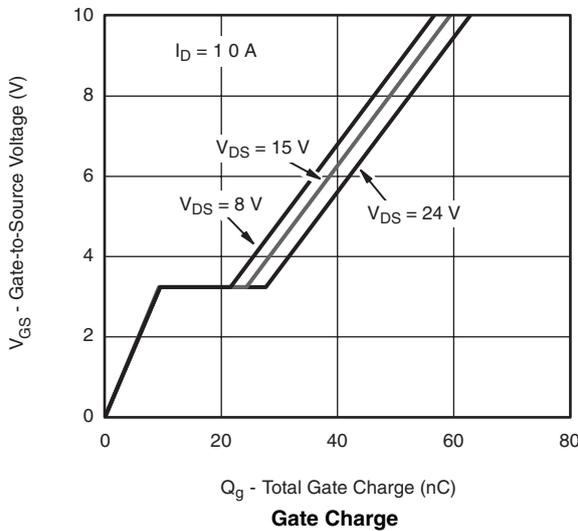
Transfer Characteristics



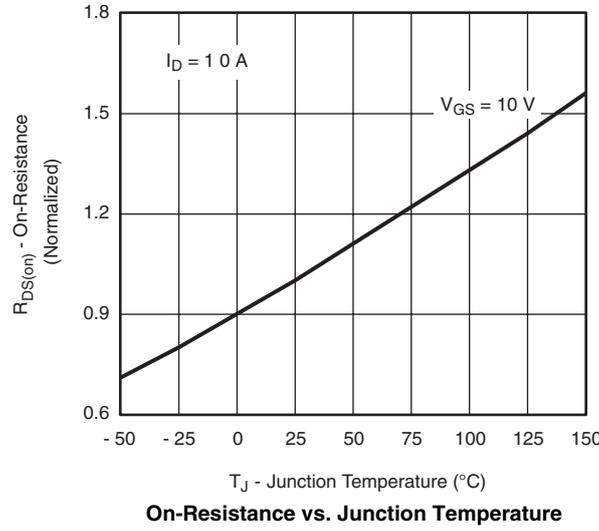
On-Resistance vs. Drain Current



Capacitance

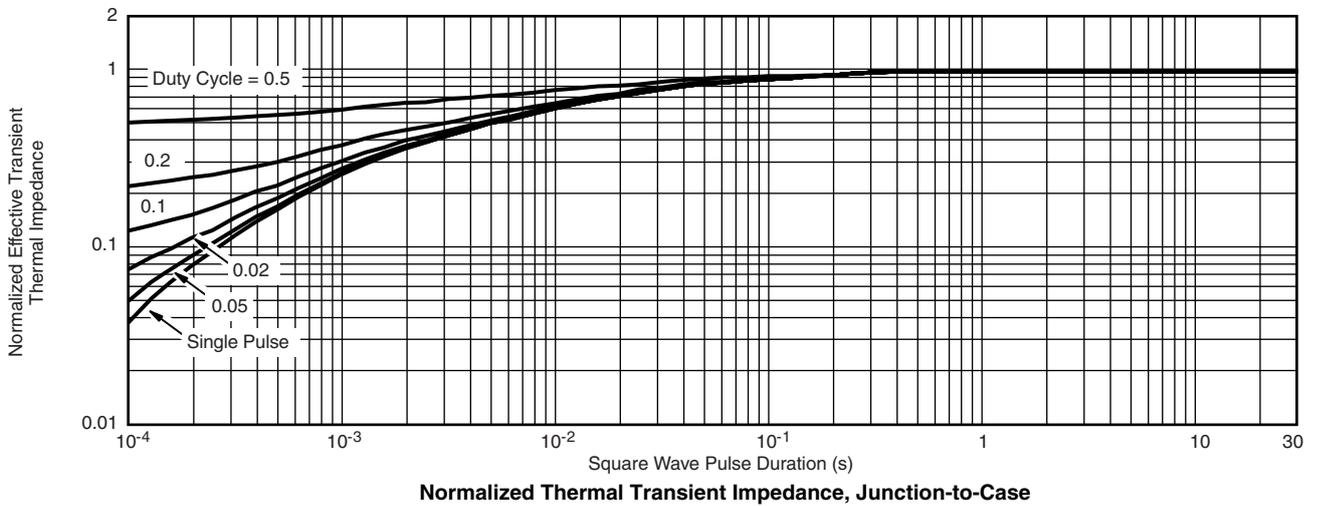
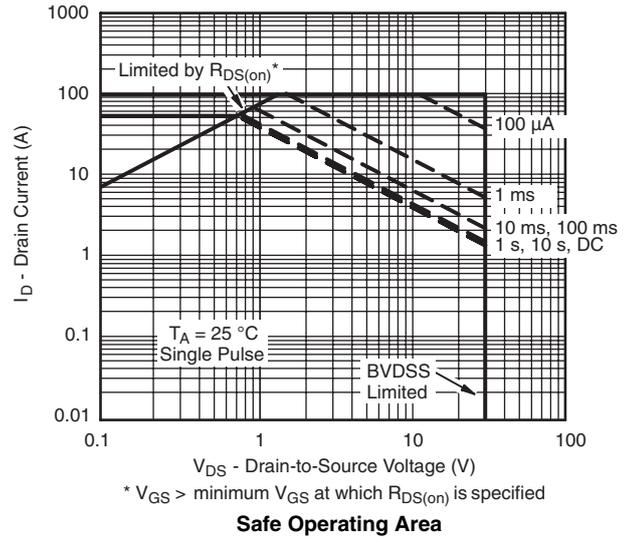
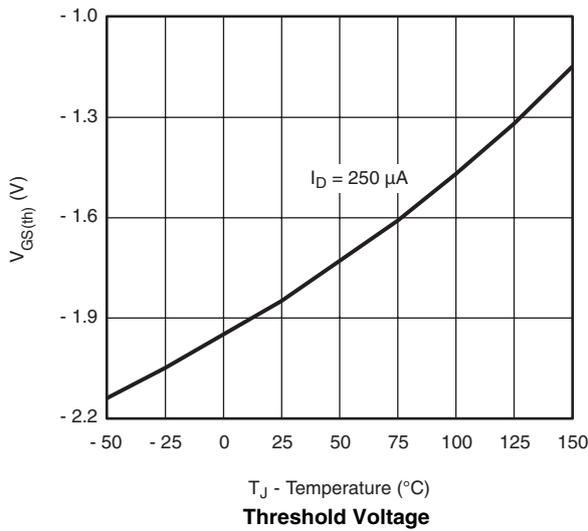
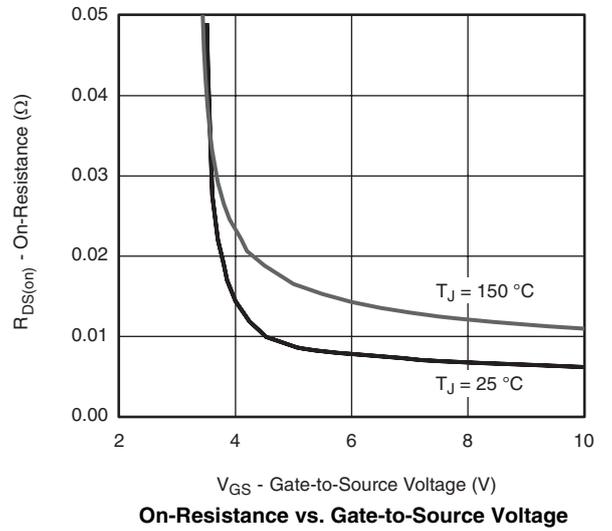
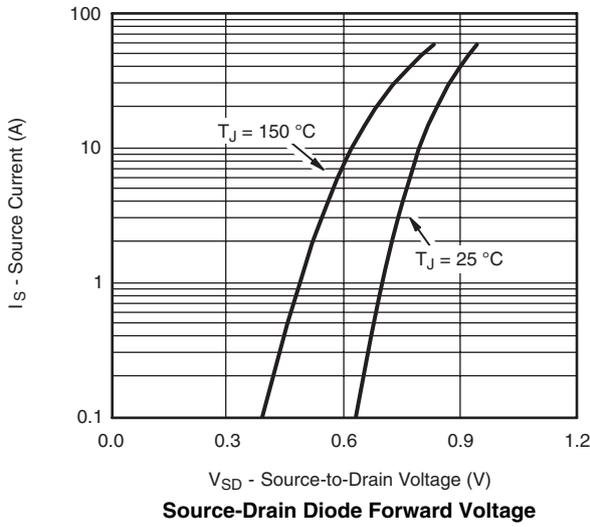


Gate Charge



On-Resistance vs. Junction Temperature

Characteristics Curve



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