# MT3206

## 60V N-Channel MOSFET

# **General Description**

These N-Channel enhancement mode power field effect transistors are produced using Mos-tech's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior swiching performance, and withstand high energy pulse in the avalanche and commutation mode.

#### **Features**

- 50A, 60V,  $R_{DS(on)} = 0.009 \Omega$  @ $V_{GS} = 10 V$
- Low gate charge(typical 43 nC)
- Low Crss(typical 85 nF)
- · Fast switching
- · 100% avalanche tested
- · Impproved dv/dt capability
- 175 °C maximum junction temperature rating

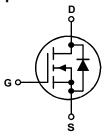
# **Applications**

- · DC/DC converters
- Automotive
- Portable equipment

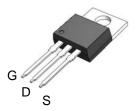


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### Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-220FB-3L

# **Absolute Maximum Ratings**(T<sub>A</sub> = 25℃ unless otherwise noted)

Symbol	Parameter		MT3206	Units	
V <sub>DSS</sub>	Drain-Source Voltage		60	V	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C)	)	50	Α	
	- Continuous (T <sub>C</sub> = 100°C)		28	Α	
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	180	Α	
V <sub>GSS</sub>	Gate-Source Voltage		± 25	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	420	mJ	
I <sub>AR</sub>	Avalanche Current	(Note 1)	40	А	
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	10	mJ	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns	
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)		100	W	
	- Derate above 25°C		0.9	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C	
T <sub>L</sub>	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C	

#### **Thermal Characteristics**

Symbol	Parameter	Тур	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		1.64	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink	0.7		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		65.5	°C/W

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$	60			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to	25°C	0.06		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V			1	μА
		V <sub>DS</sub> = 48 V, T <sub>C</sub> = 150°C			10	μА
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 25 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -25 V, V <sub>DS</sub> = 0 V			-100	nA
On Cha	aracteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0	2.7	4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 VI <sub>D</sub> = 25 A		0.009	0.01	Ω
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 25 \text{ V}, I_D = 25 \text{ A}$ (N	lote 4)	20		S
C <sub>iss</sub>	ic Characteristics Input Capacitance Output Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		1380 490	1600 590	pF pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 1.0 WH12		85	90	pF
	ing Characteristics			10		
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 30 \text{ V}, I_{D} = 25 \text{ A},$		18	45	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25 \Omega$		135	270	n
t <sub>d(off)</sub>	Turn-Off Delay Time Turn-Off Fall Time	(Not	e 4, 5)	60	130	ns
		· ·		65	140 41	ns
Q <sub>g</sub>	Total Gate Charge Gate-Source Charge	$V_{DS} = 48 \text{ V}, I_{D} = 50 \text{ A},$		31 8	41	nC nC
Q <sub>gs</sub> Q <sub>gd</sub>	Gate-Drain Charge	V <sub>GS</sub> = 10 V	e 4, 5)	13		nC
∽ya	Cate Brain Onlarge	(100	, 0/	10		110
Drain-S	Source Diode Characteristics ar	nd Maximum Ratings				
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current				50	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				170	А
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 50 A			1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 50 A,		57		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$dI_F / dt = 100 A/\mu s$ (N				

Notes: 1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 230µH, I<sub>AS</sub> = 50A, V<sub>DD</sub> = 25V, R<sub>G</sub> = 25  $\Omega$ , Starting T<sub>J</sub> = 25°C 3. I<sub>SD</sub>  $\leq$  50A, didt  $\leq$  300Aµs, V<sub>DD</sub>  $\leq$  BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C 4. Pulse Test : Pulse width  $\leq$  300µs, Duty cycle  $\leq$  2% 5. Essentially independent of operating temperature

# **Typical Characteristics**

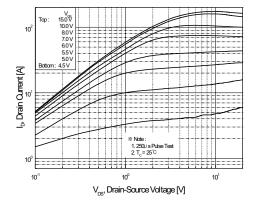


Figure 1. On-Region Characteristics

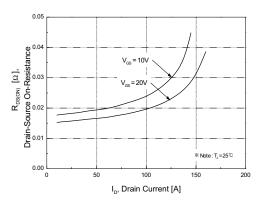


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

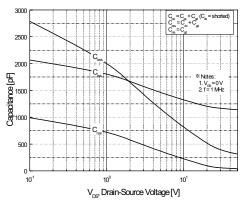


Figure 5. Capacitance Characteristics

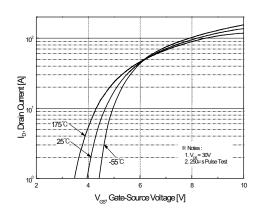


Figure 2. Transfer Characteristics

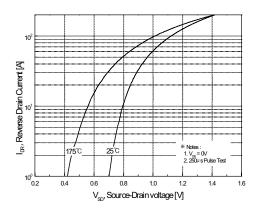


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

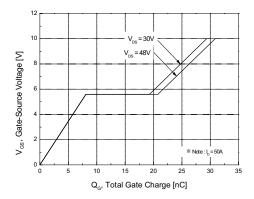
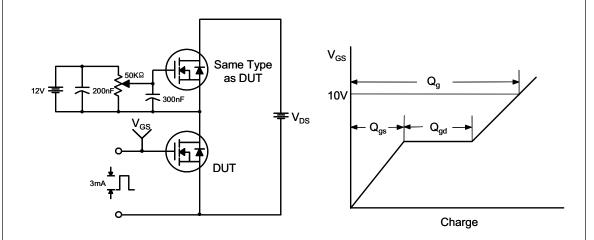


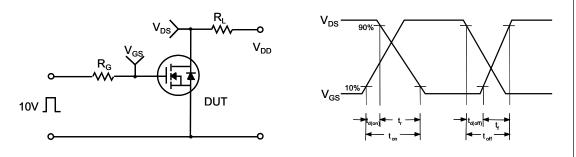
Figure 6. Gate Charge Characteristics

# Typical Characteristics (Continued) 2.5 0.8 L -100 150 100 T,, Junction Temperature [°C] $\mathsf{T}_{_{\!J}}\!, \mathsf{Junction}\,\mathsf{Temperature}\,[^{\circ}\!\mathsf{C}]$ Figure 7. Breakdown Voltage Variation Figure 8. On-Resistance Variation vs. Temperature vs. Temperature I<sub>D</sub>, Drain Current [A] I<sub>D</sub>, Drain Current [A] $\mathsf{T}_{\scriptscriptstyle{\mathbb{C}}}$ , Case Temperature [ ${}^{\circ}\!\!{}^{\circ}\!\!{}^{\circ}$ ] $V_{DS}$ , Drain-Source Voltage [V] Figure 9. Maximum Safe Operating Area Figure 10. Maximum Drain Current vs. Case Temperature $Z_{_{\theta}}$ $_{J_{c}}(t),$ Thermal Response $t_1$ , Square Wave Pulse Duration [sec] Figure 11. Transient Thermal Response Curve

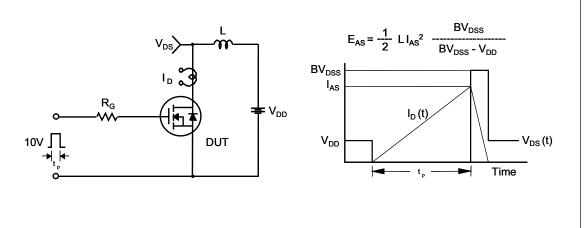
## Gate Charge Test Circuit & Waveform



### **Resistive Switching Test Circuit & Waveforms**



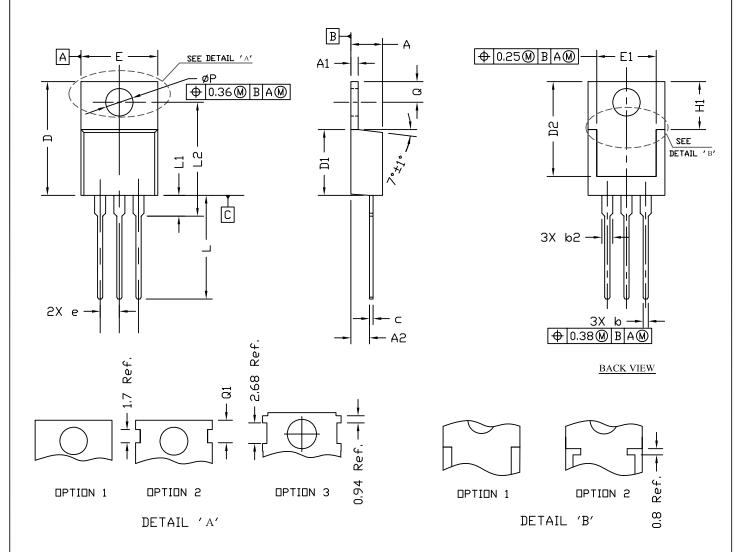
# **Unclamped Inductive Switching Test Circuit & Waveforms**



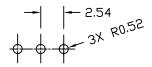
# Peak Diode Recovery dv/dt Test Circuit & Waveforms DUT I<sub>SD</sub> ~ Driver Same Type as DUT $V_{DD}$ • dv/dt controlled by R<sub>G</sub> • I<sub>SD</sub> controlled by pulse period Gate Pulse Width $\textbf{V}_{\text{GS}}$ Gate Pulse Period 10V (Driver) $\mathbf{I}_{\mathrm{FM}}$ , Body Diode Forward Current $\mathbf{I}_{\text{SD}}$ di/dt (DUT) $I_{\text{RM}}$ Body Diode Reverse Current $\textbf{V}_{\text{DS}}$ (DUT) Body Diode Recovery dv/dt $V^{DD}$ **Body Diode** Forward Voltage Drop

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## TO220 PACKAGE OUTLINE



#### RECOMMENDATION OF HOLE PATTERN



UNIT: mm

#### NOTE

- PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH SHOULD BE LESS THAN 6 MIL.
- 2. TOLERANCE 0.100 MILLIMETERS UNLESS OTHERWISE SPECIFIED.
- 3. CONTROLLING DIMENSION IS MILLIMETER.
- CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

SAMBOLS	DIMENSIONS IN MILLIMETERS		DIMENSIONS IN INCHES			
21MBUL2	MIN	NDM	MAX	MIN	NDM	MAX
Α	4.30	4.45	4.72	0.169	0.175	0.186
A1	1.15	1.27	1.40	0.045	0.050	0.055
A2	2.20	2.67	2.90	0.087	0.105	0.114
b	0.69	0.81	0.95	0.027	0.032	0.037
b2	1.17	1.37	1.45	0.046	0.050	0.068
C	0.36	0.38	0.60	0.014	0.015	0.024
D	14.50	15.44	15.80	0.571	0.608	0.622
D1	8.59	9.14	9.65	0.338	0.360	0.380
D2	11.43	11.73	12.48	0.450	0.462	0.491
е	2.54 BSC			0.100 BSC.		
E	9.66	10.03	10.54	0.380	0.395	0.415
E1	6.22			0.245		
H1	6.10	6.30	6.50	0.240	0.248	0.256
L	12.27	12.82	14.27	0.483	0.505	0.562
L1	2.47		3.90	0.097		0.154
L2			16.70			0.657
Q	2.59	2.74	2.89	0.102	0.108	0.114
ØΡ	3.50	3.84	3.89	0.138	0.151	0.153
Q1	2.70		2.90	0.106		0.114

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