

MT3238

N-Channel Power MOSFET

80V, 120A, 5.5mΩ

Features

- Max $R_{DS(on)}$ = 5.5mΩ at V_{GS} = 10V, I_D = 45A
- Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extr emely Low $R_{DS(on)}$
- High Power and Current Handling Capability
- RoHS Compliant

General Description

This N-Channel MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Applications

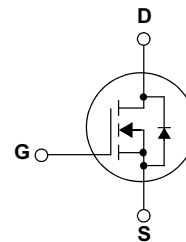
- DC-DC primary bridge
- DC-DC Synchronous rectification
- Hot swap



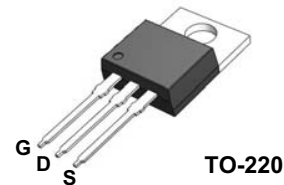
MT Semiconductor®

<http://www.mtsemi.com>

Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



MOSFET Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter		Rating	Unit
Common Ratings (T _C =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		80	V
V _{GSS}	Gate-Source Voltage		±25	
T _J	Maximum Junction Temperature		175	°C
T _{STG}	Storage Temperature Range		55 to 175	°C
I _S	Diode Continuous Forward Current	T _C =25°C	120	A
Mounted on Large Heat Sink				
I _{DM}		T _C =25°C	480**	A
I _D	Continuous Drain Current	T _C =25°C	120	A
		T _C =100°C	85	
P _D	Maximum Power Dissipation	T _C =25°C	226	W
		T _C =100°C	113	
R _{θJC}	Thermal Resistance-Junction to Case		0.66	°C/W
R _{θJA}	Thermal Resistance-Junction to Ambient		62.5	
Avalanche Ratings				
E _{AS}	Avalanche Energy, Single Pulsed	L=0.5mH	630***	mJ

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
MT3238	MT3238	TO-220	-	-	50

Electrical Characteristics ($T_c = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	80		-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 80 V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	10	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2.0	3.0	4.0	V
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)}	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =60A	-	5.5	7.0	mΩ
Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _{SD} =60A, V _{GS} =0V	-	0.8	1	V
t _{rr}	Reverse Recovery Time	I _{SD} =60A, dI _{SD} /dt=100A/μs	-	46	-	ns
Q _{rr}	Reverse Recovery Charge		-	98	-	nC

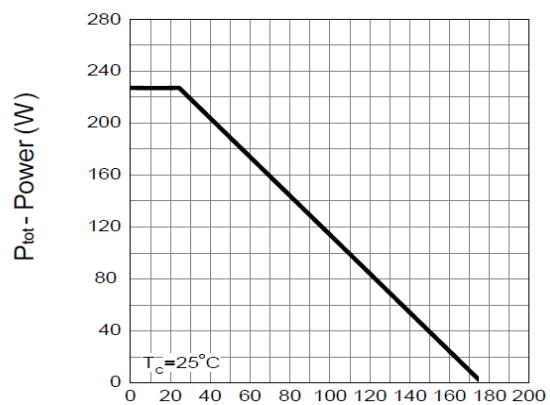
Electrical Characteristics (Cont.) ($T_c = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit
			Min.	Typ.	Max.	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	0.6	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Frequency=1.0MHz	-	3680	-	pF
C _{oss}	Output Capacitance		-	552	-	
C _{rss}	Reverse Transfer Capacitance		-	192	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =40V, R _G = 6 Ω, I _{DS} =60A, V _{GS} =10V,	-	23	-	ns
T _r	Turn-on Rise Time		-	35	-	
t _{d(OFF)}	Turn-off Delay Time		-	77	-	
T _f	Turn-off Fall Time		-	44	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =64V, V _{GS} =10V, I _{DS} =60A	-	87.9	-	nC
Q _{gs}	Gate-Source Charge		-	15	-	
Q _{gd}	Gate-Drain Charge		-	30	-	

Note * : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

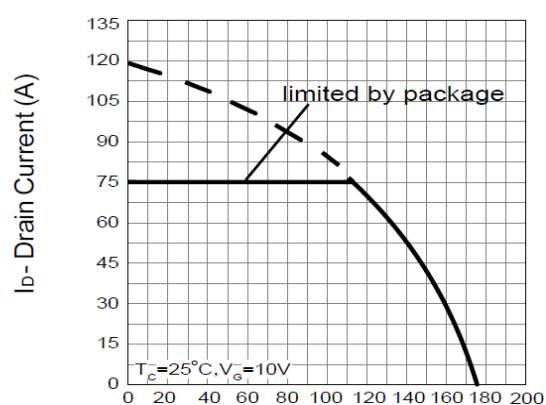
Typical Operating Characteristics

Power Dissipation



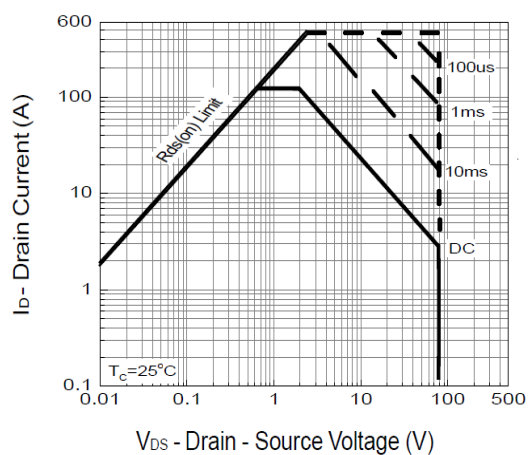
T_c - Case Temperature ($^{\circ}C$)

Drain Current



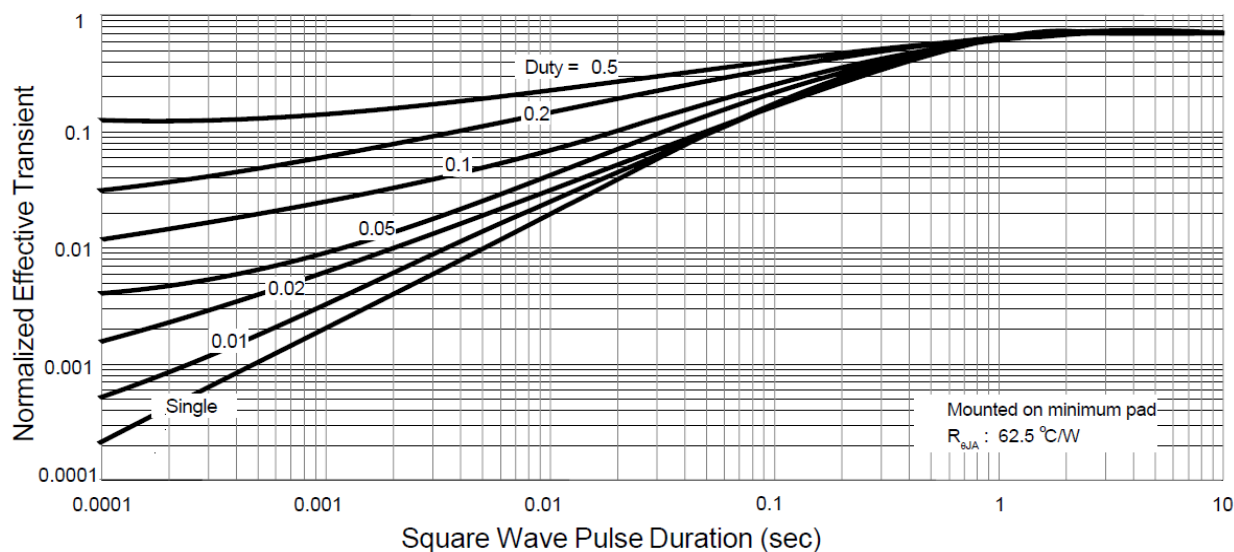
T_c - Case Temperature ($^{\circ}C$)

Safe Operation Area



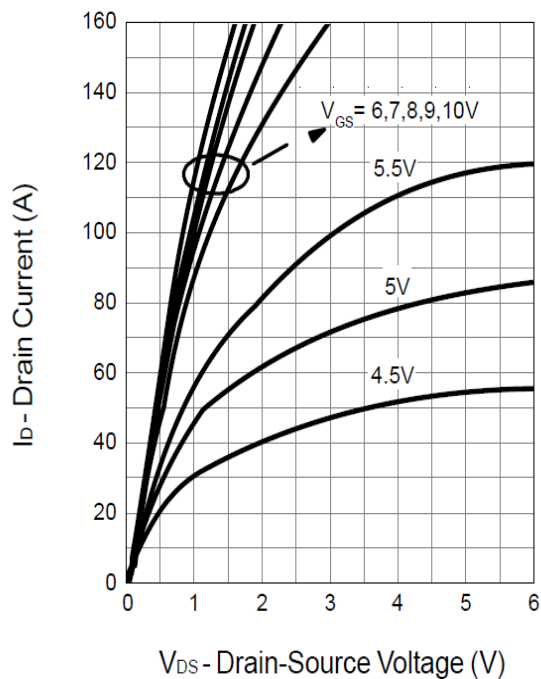
V_{DS} - Drain - Source Voltage (V)

Thermal Transient Impedance

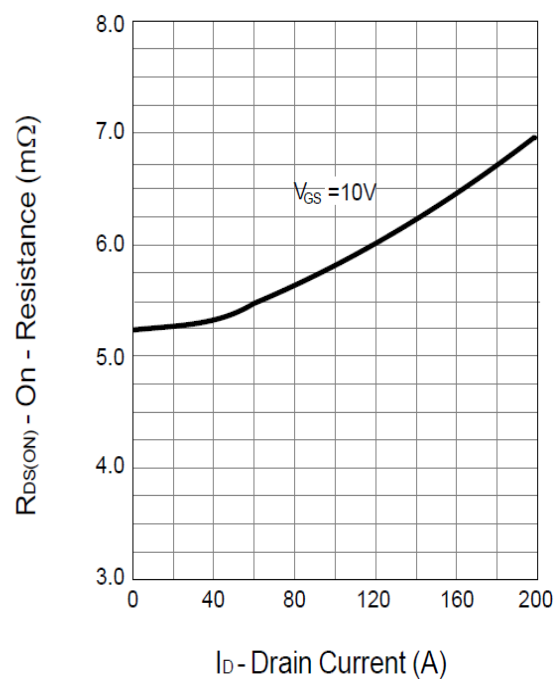


Typical Operating Characteristics (Cont.)

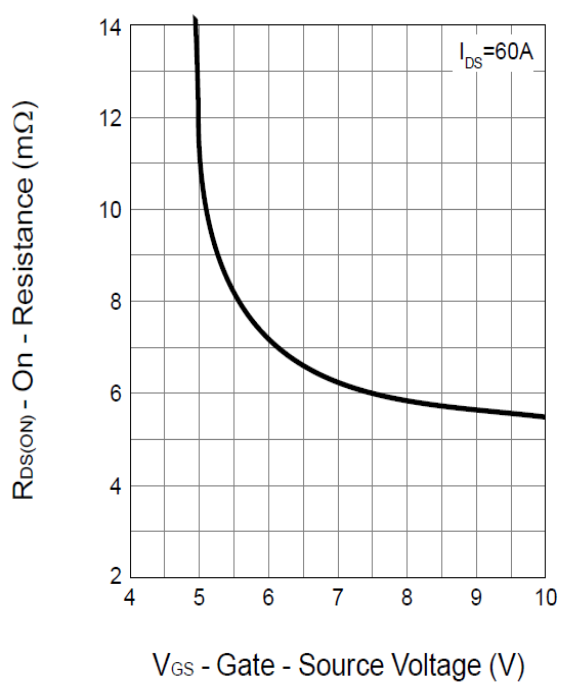
Output Characteristics



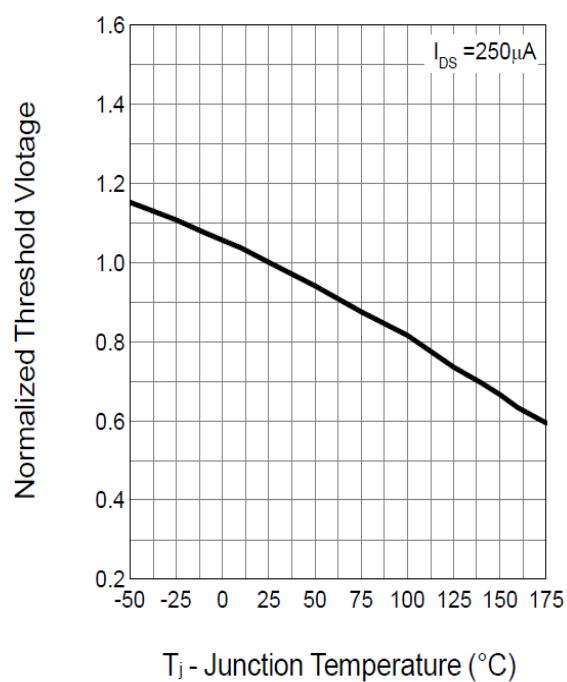
Drain-Source On Resistance



Drain-Source On Resistance

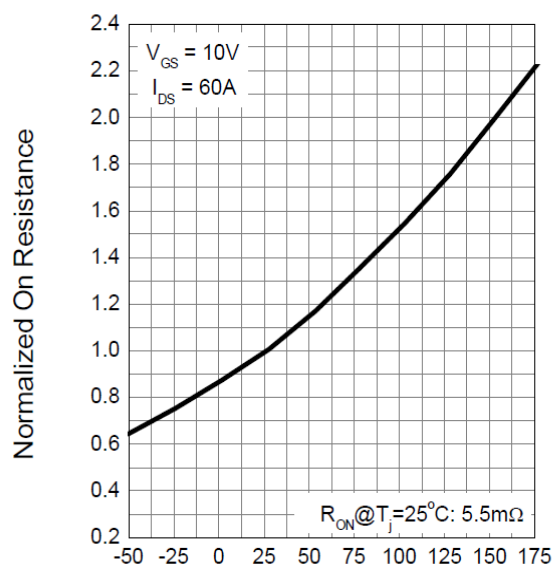


Gate Threshold Voltage



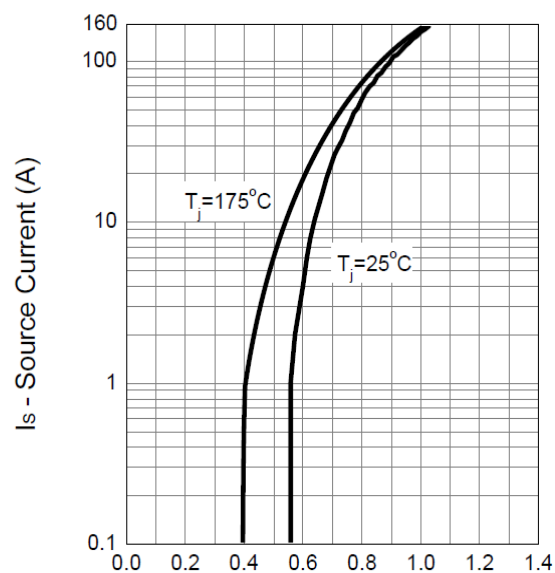
Typical Operating Characteristics (Cont.)

Drain-Source On Resistance



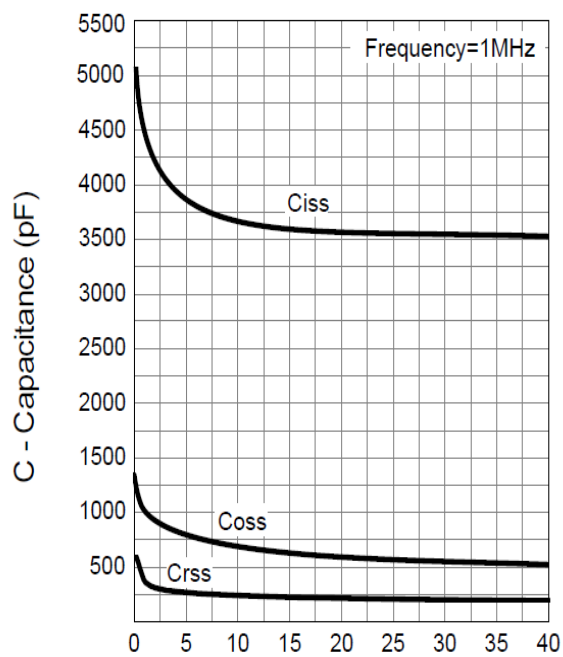
T_j - Junction Temperature ($^{\circ}\text{C}$)

Source-Drain Diode Forward



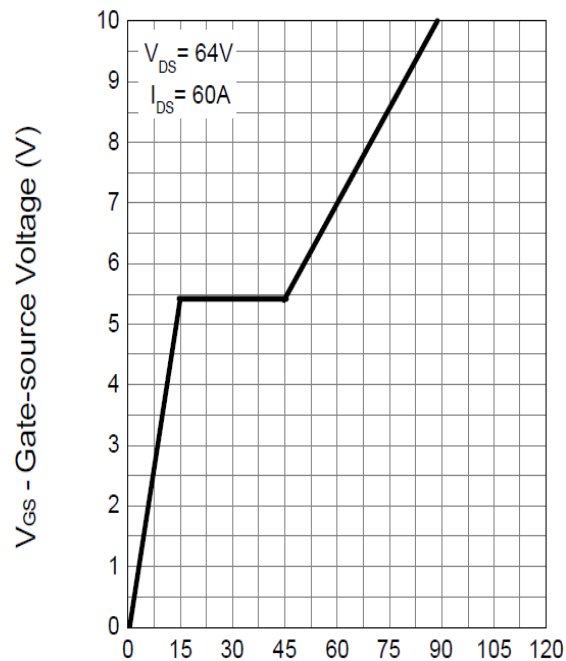
V_{SD} - Source-Drain Voltage (V)

Capacitance



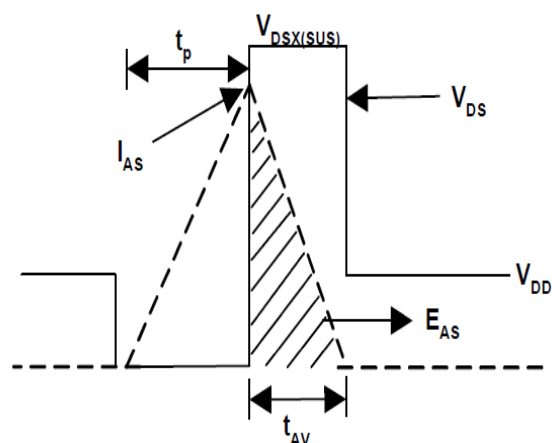
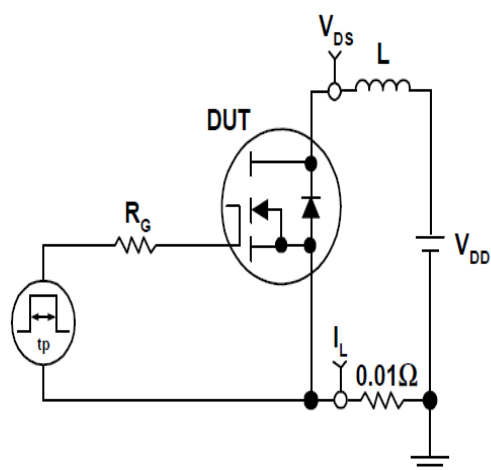
V_{DS} - Drain - Source Voltage (V)

Gate Charge

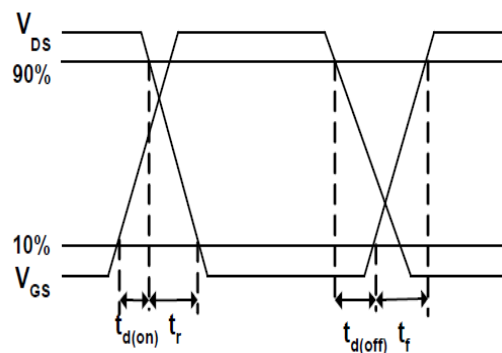
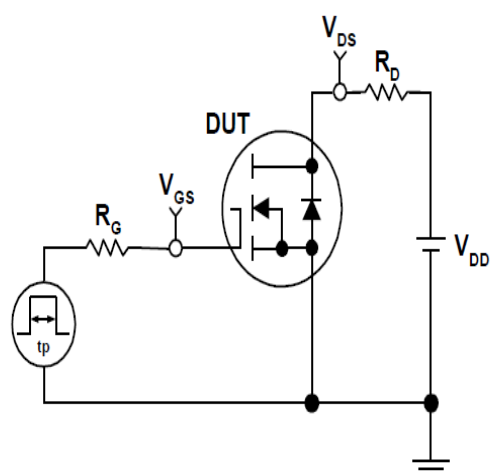


Q_G - Gate Charge (nC)

Avalanche Test Circuit and Waveforms



Avalanche Test Circuit and Waveforms



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