## N-Channel 60V/50A Power MOSFET

#### Features

- Max  $R_{DS}(on)=10m_{\Omega}$  at  $V_{GS}=10V, I_D=25A$
- Low gate charge(typical 43 nC)
- · Low crss(typical 85pF)
- · 100% avalanche tested
- · Improved dv/dt capability

## **General Description**

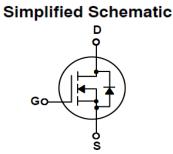
These N-Channel enhancement mode power field effect transistors are produced using Mos-tech's proprietary, planar stripe, DMOS technology.

## Applications

- DC-DC Buck Converters
- Notebook battery power management
- · Load Switch im Notebook

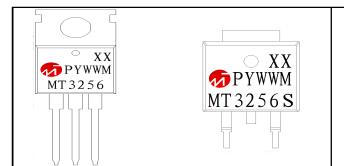


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MARKING DIAGRAM & PIN ASSIGNMENT





Package	Code
MT3256:	TO-220FB-3L
MT3256S	:T0-252-2L

Date Code

PYWWM

Lot NO

XX

**MOSFET Maximum Ratings** T<sub>C</sub> = 25°C unless otherwise noted

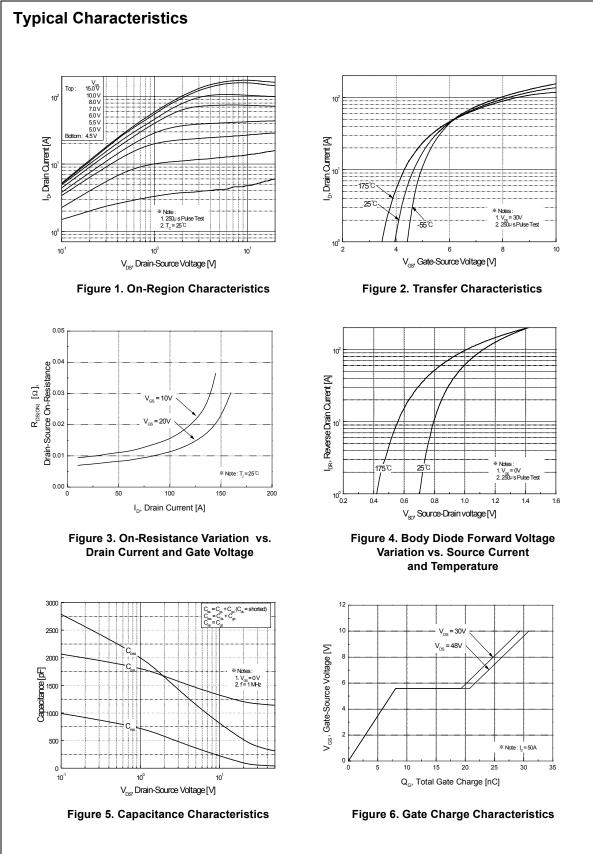
Symbol			Ratings	Units				
V <sub>DSS</sub>	Drain to Source V	oltage	60	V				
V <sub>GSS</sub>	Gate to Source Vo	oltage	±20	V				
	Drain Curren	- Continuc	50					
		- Continuo	us( Package Limited)	T <sub>C</sub> = 25°C	28	Α		
ID		- Continuo	us	T <sub>C</sub> = 25 <sup>o</sup> C(Note 1a)	45			
		- Pulsed	180	Α				
E <sub>AS</sub>	Single Pulsed Avalanche Energy			(Note 3)	10	mJ		
P <sub>D</sub>	Power Dissipation		- T <sub>C</sub> = 25°C	(Note 1a)	TO-220=100/TO-252=50	W		
		- T <sub>A</sub> = 25 <sup>o</sup> C	(Note 1b)	0.9	W/ºC			
T <sub>J</sub> , T <sub>STG</sub>	Operating and Sto	orage Temperat		-55 to +150	°C			

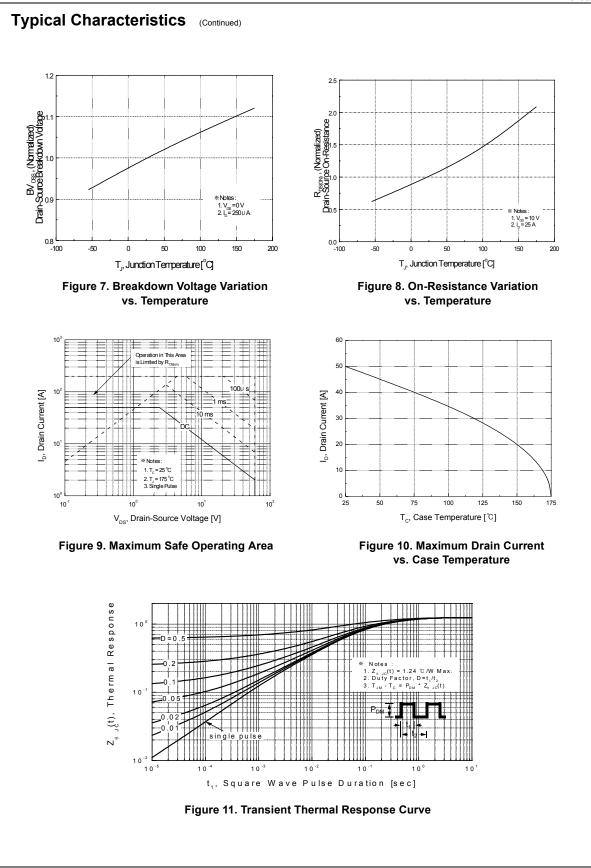
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Symbol	Parameter					R		Units			
θJC	Thermal Resistance, Junction to Case   Thermal Resistance, Junction to Ambient				(No	te 1)		1.64		0000	
θJA				(Note 1a)				65.5		°C/W	
ackage	e Marki	ng and Order	ing Infor	matio	n						
Device N	larking	Device	Packa				e Width	e Width -		<b>Quantity</b> 50/2500	
MT32	56/S	MT3256/S	TO-220/T				-				
Electri	cal Cha	aracteristics	T <sub>C</sub> = 25°0	C unless oth	erwise noted						
Symbol		Parameter			Test Condition	ns	Min	Тур	Max	Units	
Off Cha	aracteris	stics									
BV <sub>DSS</sub>	Drain-So	ource Breakdown Vo	oltage	$V_{GS} = 0$	Ο V, I <sub>D</sub> = 250 μA		60			V	
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdo Coefficie	own Voltage Temper ent	ature	I <sub>D</sub> = 25	0 μA, Reference	ed to 25°C		0.06		V/°C	
IDSS	Zero Gate Voltage Drain Current			V <sub>DS</sub> = 6	60 V, V <sub>GS</sub> = 0 V				1	μA	
			$V_{DS} = 4$	48 V, T <sub>C</sub> = 150°0	2			10	μA		
I <sub>GSSF</sub>	Gate-Bo	dy Leakage Curren	t, Forward	V <sub>GS</sub> = 2	25 V, V <sub>DS</sub> = 0 V				100	nA	
I <sub>GSSR</sub>	Gate-Bo	dy Leakage Curren	t, Reverse	$V_{GS}$ = -25 V, $V_{DS}$ = 0 V					-100	nA	
On Cha	racteris	stics									
V <sub>GS(th)</sub>	Gate Th	reshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250 \mu A$			2.0	2.7	4.0	V	
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance		V <sub>GS</sub> = 10 VI <sub>D</sub> = 25 A				10		mΩ		
9 <sub>FS</sub>	Forward Transconductance			V <sub>DS</sub> = 2	25 V, I <sub>D</sub> = 25 A	(Note 4)		20		S	
Dynam	ic Char	acteristics									
C <sub>iss</sub>		acteristics		\/	$25 \sqrt{1} = 0 \sqrt{1}$			1380	1600	pF	
C <sub>oss</sub>	Output Capacitance Reverse Transfer Capacitance			V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz				490	590	pF	
C <sub>rss</sub>								85	90	pF	
<b>Switch</b> i t <sub>d(on)</sub>	-	racteristics		N/ -	20.1/1 - 25.4			18	45	ns	
t <sub>r</sub>	Turn-On	Rise Time		$V_{DD} = $ $R_G = 2$	30 V, I <sub>D</sub> = 25 A, 5 Ω			135	270	ns	
t <sub>d(off)</sub>	Turn-Off	Delay Time						60	130	ns	
t <sub>f</sub>	Turn-Off	Fall Time				(Note 4, 5)		65	140	ns	
Qg	Total Ga	ite Charge		V <sub>DS</sub> = 4	48 V, I <sub>D</sub> = 50 A,			31	41	nC	
Q <sub>gs</sub>	Gate-So	ource Charge		$V_{GS} = 1$	-			8		nC	
Q <sub>gd</sub>	Gate-Dra	ain Charge				(Note 4, 5)		13		nC	
Drain-S	ource [	Diode Characte	ristics a	nd Max	imum Ratin	as					
I <sub>S</sub>		m Continuous Drain				•			50	Α	
I <sub>SM</sub>	Maximu	m Pulsed Drain-Sou	irce Diode F	orward C	Current				170	Α	
V <sub>SD</sub>	Drain-So	ource Diode Forwar	d Voltage	$V_{GS} = 0$	0 V, I <sub>S</sub> = 50 A			-	1.25	V	
t <sub>rr</sub>	Reverse	Recovery Time		$V_{GS} = 0$	0 V, I <sub>S</sub> = 50 A,			57		ns	
				1	= 100 A/µs	(Note 4)		79		nC	

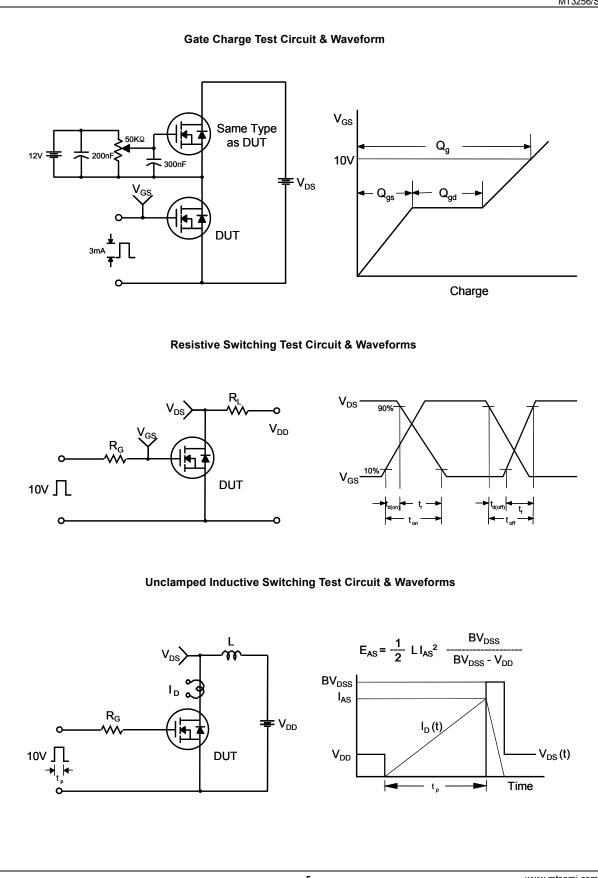
4. Pulse Test : Pulse width  $\leq$  300µs, Duty cycle  $\leq$  2% 5. Essentially independent of operating temperature

2

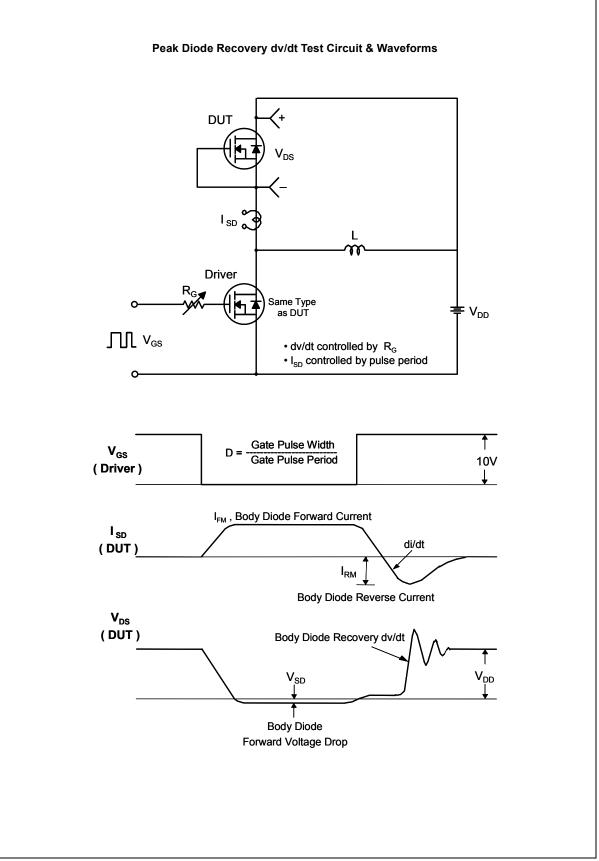






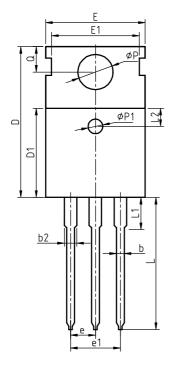


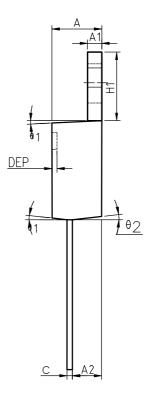
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# Package Information

TO-220FB-3L

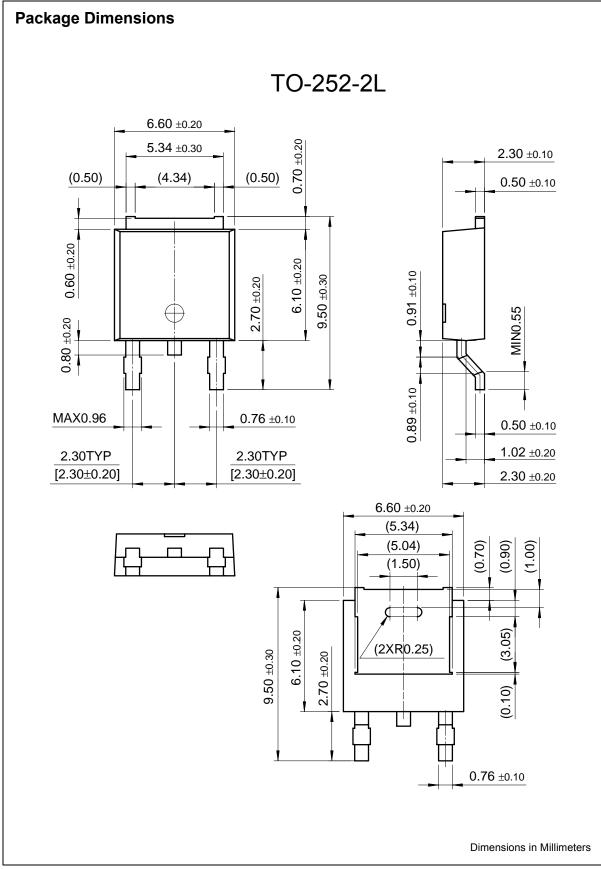




COMMON DIMENSIONS

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SYMBOL	MI N	NOM	MAX	MI N	NOM	MAX
Α	4.40	4.57	4.70	0.173	0.180	0.185
A1	1.27	1.30	1.33	0.050	0.051	0.052
A2	2.35	2.40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1.27	1.36	0.046	0.050	0.054
С	0.48	0.50	0.56	0.019	0.020	0.022
D	15.40	15.60	15.80	0.606	0.614	0.622
D1	9.00	9.10	9.20	0.354	0.358	0.362
DEP	0.05	0.10	0.20	0.002	0.004	0.008
E	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8.70	-	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
е		2.54	BSC		0.100	BSC
e1		5.08	BSC		0.200	BSC
H1	6.40	6.50	6.60	0.252	0.256	0.260
L	12.75	13.50	13.65	0.502	0.531	0.537
L1	-	3.10	3.30	-	0.122	0.130
L2		2.50	REF		0.098	REF
Р	3.50	3.60	3.63	0.138	0.142	0.143
P1	3.50	3.60	3.63	0.138	0.142	0.143
Q	2.73	2.80	2.87	0.107	0.110	0.113
θ 1	5°	<b>7</b> °	9°	5°	<b>7</b> °	9°
θ 2	<b>1</b> °	3°	5°	<b>1</b> °	3°	5°
θ3	<b>1</b> °	3°	5°	<b>1</b> °	3°	5°



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