MT3258/B

N-Channel 80V/170A Power MOSFET

Features

- Max $R_{DS}(on)=5m\Omega$ at $V_{GS}=10V, I_D=85A$
- · Fast Switching Speed
- · Low Gate Charge
- 100% avalanche tested

General Description

This N-Channel MOSFET is produced using MOS-TECH Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

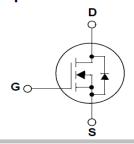
Applications

- · DC-DC primary bridge
- DC-DC Synchronous rectification
- · Power Managemement for Inverter Systems

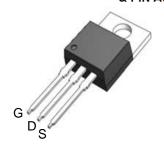


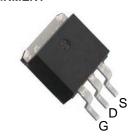
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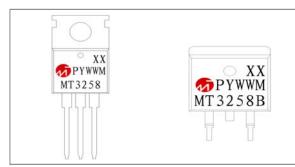
Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT







Package Code

MT3258: T0-220FB-3L MT3258B: T0-263-2L

Date Code

Lot No

Absolute Maximum Ratings

Abbonate Haximaiii Ratings							
Symbol	Parameter	Rating	Unit				
Common	Ratings (T _C =25°C Unless Otherwise Noted)			•			
V_{DSS}	Drain-Source Voltage			80	V		
V_{GSS}	Gate-Source Voltage	±25	ľ				
TJ	Maximum Junction Temperature	175	°C				
T _{STG}	Storage Temperature Range			-55 to 175	°C		
Is	Diode Continuous Forward Current T _C =25°C			170	Α		

Mounted on Large Heat Sink

I _{DM}	Pulsed Drain Current * T _C =25°C		660**	Α			
I _D	Continuous Drain Current	T _C =25°C	170	А			
	Continuous Diain Current	T _C =100°C	114				
ь	Maximum Power Dissipation	T _C =25°C	288	W			
P _D	Iviaximum Fower Dissipation	T _C =100°C	144	VV			
$R_{ heta JC}$	Thermal Resistance-Junction to Case	0.52	°C/W				
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	62.5	C/VV				
Avalanche Ratings							
E _{AS}	Avalanche Energy, Single Pulsed L=0.5mH		1168***	mJ			

Electrical Characteristics (T_c = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit	
Syllibol	rarameter rest conditions		Min.	Тур.	Max.	Oill	
Static Cha	racteristics			-			
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	80	-	-	V	
i	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μА	
I _{DSS}	Zero Gate Voltage Drain Current	T _J =85°C	-	-	10		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{DS}=250\mu A$	2.0	3.0	4.0	V	
I _{GSS}	Gate Leakage Current	V_{GS} =±25V, V_{DS} =0V	-	-	±100	nA	
R _{DS(ON)} *	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =85A	ı	3.8	5.0	mΩ	
Diode Cha	aracteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =85 A, V _{GS} =0V	-	0.8	1.2	V	
t _{rr}	Reverse Recovery Time	-95 A dl /dt-100 A /v.o	-	30	-	ns	
Q_{rr}	Reverse Recovery Charge	l _{SD} =85A, dl _{SD} /dt=100A/μs	-	52	-	nC	

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Note * Repetitive rating ; pulse width limited by junction temperature

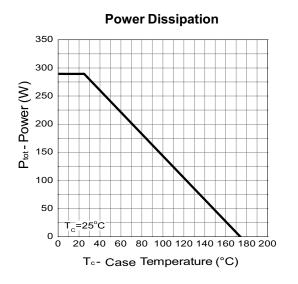
^{**} Drain current is limited by junction temperature

Electrical Characteristics (Cont.) $(T_c = 25^{\circ}C \text{ Unless Otherwise Noted})$

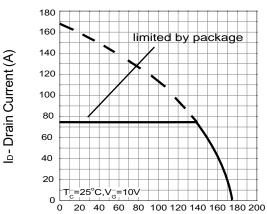
Symbol	Parameter	Test Conditions				Unit		
Syllibol	Farameter	rest Conditions	Min.	Тур.	Max.	Oille		
Dynamic (Dynamic Characteristics							
R_G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	1.8	-	Ω		
C _{iss}	Input Capacitance	V _{GS} =0V,	_	6109	-			
C _{oss}	Output Capacitance	V _{DS} =25V,	-	995	-	pF		
C _{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	530	-			
t _{d(ON)}	Turn-on Delay Time		-	28	-			
T _r	Turn-on Rise Time	V_{DD} =40V, R_{G} =6 Ω , I_{DS} =85A, V_{GS} =10V,	-	18	-	ns		
t _{d(OFF)}	Turn-off Delay Time		-	42	-	113		
T_f	Turn-off Fall Time		-	54	-			
Gate Charge Characteristics								
Q_g	Total Gate Charge			152	-			
Q_{gs}	Gate-Source Charge		_	25	-	nC		
Q_{gd}	Gate-Drain Charge	103 007.1	-	53	-			

Note * : Pulse test ; pulse width ≤300µs, duty cycle≤2%.

Typical Operating Characteristics

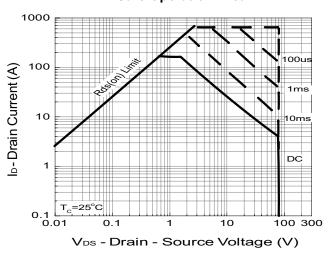


Drain Current

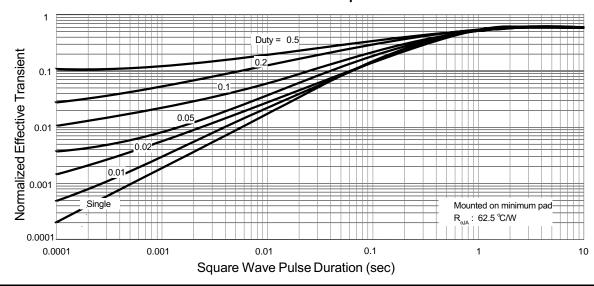


T_c-Case Temperature (°C)

Safe Operation Area

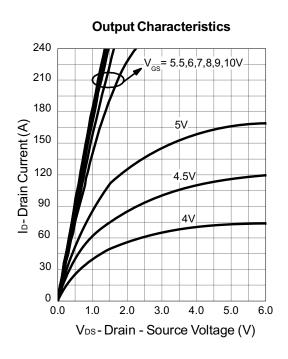


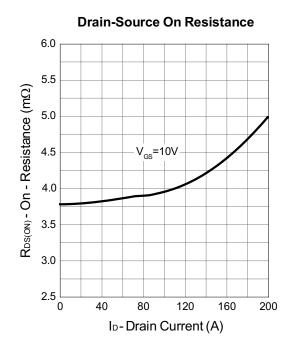
Thermal Transient Impedance

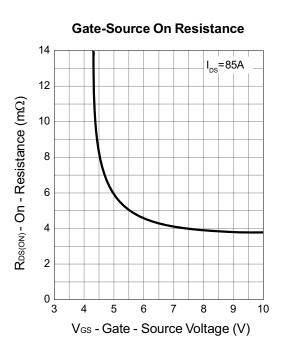


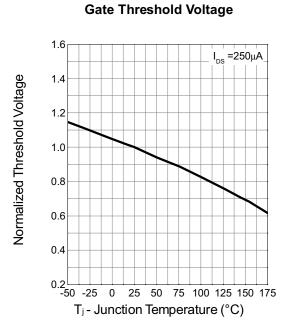
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Typical Operating Characteristics (Cont.)



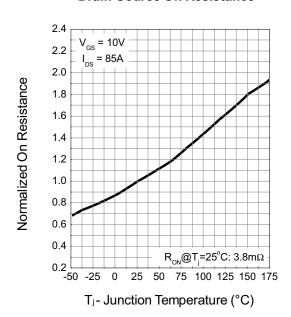




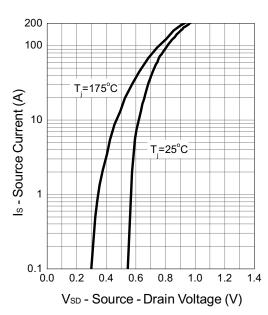


Typical Operating Characteristics (Cont.)

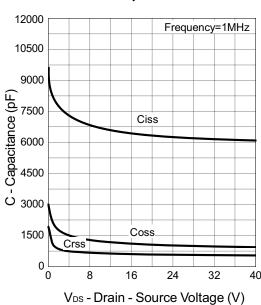
Drain-Source On Resistance



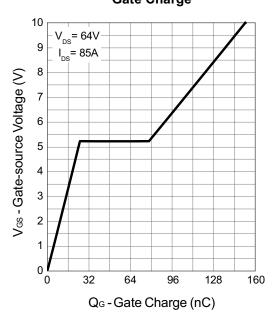
Source-Drain Diode Forward



Capacitance



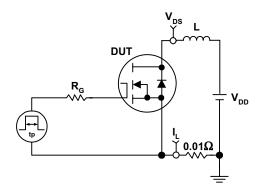
Gate Charge

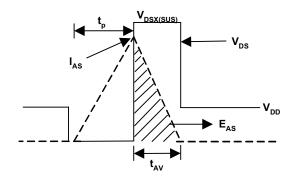


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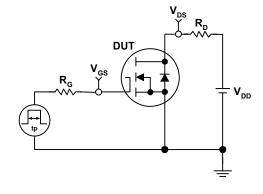
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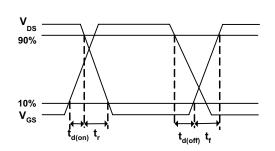
Avalanche Test Circuit and Waveforms



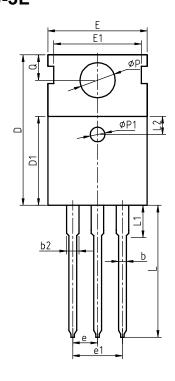


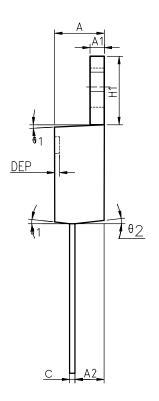
Avalanche Test Circuit and Waveforms





Package Information TO-220FB-3L



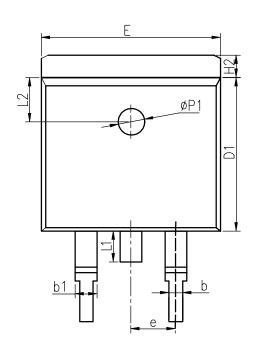


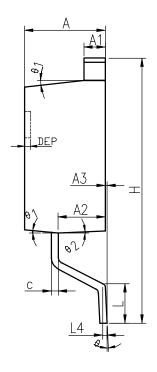
COMMON DIMENSIONS



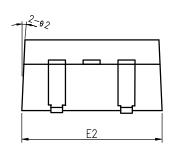
SYMBOL	MIN	NOM	MAX	MIN	NOM	MAX
A	4.40	4. 57	4. 70	0.173	0. 180	0. 185
A1	1.27	1. 30	1. 33	0.050	0.051	0.052
A2	2.35	2. 40	2.50	0.093	0.094	0.098
b	0.77	0.80	0.90	0.030	0.031	0.035
b2	1.17	1. 27	1.36	0.046	0.050	0.054
c	0.48	0.50	0.56	0.019	0.020	0.022
D	15. 40	15. 60	15.80	0.606	0.614	0.622
D1	9.00	9. 10	9. 20	0.354	0.358	0.362
DEP	0.05	0.10	0. 20	0.002	0.004	0.008
Е	9.80	10.00	10.20	0.386	0.394	0.402
E1	-	8. 70	ı	-	0.343	-
E2	9.80	10.00	10.20	0.386	0.394	0.402
е		2. 54	BSC		0.100	BSC
e1		5. 08	BSC		0.200	BSC
H1	6.40	6. 50	6.60	0. 252	0. 256	0. 260
L	12.75	13.50	13.65	0.502	0. 531	0. 537
L1	ı	3. 10	3.30	_	0. 122	0.130
L2		2.50	REF		0.098	REF
Р	3.50	3. 60	3. 63	0. 138	0. 142	0. 143
P1	3.50	3. 60	3. 63	0. 138	0. 142	0. 143
Q	2.73	2.80	2.87	0. 107	0.110	0.113
θ 1	5°	7°	9°	5°	7°	9°
θ2	1°	3°	5°	1°	3°	5°
θ3	1°	3°	5°	1°	3°	5°

TO-263-2L





COMMON DIMENSIONS



SYMBOL	MM			INCH			
STIVIDUL	MIN	NOM	MAX	MIN	NOM	MAX	
Α	4.40	4.57	4.70	0.173	0.180	0.185	
A1	1.22	1.27	1.32	0.048	0.050	0.052	
A2	2.59	2.69	2.79	0.102	0.106	0.110	
A3	0.00	0.10	0.20	0.000	0.004	0.008	
b	0.77	0.813	0.90	0.030	0.032	0.035	
b1	1.20	1.270	1.36	0.047	0.050	0.054	
С	0.34	0.381	0.47	0.013	0.015	0.019	
D1	8.60	8.70	8.80	0.339	0.343	0.346	
E	10.00	10.16	10.26	0.394	0.400	0.404	
E2	10.00	10.10	10.20	0.394	0.398	0.402	
е		2.54	BSC		0.100 BSC		
Н	14.70	15.10	15.50	0.579	0.594	0.610	
H2	1.17	1.27	1.40	0.046	0.050	0.055	
L	2.00	2.30	2.60	0.079	0.091	0.102	
L1	1.45	1.55	1.70	0.057	0.061	0.067	
L2		2.50	REF		0.098	REF	
L4		0.25	BSC	0.010 BSC			
	0°	5°	8°	0°	5°	8°	
1	5°	7°	9°	5°	7°	9°	
2	1°	3°	5°	1°	3°	5°	
ФР1	1.40	1.50	1.60	0.055	0.059	0.063	
DEP	0.05	0.10	0.20	0.002	0.004	0.008	

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