# MT3287S

# N-Channel Power MOSFET 70V,70A,6.8m $\Omega$

#### **Features**

- R<sub>DS</sub> (on)=6.8mΩ at V<sub>SS</sub> =10V,I<sub>D</sub> =20A
- High performance trench technology for extremely lov R<sub>DS</sub> (on)
- · High power and current handing capability

#### **General Description**

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low RDS(ON) and fast switching speed.

#### **Applications**

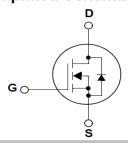
 $\mathsf{E}_{\mathsf{AS}}$ 

- DC-DC primary bridge
- · DC-DC Synchronous rectification
- · Power Managemement for Inverter Systems



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#### **Simplified Schematic**



#### MARKING DIAGRAM & PIN ASSIGNMENT



TO-252-2L

## **Absolute Maximum Ratings**

Avalanche Energy, Single Pulsed

Symbol	Parameter	Rating	Unit		
Common	Ratings (T <sub>A</sub> =25°C Unless Otherwise Noted)		•		
V <sub>DSS</sub>	Drain-Source Voltage		70	V	
V <sub>GSS</sub>	Gate-Source Voltage		±25	v	
$T_J$	Maximum Junction Temperature		175	°C	
$T_{STG}$	Storage Temperature Range		-55 to 175	°C	
Is	Diode Continuous Forward Current	T <sub>C</sub> =25°C	70	Α	
Mounted o	on Large Heat Sink	•	•	· ·	
I <sub>DM</sub>	Pulsed Drain Current *		230**	Α	
I <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> =25°C	70	A	
		T <sub>C</sub> =100°C	51	□ ~	
$P_D$	Maximum Power Dissipation	T <sub>C</sub> =25°C	50	$\square$ w	
		T <sub>C</sub> =100°C	32		
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.3	°C/W	
$R_{ heta JA}$	Thermal Resistance-Junction to Ambient		62.5		
Avalanche	e Ratings			,	

L=0.5mH

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320\*\*\*

mJ

Note : \* Repetitive rating ; pulse width limited by junction temperature 
\*\* Drain current is limited by junction temperature

\*\*\* VD=55V

# **Electrical Characteristics** $(T_A = 25^{\circ}C \text{ Unless Otherwise Noted})$

Symbol	Parameter	Test Conditions				Unit		
		rest Conditions	Min.	Тур.	Max.	Onit		
Static Characteristics								
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	70	-	-	V		
	Zero Gate Voltage Drain Current	V <sub>DS</sub> =68V, V <sub>GS</sub> =0V	-	-	1	^		
I <sub>DSS</sub>		T <sub>J</sub> =85°C	-	-	10	μΑ		
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_{DS}=250\mu A$	2	3	4	V		
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V	-	-	±100	nA		
R <sub>DS(ON)</sub> *	Drain-Source On-state Resistance	V <sub>GS</sub> =10V, I <sub>DS</sub> =40A	-	6.8	7.8	mΩ		
Diode Characteristics								
V <sub>SD</sub> *	Diode Forward Voltage	I <sub>SD</sub> =40A, V <sub>GS</sub> =0V	-	0.8	1	V		
t <sub>rr</sub>	Reverse Recovery Time	1 -40A dl /dt-100A/ua	-	33	-	ns		
Q <sub>rr</sub>	Reverse Recovery Charge	l <sub>SD</sub> =40A, dl <sub>SD</sub> /dt=100A/μs	-	61	-	nC		

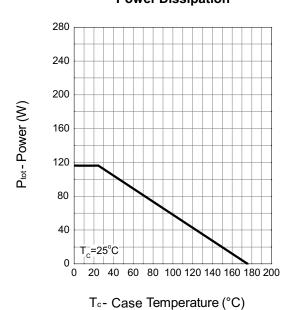
# **Electrical Characteristics (Cont.)** (T<sub>A</sub> = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit		
		rest Conditions	Min.	Тур.	Max.	Uill		
Dynamic Characteristics								
$R_{G}$	Gate Resistance	V <sub>GS</sub> =0V,V <sub>DS</sub> =0V,F=1MHz	-	1.8	-	Ω		
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V,	_	3203	-	pF		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V,	-	362	-			
C <sub>rss</sub>	Reverse Transfer Capacitance	Frequency=1.0MHz	-	277	-			
t <sub>d(ON)</sub>	Turn-on Delay Time	$V_{DD}$ =34V, R <sub>G</sub> =3 $\Omega$ , $I_{DS}$ =40A, V <sub>GS</sub> =10V,	-	15	-	ns		
T <sub>r</sub>	Turn-on Rise Time		-	13	-			
t <sub>d(OFF)</sub>	Turn-off Delay Time		-	20	-			
T <sub>f</sub>	Turn-off Fall Time		-	8	-			
Gate Charge Characteristics								
$Q_g$	Total Gate Charge	V <sub>DS</sub> =55V, V <sub>GS</sub> =10V, I <sub>DS</sub> =40A	-	84	-	nC		
$Q_gs$	Gate-Source Charge		_	14	-			
$Q_{gd}$	Gate-Drain Charge		-	30	-			

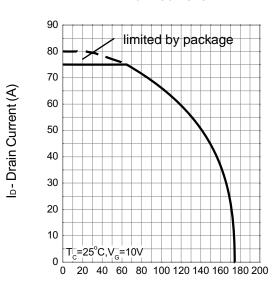
Note \* : Pulse test ; pulse width ≤300µs, duty cycle≤2%.

### **Typical Operating Characteristics**

# Power Dissipation

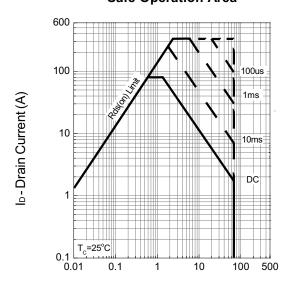


#### **Drain Current**



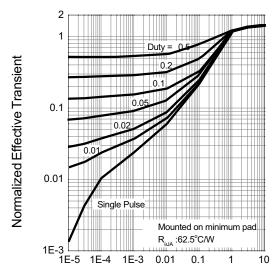
T<sub>c</sub>-Case Temperature (°C)

#### Safe Operation Area



V<sub>DS</sub> - Drain - Source Voltage (V)

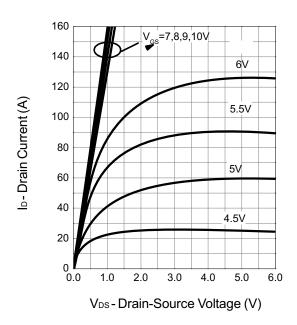
#### **Thermal Transient Impedance**



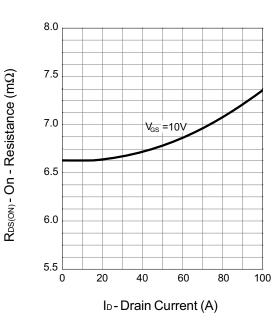
Square Wave Pulse Duration (sec)

### **Typical Operating Characteristics (Cont.)**

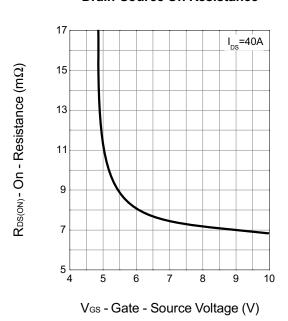




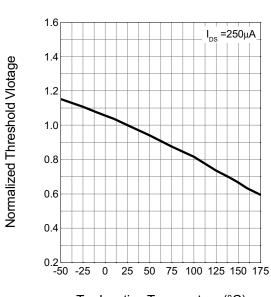
#### **Drain-Source On Resistance**



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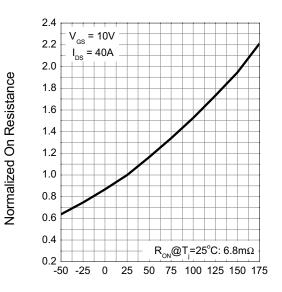
#### **Gate Threshold Voltage**



T<sub>j</sub> - Junction Temperature (°C)

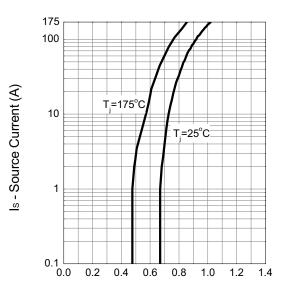
### **Typical Operating Characteristics (Cont.)**

#### **Drain-Source On Resistance**



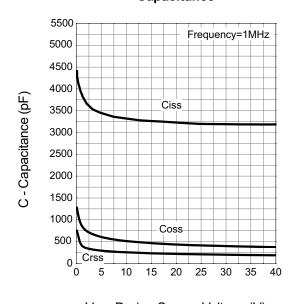
T<sub>j</sub>- Junction Temperature (°C)

#### **Source-Drain Diode Forward**



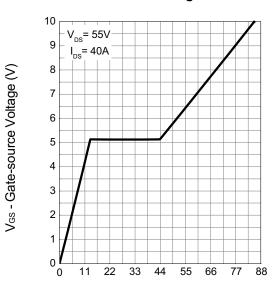
Vsp - Source-Drain Voltage (V)

#### Capacitance



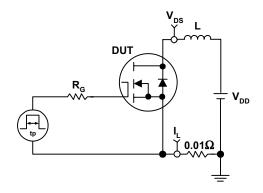
V<sub>DS</sub> - Drain - Source Voltage (V)

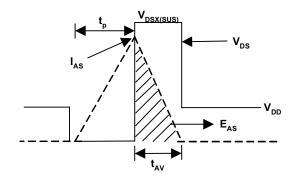
#### **Gate Charge**



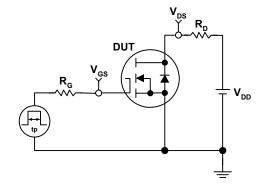
Q<sub>G</sub> - Gate Charge (nC)

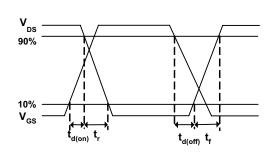
### **Avalanche Test Circuit and Waveforms**

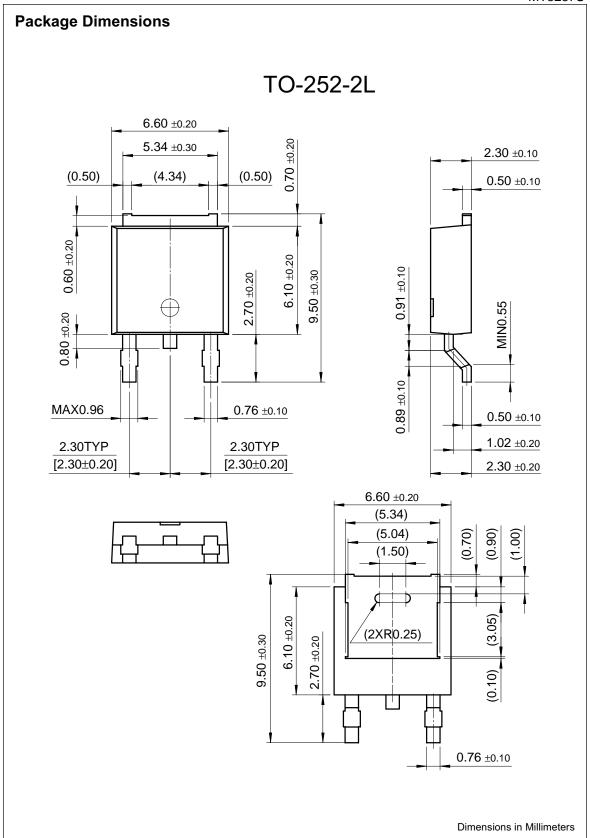




### **Avalanche Test Circuit and Waveforms**







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