# N-Channel Enhancement Mode Field Effect Transistor

#### **Product Summary**

PRODUCT S	UMMARY		
Vdss	Id	$RDS(ON)$ (m $\Omega$ ) Typ	
30V	254	55 @ VGS=10V	
	2.5A	80 @ VGS=4.5V	

### Features

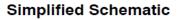
- Super high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement
- SOT-23 package

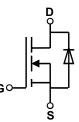
#### Applications

- Notebook Computer
- Portable Battery Pack

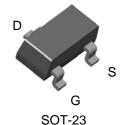


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MARKING DIAGRAM & PIN ASSIGNMENT



## ABSOLUTE MAXIMUM RATINGS (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	VGS	12	V
Drain Current-Continuous <sup>a</sup> @Tj=125°C	ID	2.5	A
- Pulse $d^b$	Ідм	10	A
Drain-source Diode Forward Current <sup>a</sup>	Is	1.25	А
Maximum Power Dissipation <sup>a</sup>	PD	1.25	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 to 150	°C

#### THERMAL CHARACTERISTICS

	Thermal Resistance, Junction-to Ambient <sup>a</sup>	Rth JA	100	°C/W
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Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS				<u> </u>	1	
Drain-Source Breakdown Voltage	BVDSS	VGS=0V,ID=250µA	30			V
Zero Gate Voltage Drain Current	Idss	VDS=16V,VGS=0V			1	μA
Gate Leakage Current	Igss	VGS=20V,VDS=0V			100	nA
ON CHARACTERITICS						
Gate Threshold Voltage	VGs(th)	VDS=VGS,ID=250µA	1.0		3.0	V
	Descus	VGS=10V,ID=2.5A		55	70	- mΩ
Drain-Source On-State Resistance	Rds(on)	Vgs=4.5V,ID=2.0A		80	105	
Forward Transconductance	gfs	Vds=4.5V,Id=2.5A		4.6		S
DAYNAMIC CHARACTERISTICS					1	I
Input Capacitance	Ciss	Vds=10V,Vds=0V f=1.0MHz		240		pF
Output Capacitance	Coss			110		pF
Reverse Transfer Capacitance	Crss	I I.OMIL		17		pF
SWITCHING CHARACTERISISTICS					1	1
Turn-On Delay Time	td(ON)			8	20	ns
Rise Time	tr	VDD=15V ID=1.0A, VGEN=10V $R_L=15 \Omega$ $R_G=6 \Omega$		12	30	ns
Turn-Off Delay Time	td(off)			17	35	ns
Fall Time	tf			8	20	ns
Total Gate Charge	Qg	Vds=15V,Id=2.5A Vgs=4.5V		4.5	10	nC
Gate-Source Charge	Qgs			0.8		nC
Gate-Drain Charge	Qgd	v Go=4.3 v		1.0		nC

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# ELECTRICAL CHARACTERICS (TA=25°C unless otherwise noted)

Typ

Min

DRAIN-SOURCE DIODE CHARACTERISTICS

Diode Forward Voltage	Vsd	Vgs=0V,Is=1.25A	0.7	1.2	V
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Condition

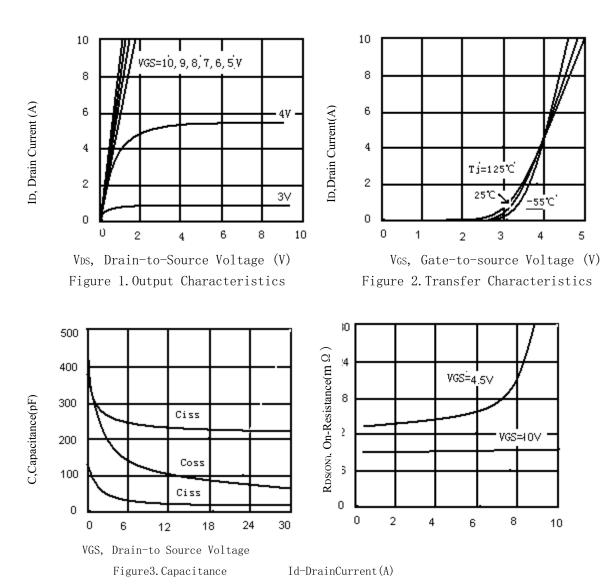
Symbol

Notes

a. Surface Mounted on FR4 Board, t  $\leq$  10sec

Parameter

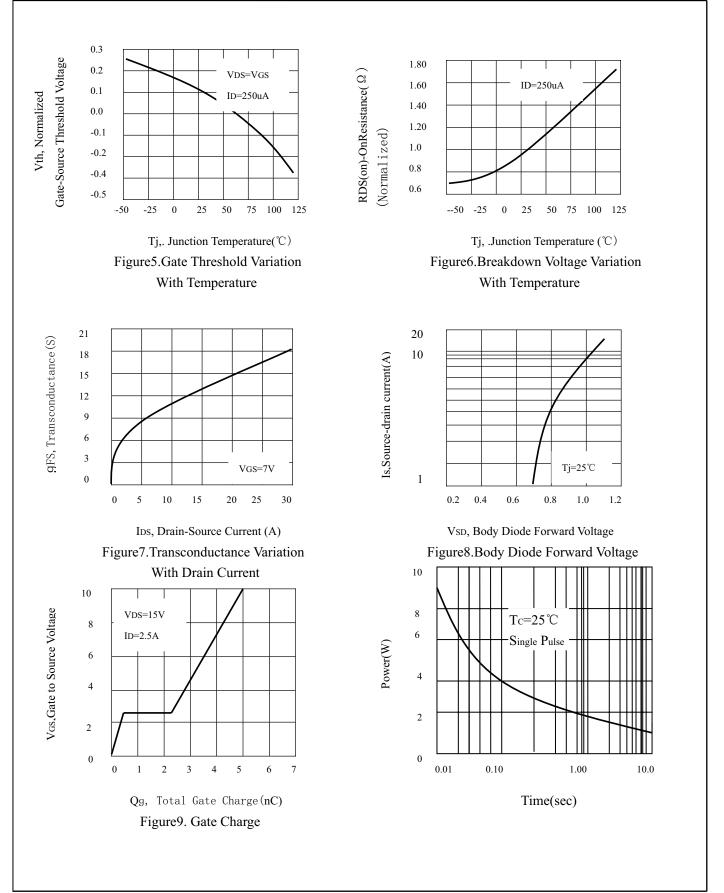
- b. Pulse Test: Pulse Width  $\leq$  300Us, Duty Cycle  $\leq$  2%
- c. Guaranteed by design, not subject to production testing.



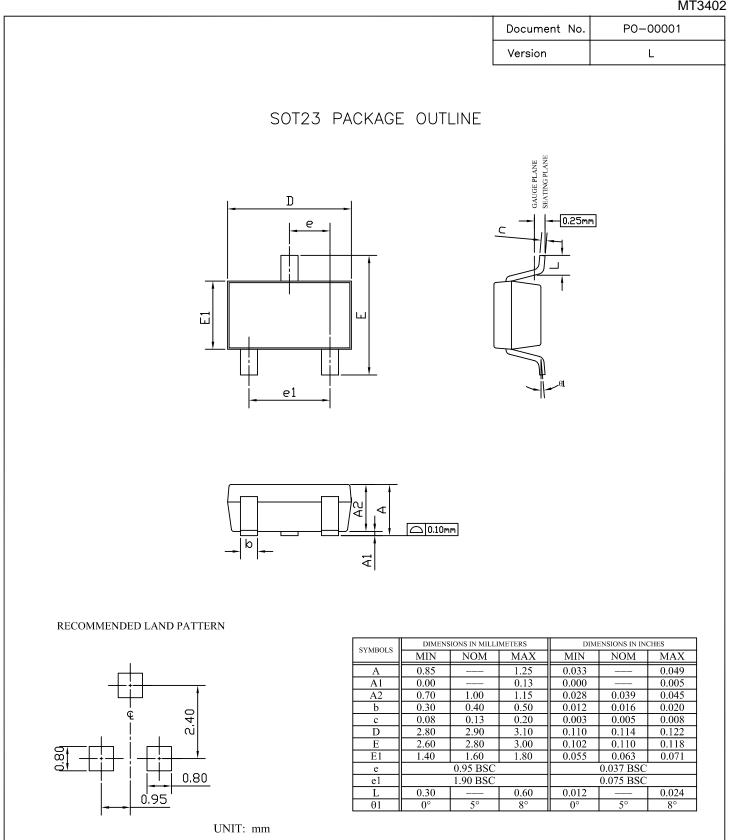
MT3402

Unit

Max



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NOTE

- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH OR GATE BURRS.
- MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 5 MILS EACH.
- 2. TOLERANCE ±0.100 mm (4 mil) UNLESS OTHERWISE SPECIFIED.
- 3. DIMENSION L IS MEASURED IN GAUGE PLANE.
- 4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS
- ARE NOT NECESSARILY EXACT.
- 5. ALL DIMENSIONS ARE IN MILLIMETERS.

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