# MT3421

# P-Channel Enhancement Mode Field Effect Transistor

### **General Description**

This P-Channel Power MOSFET is produced using MOS -TECH Semiconductor's advanced PowerTrech process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

These devices are well suited for portable electronics applications: load switching and power management, battery charging circuits and DC/DC converters.

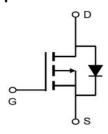
#### **Features**

- -4.3 A, -25 V.  $R_{DS(ON)} = 0.08 \Omega$  @  $V_{GS} = -4.5 \text{ V}$  $R_{DS(ON)} = 0.11 \Omega$  @  $V_{GS} = -2.5 \text{ V}$
- Low gate charge (3.6 nC typical)
- High performance trench technology for extremely low R<sub>DS(ON)</sub>
- SuperSOT-<sup>TM</sup>23 provides low R<sub>DS(ON)</sub> and 30% higher power handling capability than SOT23 in the same footprint

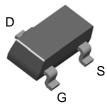


http://www.mtsemi.com

# Simplified Schematic



# MARKING DIAGRAM & PIN ASSIGNMENT



**SOT-23** 

# **Absolute Maximum Ratings**(T<sub>A</sub> = 25℃ unless otherwise noted)

| Symbol                            | Parameter                          |                | Ratings     | Units |
|-----------------------------------|------------------------------------|----------------|-------------|-------|
| V <sub>DSS</sub>                  | Drain-Source Voltage               |                | -25         | V     |
| V <sub>GSS</sub>                  | Gate-Source Voltage                |                | ±12         | V     |
| I <sub>D</sub>                    | Drain Current - Continuous         | (Note 1a)      | -4.3        | А     |
|                                   | – Pulsed                           |                | -10         |       |
| P <sub>D</sub>                    | Maximum Power Dissipation          | (Note 1a)      | 0.5         | W     |
|                                   |                                    | (Note 1b)      | 0.46        |       |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Junction Tem | perature Range | -55 to +150 | °C    |

#### **Thermal Characteristics**

| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient | (Note 1a) | 250 | °C/W |
|-----------------|-----------------------------------------|-----------|-----|------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case    | (Note 1)  | 75  | °C/W |

**Package Marking and Ordering Information** 

| Device Marking | Device      | Reel Size | Tape width | Quantity   |  |
|----------------|-------------|-----------|------------|------------|--|
| 010X           | 010X MT3421 |           | 8mm        | 3000 units |  |

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| Symbol                           | Parameter                                                                         | Conditions                                                     | Min  | Тур  | Max   | Units  |
|----------------------------------|-----------------------------------------------------------------------------------|----------------------------------------------------------------|------|------|-------|--------|
| OFF CHAR                         | ACTERISTICS                                                                       |                                                                |      | •    |       |        |
| BV <sub>DSS</sub>                | Drain-Source Breakdown Voltage                                                    | $V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$               | -25  |      |       | V      |
| $\Delta BV_{DSS}/\Delta T_{J}$   | Breakdown Voltage Temp. Coefficient                                               | I <sub>D</sub> = -250 μA, Referenced to 25 °C                  |      | -16  |       | mV /°C |
| DSS                              | Zero Gate Voltage Drain Current                                                   | V <sub>DS</sub> = -16 V, V <sub>GS</sub> = 0 V                 |      |      | -1    | μA     |
|                                  |                                                                                   | $T_J = 5$                                                      | 5°C  |      | -10   | μA     |
| GSSF                             | Gate - Body Leakage, Forward                                                      | $V_{GS} = 8 \text{ V}, V_{DS} = 0 \text{ V}$                   |      |      | 100   | nA     |
| GSSR                             | Gate - Body Leakage, Reverse                                                      | $V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$                  |      |      | -100  | nA     |
| ON CHARA                         | CTERISTICS (Note 2)                                                               |                                                                | ·    |      |       |        |
| $I_{GS(th)}$                     | Gate Threshold Voltage                                                            | $V_{DS} = V_{GS}, I_{D} = -250 \mu A$                          | -0.6 | -0.8 | -1.2  | V      |
| $\Delta V_{GS(th)}/\Delta T_{J}$ | Gate Threshold Voltage Temp. Coefficient                                          | $I_D$ = -250 $\mu$ A, Referenced to 25 °C                      |      | 3    |       | mV /°C |
| R <sub>DS(ON)</sub>              | Static Drain-Source On-Resistance $V_{GS} = -4.5 \text{ V}, I_D = -1.3 \text{ A}$ |                                                                |      | 0.8  | 0.9   | Ω      |
|                                  |                                                                                   | $T_J = 12$                                                     | :5°C | 0.12 | 0.15  |        |
|                                  |                                                                                   | $V_{GS} = -2.5 \text{ V}, I_{D} = -1.1 \text{ A}$              |      | 0.11 | 0.15  |        |
| D(ON)                            | On-State Drain Current                                                            | $V_{GS} = -4.5 \text{ V}, \ V_{DS} = -5 \text{ V}$             | -4.3 |      |       | Α      |
| FS                               | Forward Transconductance                                                          | $V_{DS} = -4.5 \text{ V}, I_{D} = -2 \text{ A}$                |      | 4    |       | S      |
| YNAMIC (                         | CHARACTERISTICS                                                                   |                                                                |      |      |       |        |
| iss                              | Input Capacitance                                                                 | $V_{DS} = -10 \text{ V}, \ V_{GS} = 0 \text{ V},$              |      | 330  |       | pF     |
| oss                              | Output Capacitance                                                                | f = 1.0 MHz                                                    |      | 80   |       | pF     |
| rss                              | Reverse Transfer Capacitance                                                      |                                                                |      | 35   |       | pF     |
| WITCHING                         | CHARACTERISTICS (Note 2)                                                          |                                                                |      |      |       |        |
| O(on)                            | Turn - On Delay Time                                                              | $V_{DD} = -5 \text{ V}, I_{D} = -0.5 \text{ A},$               |      | 7    | 15    | ns     |
| -                                | Tum - On Rise Time                                                                | $V_{GS}$ = -4.5 V, $R_{GEN}$ = 6 $\Omega$                      |      | 12   | 22    | ns     |
| D(off)                           | Turn - Off Delay Time                                                             |                                                                |      | 16   | 26    | ns     |
| •                                | Turn - Off Fall Time                                                              |                                                                |      | 5    | 12    | ns     |
| $Q_g$                            | Total Gate Charge                                                                 | $V_{DS} = -10 \text{ V}, I_{D} = -2 \text{ A},$                |      | 3.6  | 5     | nC     |
| $Q_{gs}$                         | Gate-Source Charge                                                                | V <sub>GS</sub> = -4.5 V                                       |      | 0.8  |       | nC     |
| $Q_{ m gd}$                      | Gate-Drain Charge                                                                 |                                                                |      | 0.7  |       | nC     |
| RAIN-SOL                         | IRCE DIODE CHARACTERISTICS AND MAXI                                               | MUM RATINGS                                                    |      |      | 1     | 1      |
| s                                | Maximum Continuous Drain-Source Diode For                                         | ward Current                                                   |      |      | -0.42 | Α      |
| V <sub>SD</sub>                  | Drain-Source Diode Forward Voltage                                                | $V_{GS} = 0 \text{ V}, I_{S} = -0.42 \text{ A} \text{ (Note)}$ |      | -0.7 | -1.2  | V      |

Note:

<sup>1.</sup> R<sub>gus</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>gus</sub> is guaranteed by design while R<sub>gus</sub> is determined by the user's board design.



a. 250°C/W when mounted on a 0.02 in² pad of 2oz Cu.



b. 270°C/W when mounted on a 0.001 in² pad of 2oz Cu.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  2.0%.

#### **Typical Electrical Characteristics**

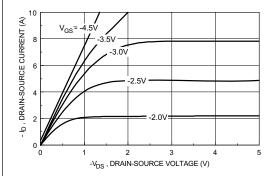


Figure 1. On-Region Characteristics.

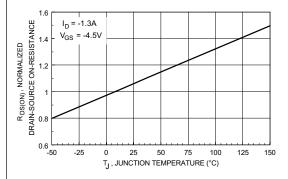


Figure 3. On-Resistance Variation with Temperature.

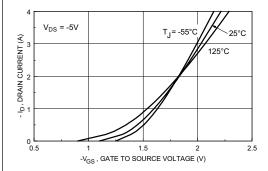


Figure 5. Transfer Characteristics.

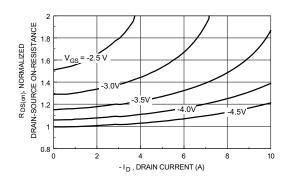


Figure 2. On-Resistance Variation with Drain Current and Gate

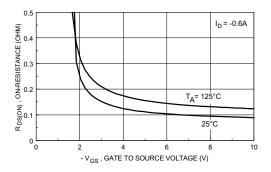


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

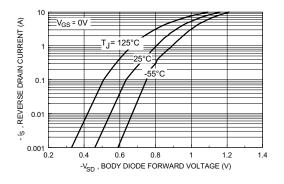


Figure 6. Body Diode Forward Voltage

Variation with Source

Current

and Temperature.

# Typical Electrical Characteristics (continued)

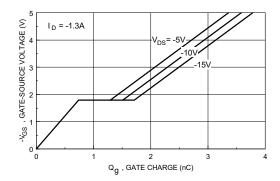


Figure 7. Gate Charge Characteristics.

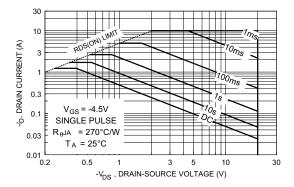


Figure 9. Maximum Safe Operating Area.

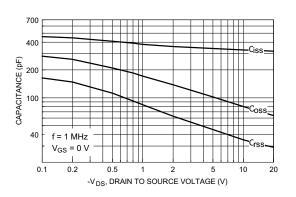


Figure 8. Capacitance Characteristics.

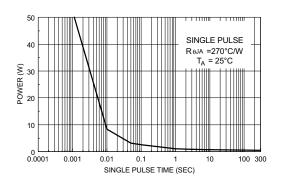


Figure 10. Single Pulse Maximum Power Dissipation.

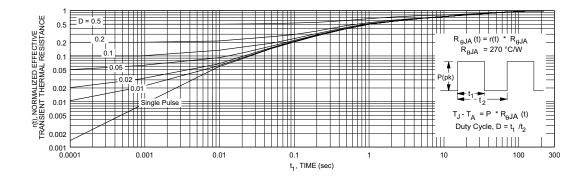


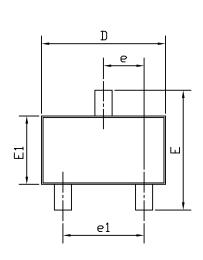
Figure 11. Transient Thermal Response Curve.

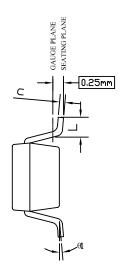
Thermal characterization performed using the conditions described in Note 1b. Transient thermal response will change depending on the circuit board design.

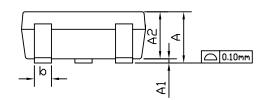
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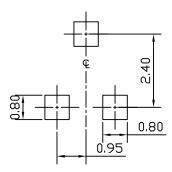
# SOT23 PACKAGE OUTLINE







#### RECOMMENDED LAND PATTERN



UNIT: mm

| SYMBOLS  | DIMENS | IONS IN MILLI | METERS | DIMI      | ENSIONS IN INC | CHES  |
|----------|--------|---------------|--------|-----------|----------------|-------|
| STWIBOLS | MIN    | NOM           | MAX    | MIN       | NOM            | MAX   |
| A        | 0.85   |               | 1.25   | 0.033     |                | 0.049 |
| A1       | 0.00   |               | 0.13   | 0.000     |                | 0.005 |
| A2       | 0.70   | 1.00          | 1.15   | 0.028     | 0.039          | 0.045 |
| b        | 0.30   | 0.40          | 0.50   | 0.012     | 0.016          | 0.020 |
| С        | 0.08   | 0.13          | 0.20   | 0.003     | 0.005          | 0.008 |
| D        | 2.80   | 2.90          | 3.10   | 0.110     | 0.114          | 0.122 |
| Е        | 2.60   | 2.80          | 3.00   | 0.102     | 0.110          | 0.118 |
| E1       | 1.40   | 1.60          | 1.80   | 0.055     | 0.063          | 0.071 |
| e        |        | 0.95 BSC      |        | 0.037 BSC |                |       |
| e1       |        | 1.90 BSC      |        |           | 0.075 BSC      |       |
| L        | 0.30   |               | 0.60   | 0.012     |                | 0.024 |
| θ1       | 0°     | 5°            | 8°     | 0°        | 5°             | 8°    |

#### NOTE

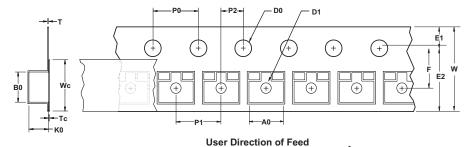
- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH OR GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 5 MILS EACH.
- 2. TOLERANCE ±0.100 mm (4 mil) UNLESS OTHERWISE SPECIFIED.
- 3. DIMENSION L IS MEASURED IN GAUGE PLANE.
- 4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.
- 5. ALL DIMENSIONS ARE IN MILLIMETERS.

#### **SOT-23 Std Tape and Reel Data** SOT23-3L Packaging Configuration: Figure 1.0 Packaging Description: Packaging Description: SOT23-31. parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 3,000 units per 7" or 177mm diameter reel. The reels are dark blue in color and is made of polystyrene plastic (anti-static coated). Other option comes in 10,000 units per 13" or 330cm diameter reel. This and so me other options are described in the Packaging Information table. Antistatic Cover Tape described in the Fackaging information table. These full freels are individually labeled and placed inside as tandard immediate box made of recyclable corrugated brown paper with a Fairchild logo printing. One box contains five reels maximum. And these immediate boxes are placed inside a labeled is hipping box which comes in different sizes depending on the number of parts shipped. PartNO.Label Embossed Carrier Tape 018I 0 SOT23-3L Packaging Information S tandar d Packaging Option D87Z SOT23-3L Unit Orientation Packaging type TNR TNR Qty per Reel/Tube/Bag 3,000 10,000 Reel Size 7" Dia 13" Box Dimension (mm) 193x 183 x80 355x 333 x 40 MARKING DIAGRAM Max qty per Box 15,000 30,000 01 X Weight per unit (gm) 0.0082 0.0082 Weight per Reel (kg) 0.1175 0.4006 Note/Comments X:Month Code X:Year Code Barcode 01: Manufacturer ID Lab el Barcode Labels ample XH1 MT3421ACTR QTY: 3000 MOS-TECH SEMICONDUCTOR LTD (ACTR) 193mm x 183m m x 80m m Pizza Box for Standard Option SOT23-3L Tape Leader and Trailer Configuration: Figure 2.0 $\bigcirc$ 0 0 0 ComponentsTrailer Ta pe 300mm minimum or Leade r T ape 500mm minimum or 75 em pty pockets 125 empty pockets

# SOT-23 Std Tape and Reel Data, continued

# **SOT23-3L Embossed Carrier Tape**

Configuration: Figure 3.0

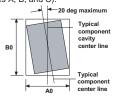


| Dimensions are in millimeter |      |      |     |      |       |      |      |      |     |     |      |       |     |      |
|------------------------------|------|------|-----|------|-------|------|------|------|-----|-----|------|-------|-----|------|
| Pkg type                     | Α0   | В0   | w   | D0   | D1    | E1   | E2   | F    | P1  | P0  | K0   | Т     | Wc  | Тс   |
| SOT-23                       | 3.15 | 2.77 | 8.0 | 1.55 | 1.125 | 1.75 | 6.25 | 3.50 | 4.0 | 4.0 | 1.30 | 0.228 | 5.2 | 0.06 |

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation

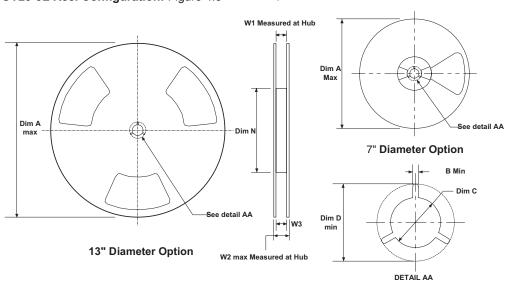


Sketch B (Top View)
Component Rotation



Sketch C (Top View)
Component lateral movement

# SOT23-3L Reel Configuration: Figure 4.0



| Dimensions are in inches and millimeters                                                                                                        |         |               |              |                                   |               |             |                                   |               |                             |  |  |
|-------------------------------------------------------------------------------------------------------------------------------------------------|---------|---------------|--------------|-----------------------------------|---------------|-------------|-----------------------------------|---------------|-----------------------------|--|--|
| Tape Size         Reel Option         Dim A         Dim B         Dim C         Dim D         Dim N         Dim W1         Dim W2         Dim W |         |               |              |                                   |               |             | Dim W3 (LSL-USL)                  |               |                             |  |  |
| 8mm                                                                                                                                             | 7" Dia  | 7.00<br>177.8 | 0.059<br>1.5 | 512 +0.020/-0.008<br>13 +0.5/-0.2 | 0.795<br>20.2 | 2.165<br>55 | 0.331 +0.059/-0.000<br>8.4 +1.5/0 | 0.567<br>14.4 | 0.311 - 0.429<br>7.9 - 10.9 |  |  |
| 8mm                                                                                                                                             | 13" Dia | 13.00<br>330  | 0.059<br>1.5 | 512 +0.020/-0.008<br>13 +0.5/-0.2 | 0.795<br>20.2 | 4.00<br>100 | 0.331 +0.059/-0.000<br>8.4 +1.5/0 | 0.567<br>14.4 | 0.311 - 0.429<br>7.9 - 10.9 |  |  |

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  - 1) 生命维持装置。
  - 2) 植埋于人体使用的装置。
  - 3) 用于治疗(切除患部、给药等)的装置。
  - 4) 其他直接影响到人的生命的装置。
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# Keep safety first in your circuit designs!

1. MOS-TECH Semiconductor Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.