MT4002N

N-Channel Enhancement Mode Field Effect Transistor

Product Summary

- $V_{DS} = 40V$
- $I_D = 60A (V_{GS} = 10V)$
- R DS(ON)=5.2 m Ω @V_{GS} =10V
- $R DS(ON) = 6.0 m\Omega$ @V_{GS} =4.5V

Features

- · Advanced Trench Process Technology.
- · High Density Cell Design for Ultra Low On-Resistance.
- · Lead free product is acquired.
- · RoHS Compliant.
- PDFN5x6-8L Package

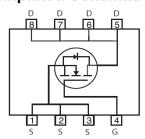
Applications

- · Portable Equipment and Battery Powered Systems.
- · Power Management in Notebook Computers.

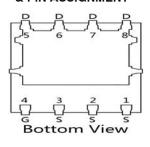
MT Semiconductor®

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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



Absolute Maximum Ratings (T_A = 25℃ unless otherwise noted)

Symbol	Parameter		Steady State	Units
V _{DS}	Drain-Source Voltage		40	V
V _{GS}	Gate-Source Voltage		±20	V
I _D	Continuous Drain Current ¹	- T _C = 25℃	60	A
I _{DM}	Pulsed Drain Current ²	1c - 25 C	300	A
Is	Continuous Source Current (Diode Conduction) 1		60	Α
E _{AS}	Single Pulse Drain-Source Avalanche Energy ³		110	mJ
P _D	Maximum Power Dissipation	T _A = 25℃	2	W
		T _C = 25°C	89	VV
T _J , T _{STG}	Operating Junction and Storage Temperature Range		-55~150	$^{\circ}$

Notes:

- 1. Surface Mounted on 1" x 1" FR4 Board, t≦10 Sec.
- 2. Pulse width limited by maximum junction temperature.
- 3. The test condition is T_J =25 °C, V_{DD} =30V, V_{GS} =10V, L=0.1mH, R_G =25 Ω , I_{AS} =50A.

Thermal Resistance Ratings

Symbol	Parameter	Typical	Maximum	Unit
R _{thJA}	Maximum Junction-to-Ambient	-	62.5	°C/W
RthJC	Maximum Junction-to-Case	-	1.4	C/VV

Electrical Characteristics (T_A=25°C, unless otherwise noted)

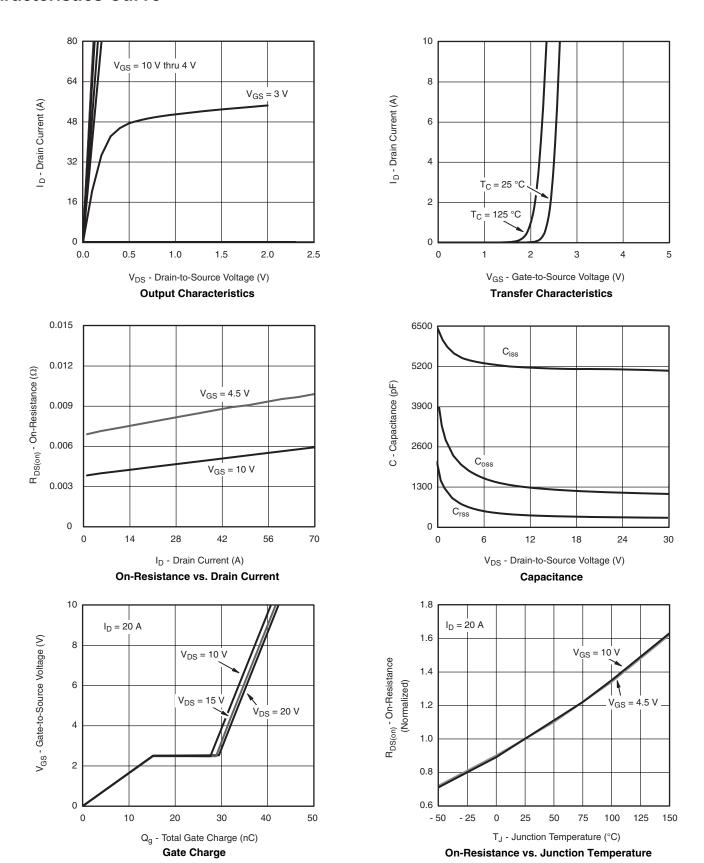
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit	
Static Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	40	-	-	V	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250μA	1	1.8	2.2	V	
Igss	Gate-Body Leakage Current	V _{GS} = ±20V, V _{DS} = 0V	-	-	±100	nA	
	Zero Gate Voltage Drain Current	V _{DS} = 32V, V _{GS} = 0V	-		1		
I _{DSS}		V_{DS} = 32V, V_{GS} = 0V, T_{J} = 85°C	-		30	μA	
	Drain Source On State Resistance ^a	V _{GS} = 10V, I _D = 60A	-	5.2	6.4	0	
$R_{DS(on)}$		V _{GS} = 4.5V, I _D = 30A	-	6.0	7.2	mΩ	
VsD	Diode Forward Voltage ^a	V _{GS} = 0V, I _S = 40A	-	0.82	1.3	V	
Dynamic	Characteristics ^b						
C _{iss}	Input Capacitance		-	5120	-	pF	
Coss	Output Capacitance	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz	-	1210	-		
C _{rss}	Reverse Transfer Capacitance		-	390	-		
Qg	Total Gate Charge		-	41	-		
Q _{gs}	Gate-Source Charge	V _{DS} = 15V, V _{GS} = 10V, I _D = 20A	-	15	-	nC	
Q _{gd}	Gate-Drain Charge		-	12	-		
t _{d(on)}	Turn-On Delay Time		-	22	-		
t _r	Rise Time	V _{DD} = 15V, V _{GS} = 4.5V	-	35	-	nSec	
t _{d(off)}	Turn-Off Delay Time	$I_D = 20A$, $R_{GEN} = 3\Omega$	-	50	-		
t _f	Fall Time		-	27	-		
t _{rr}	Body Diode Reverse Recovery Time	I _F =20A, di/dt= 100A/μA, T _J =25℃	-	33	-	nSec	

Note:

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2%.

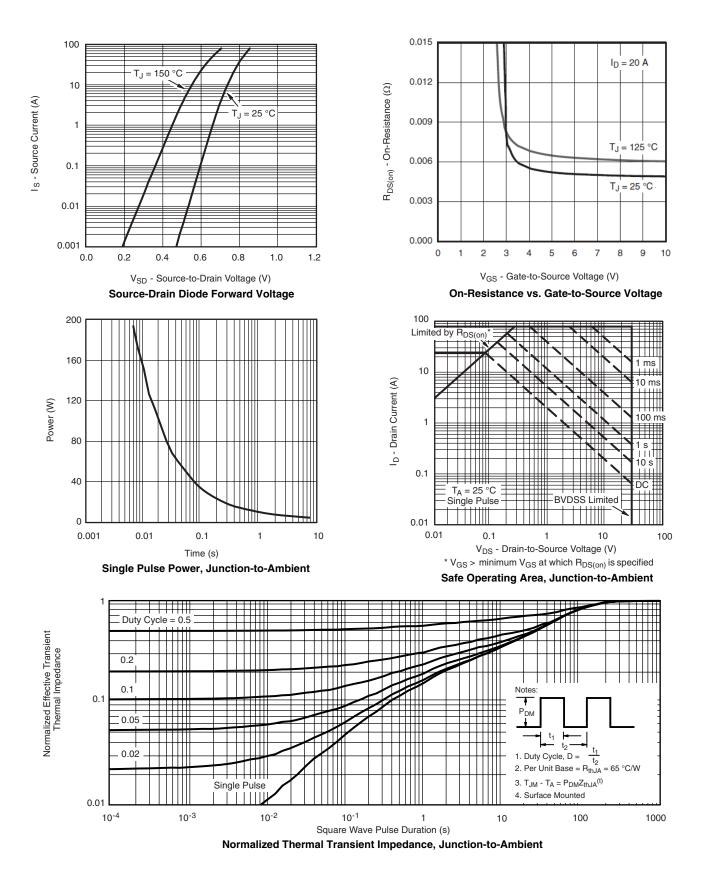
b. Guaranteed by design, not subject to production testing.

Characteristics Curve



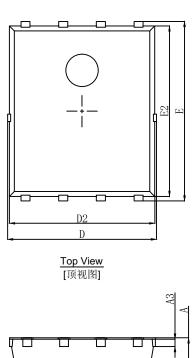
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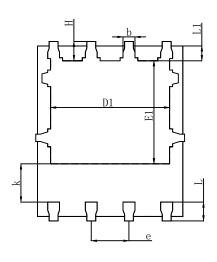
Characteristics Curve



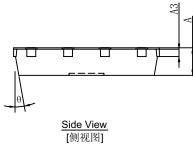
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PDFNWB5×6-8L(P1.27TO.95) PACKAGE OUTLINE DIMENSIONS





Bottom View [背视图]



Cumbal	Dimensions I	n Millimeters	Dimension	s In Inches	
Symbol	Min.	Max.	Min.	Max.	
Α	0.900	1.000	0.035	0.039	
A3	0.254REF.		0.010REF.		
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	3.910	4.110	0.154	0.162	
E1	3.375	3.575	0.133	0.141	
D2	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270TYP.		0.050TYP.		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	

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