MT4015N5

N-Channel Enhancement Mode Field Effect Transistor

Product Summary

- V_{DS} = 40V
- I_D = 160A (V_{GS} = 10V)
- $R_{DS(ON)} = 1.7 \, m_{\Omega} \, @V_{GS} = 10V$
- $R_{DS(ON)} = 2.2 m \Omega @V_{GS} = 4.5 V$

Features

- Advanced Trench Process Technology.
- · High Density Cell Design for Ultra Low On-Resistance.
- · Lead free product is acquired.
- RoHS Compliant.
- PDFN5x6-8L Package

Applications

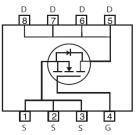
- · Portable Equipment and Battery Powered Systems.
- Power Management in Notebook Computers.

Absolute Maximum Ratings (T_A = 25[°]C unless otherwise noted)

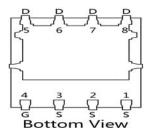


http://www.mtsemi.com

Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



Symbol	Parameter		Steady State	Units
V _{DS}	Drain-Source Voltage		40	V
V _{GS}	Gate-Source Voltage		±20	V
ID	Continuous Drain Current ¹	− T _C = 25°C	160	A
IDM	Pulsed Drain Current ²	10 - 250	300	A
ls	Continuous Source Current (Diode Conduction) ¹		60	A
E _{AS}	Single Pulse Drain-Source Avalanche Energy ³		110	mJ
D-	Maximum Power Dissipation	T _A = 25℃	2	w
PD		T _C = 25℃	89	٧V
TJ, TSTG	Operating Junction and Storage Temperature Range		-55~150	°C

Notes:

- 1. Surface Mounted on 1" x 1" FR4 Board, t \leq 10 Sec.
- 2. Pulse width limited by maximum junction temperature.
- 3. The test condition is T_J =25 $^\circ\!\mathrm{C},$ V_{DD} =30V, V_{GS} =10V, L=0.1mH, R_G =25 $\Omega,$ I_{AS} =50A.

Thermal Resistance Ratings

Symbol	Parameter	Typical	Maximum	Unit
R _{thJA}	Maximum Junction-to-Ambient	-	62.5	°C/W
RthJC	Maximum Junction-to-Case	-	1.4	C/VV

Electrical Characteristics (T_A=25°C, unless otherwise noted)

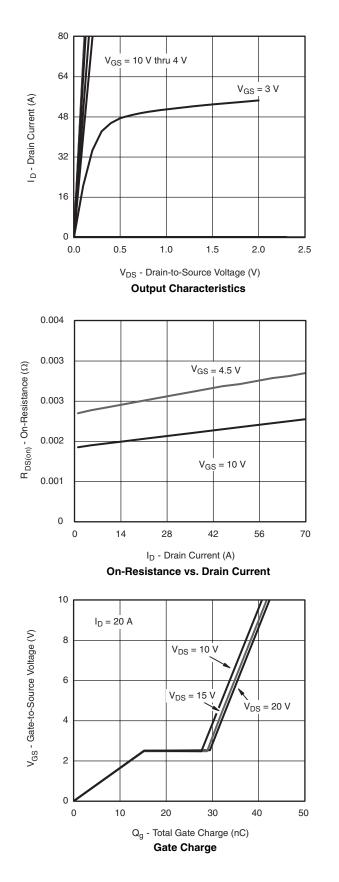
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit	
Static Cl	haracteristics		I				
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250µA 40		-	-	V	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250µA	1.0	-	2.2	V	
lgss	Gate-Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA	
IDSS	Zero Gate Voltage Drain Current	V _{DS} = 40V, V _{GS} = 0V -			1		
		V_{DS} = 40V, V_{GS} = 0V, T_{J} = 85 $^{\circ}$ C	-		30	μA	
R _{DS(on)}	Drain Source On State Resistance ^a	V _{GS} = 10V, I _D = 30A	-	1.7	2.2		
		V _{GS} = 4.5V, I _D = 20A	-	2.2	4.8	mΩ	
Vsd	Diode Forward Voltage ^a	V _{GS} = 0V, I _S = 40A	-	0.82	1.3	V	
Dynamic	Characteristics ^b		•			•	
Ciss	Input Capacitance		-	3520	-		
Coss	Output Capacitance	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz	-	1210	-	pF	
C _{rss}	Reverse Transfer Capacitance		-	390	-		
Qg	Total Gate Charge		-	41	-		
Q _{gs}	Gate-Source Charge	V _{DS} = 15V, V _{GS} = 10V, I _D = 20A	-	15	-	nC	
Q _{gd}	Gate-Drain Charge		-	12	-		
t _{d(on)}	Turn-On Delay Time		-	22	-		
tr	Rise Time	V _{DD} = 15V, V _{GS} = 4.5V	-	35	-		
t _{d(off)}	Turn-Off Delay Time	I_D = 20A, R_{GEN} = 3 Ω	-	50	-	- nSec	
t _f	Fall Time		-	27	-		
t _{rr}	Body Diode Reverse Recovery Time	I _F =20A, di/dt= 100A/μA, T _J =25℃	-	33	-	nSec	

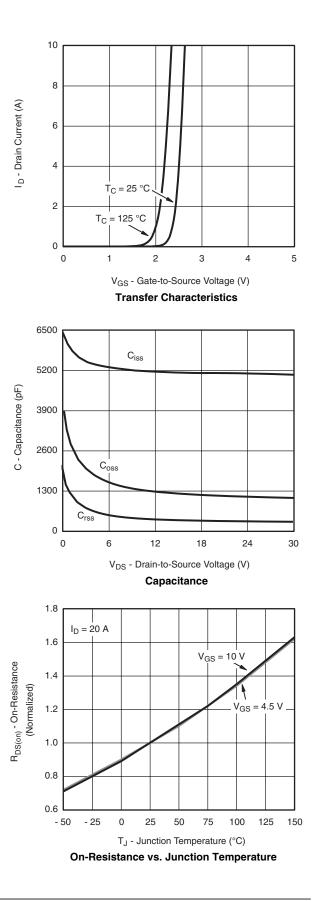
Note:

a. Pulse test; pulse width \leq 300µs, duty cycle \leq 2%.

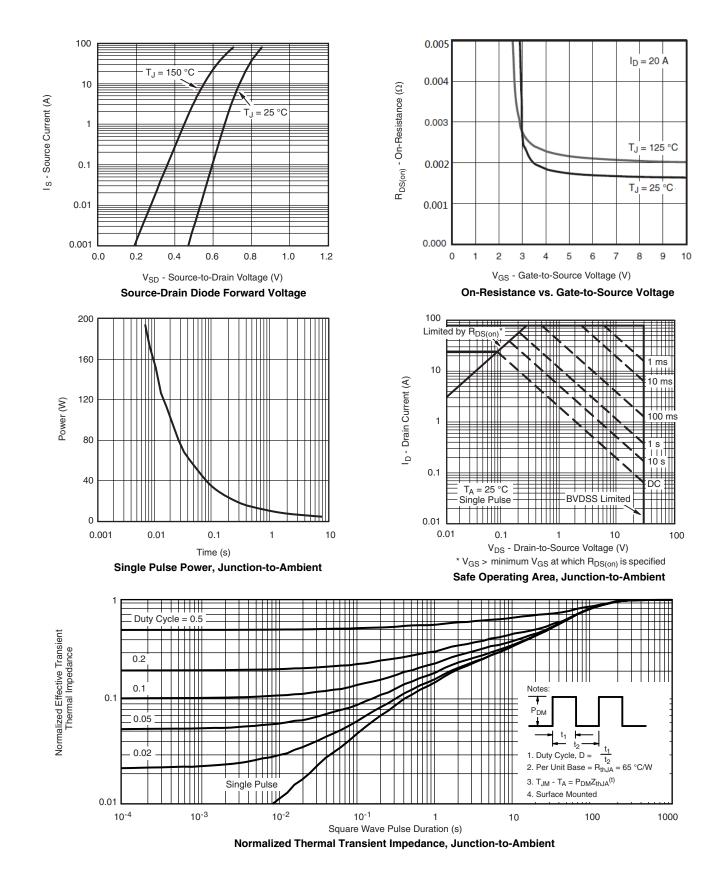
b. Guaranteed by design, not subject to production testing.

Characteristics Curve

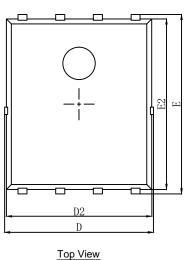




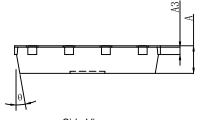
Characteristics Curve



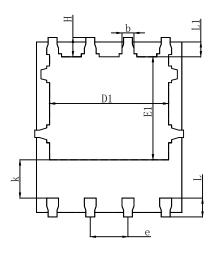
PDFNWB5×6-8L(P1.27T0.95) PACKAGE OUTLINE DIMENSIONS







<u>Side View</u> [侧视图]



<u>Bottom View</u> [背视图]

Sumbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
А	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010	REF.
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
е	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
Н	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

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