MT4110

N-Channel PowerTrench®MOSFET
100V, 8A, 18mΩ

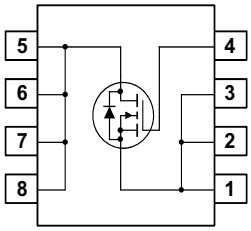
MT Semiconductor®

http://www.mtsemi.com

General Description

This N-Channel MOSFET has been designed specially to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.It has been optimized for low gate charge, low $R_{DS(on)}$ and fast switching speed.

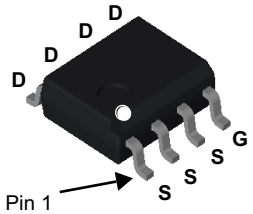
Simplified Schematic



Features

- $R_{DS(on)}$ = 18mΩ , V_{GS} = 10V, I_D = 8A
- $R_{DS(on)}$ = 20mΩ , V_{GS} = 4.5V, I_D = 8A
- Low gate charge
- High performance trench technology for extremely low $R_{DS(ON)}$
- High power and current handling capability
- RoHS compliant

MARKING DIAGRAM & PIN ASSIGNMENT



SO-8

Absolute Maximum Ratings (T_A = 25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain to Source Voltage	100	V
V_{GS}	Gate to Source Voltage	±20	V
I_D	Drain Current Continuous (T_A = 25°C, V_{GS} = 10V, $R_{\theta JA}$ = 50°C/W)	8	A
	Continuous (T_A = 25°C, V_{GS} = 4.5V, $R_{\theta JA}$ = 50°C/W)	8	A
	Pulsed	55	A
E_{AS}	Single Pulse Avalanche Energy (Note 1)	236	mJ
P_D	Power dissipation	3.1	W
	Derate above 25°C	20	mW/°C
T_J, T_{STG}	Operating and Storage Temperature	-55 to 150	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction to Case (Note 2)	25	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2a)	50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 2b)	125	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
MT4110	MT4110	SO-8	330mm	12mm	2500 units

Electrical Characteristics T_J = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
B_{VDSS}	Drain to Source Breakdown Voltage	I_D = 250μA, V_{GS} = 0V	100	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 80V	-	-	1	μA
		V_{GS} = 0V, T_J = 150°C	-	-	250	
I_{GSS}	Gate to Source Leakage Current	V_{GS} = ±20V	-	-	±100	nA
On Characteristics						
$V_{GS(TH)}$	Gate to Source Threshold Voltage	V_{GS} = V_{DS}, I_D = 250μA	1	1.6	2.5	V
$r_{DS(on)}$	Drain to Source On Resistance	I_D = 8A, V_{GS} = 10V	-	18	22	mΩ
		I_D = 8A, V_{GS} = 4.5V	-	20	25	
		I_D = 8A, V_{GS} = 10V, T_J = 150°C	-	21.2	26.5	
Dynamic Characteristics						
C_{ISS}	Input Capacitance	V_{DS} = 15V, V_{GS} = 0V, f = 1MHz	-	1005	-	pF
C_{OSS}	Output Capacitance		-	110	-	pF
C_{RSS}	Reverse Transfer Capacitance		-	25	-	pF
R_G	Gate Resistance	V_{GS}=0V, V_{DS}=0V, f=1MHz	-	1.6	-	Ω
$Q_{g(TOT)}$	Total Gate Charge at 10V	V_{GS} = 0V to 10V; V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	16.5	25	nC
$Q_{g(5)}$	Total Gate Charge at 5V	V_{GS} = 0V to 5V; V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	12	14	nC
$Q_{g(TH)}$	Threshold Gate Charge	V_{GS} = 0V to 1V; V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	2.5	3.2	nC
Q_{gs}	Gate to Source Gate Charge	V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	7.0	-	nC
Q_{gs2}	Gate Charge Threshold to Plateau	V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	4.5	-	nC
Q_{gd}	Gate to Drain “Miller” Charge	V_{DD} = 55V, I_D = 8A, I_g = 1.0mA	-	11	-	nC
Switching Characteristics (V_{GS} = 10V)						
t_{ON}	Turn-On Time	V_{DD} = 55V, I_D = 10A, V_{GS} = 10V, R_{GS} = 6.2Ω	-	-	4.8	ns
$_{d(ON)}$	Turn-On Delay Time		-	6	-	ns
t_r	Rise Time		-	20	-	ns
$t_{d(OFF)}$	Turn-Off Delay Time		-	8.6	-	ns
t_f	Fall Time			14	-	ns
t_{OFF}	Turn-Off Time		-	-	9.6	ns
Drain-Source Diode Characteristics						
V_{SD}	Source to Drain Diode Voltage	I_{SD} = 10A	-	-	1.3	V
		I_{SD} = 2.1A	-	-	1.0	V
t_{rr}	Reverse Recovery Time	I_{SD} = 10A, dI_{SD}/dt = 100A/μs	-	-	21	ns
Q_{RR}	Reverse Recovered Charge	I_{SD} = 10A, dI_{SD}/dt = 100A/μs	-	-	12	nC

Notes:

1: Starting T_J = 25°C, L = 1mH, I_{AS} = 19.8A, V_{DD} = 30V, V_{GS} = 10V.

2: $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.

a) 50°C/W when mounted on a 1in^2 pad of 2 oz copper.

b) 125°C/W when mounted on a minimum pad.

Typical Characteristics T_J = 25°C unless otherwise noted

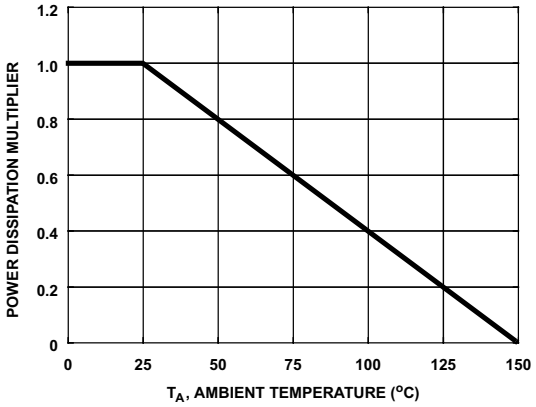


Figure 1. Normalized Power Dissipation vs Ambient Temperature

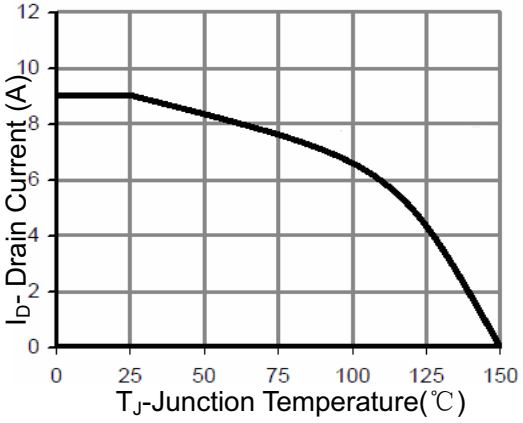


Figure 2. Maximum Continuous Drain Current vs Ambient Temperature

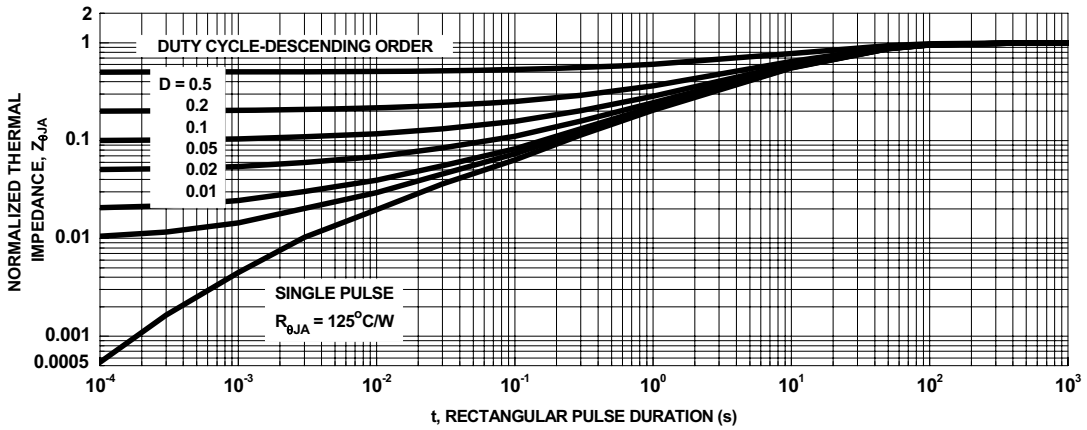


Figure 3. Normalized Maximum Transient Thermal Impedance

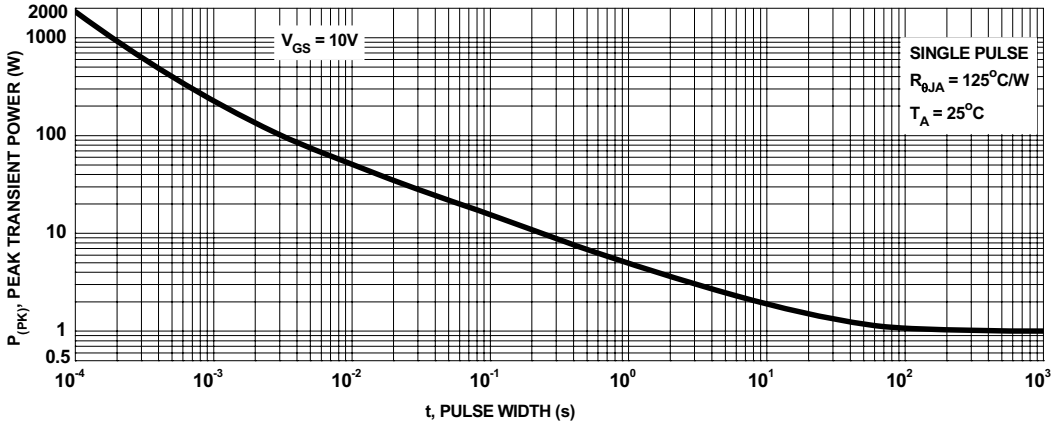
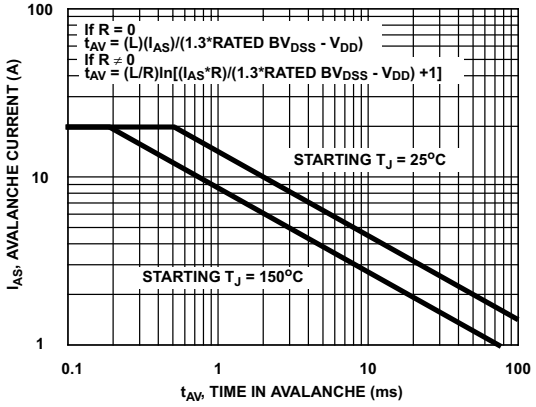


Figure 4. Single Pulse Maximum Power Dissipation

Typical Characteristics T_J = 25°C unless otherwise noted



NOTE: Refer to Fairchild Application Notes AN7514 and AN7515

Figure 5. Unclamped Inductive Switching Capability

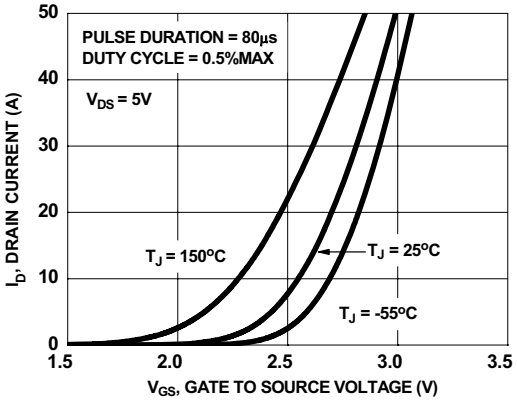


Figure 6. Transfer Characteristics

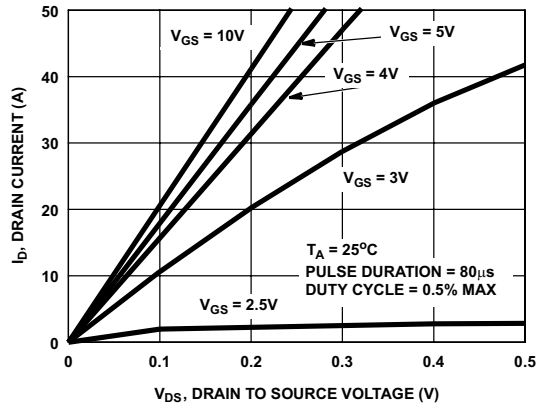


Figure 7. Saturation Characteristics

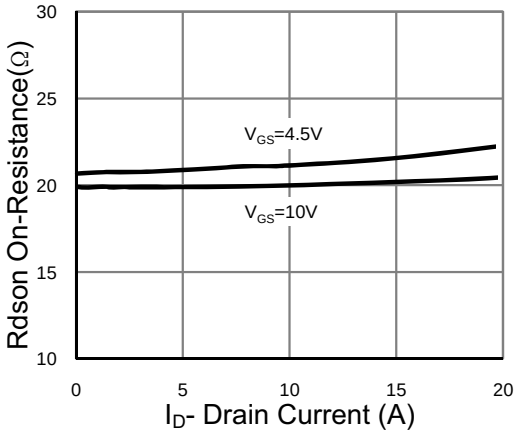


Figure 8 Rdson-Drain Current

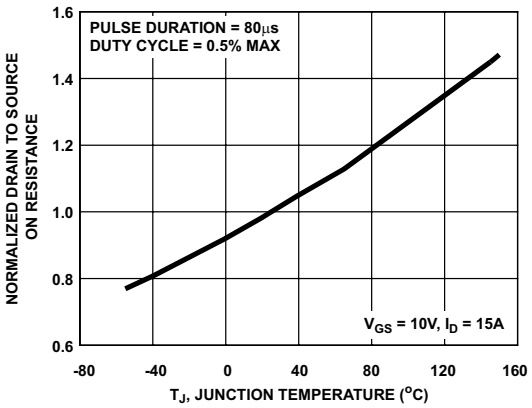


Figure 9. Normalized Drain to Source On Resistance vs Junction Temperature

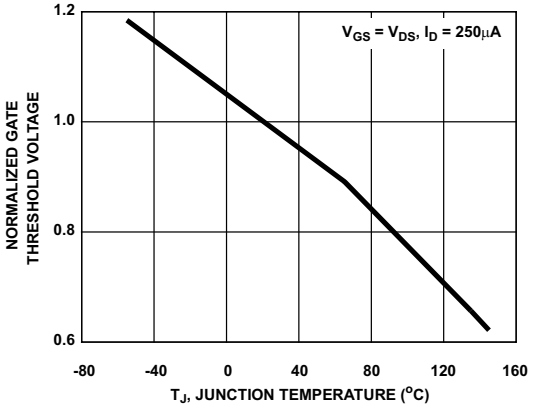


Figure 10. Normalized Gate Threshold Voltage vs Junction Temperature

Typical Characteristics T_J = 25°C unless otherwise noted

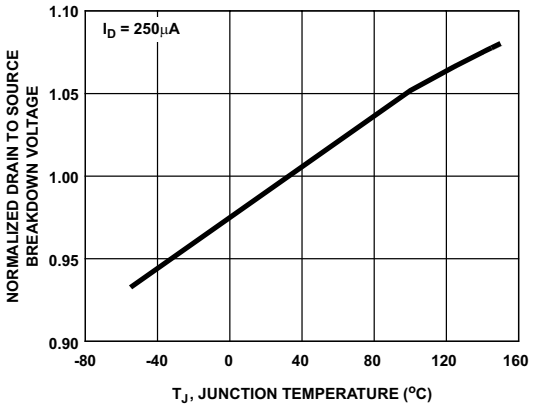


Figure 11. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

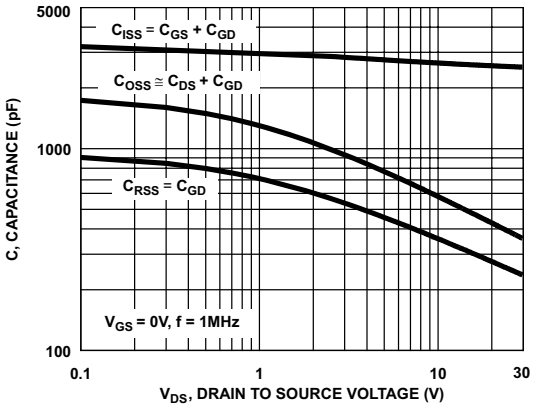


Figure 12. Capacitance vs Drain to Source Voltage

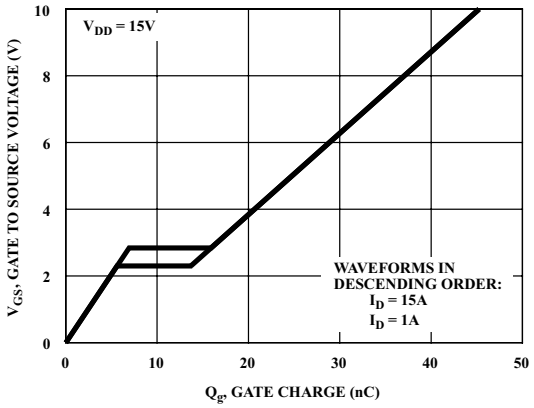


Figure 13. Gate Charge Waveforms for Constant Gate Currents

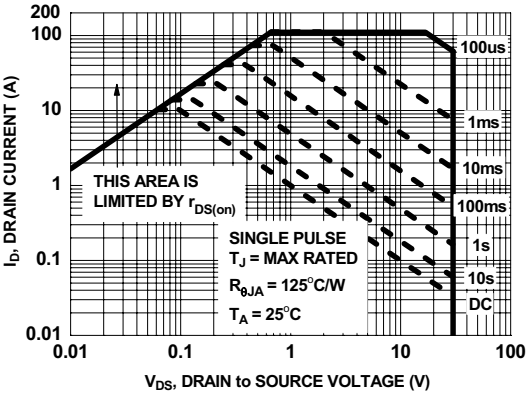


Figure 14. Forward Bias Safe Operating Area

Test Circuits and Waveforms

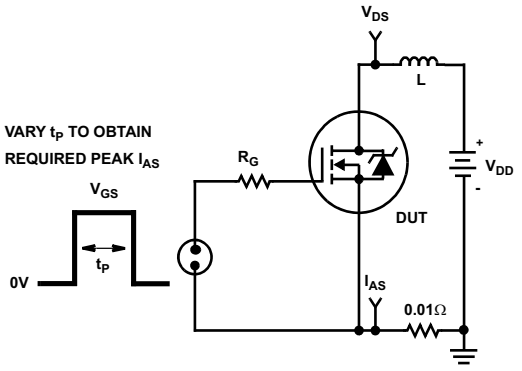


Figure 15. Unclamped Energy Test Circuit

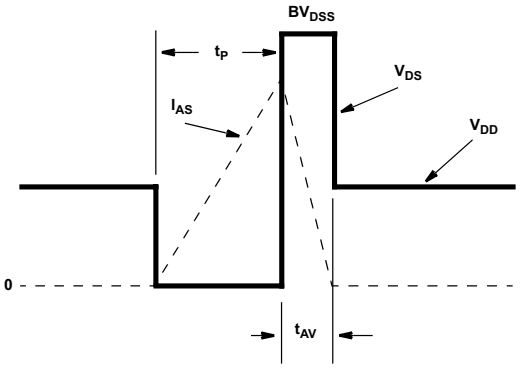


Figure 16. Unclamped Energy Waveforms

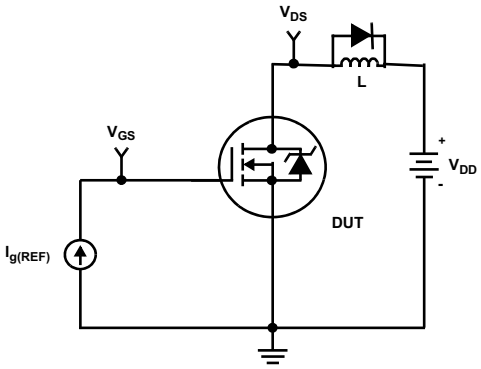


Figure 17. Gate Charge Test Circuit

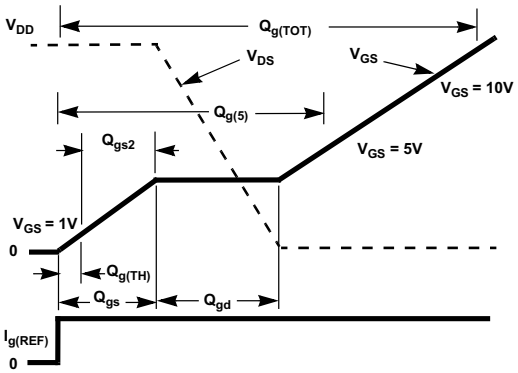


Figure 18. Gate Charge Waveforms

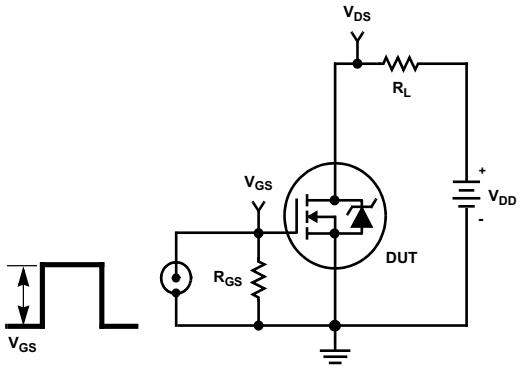


Figure 19. Switching Time Test Circuit

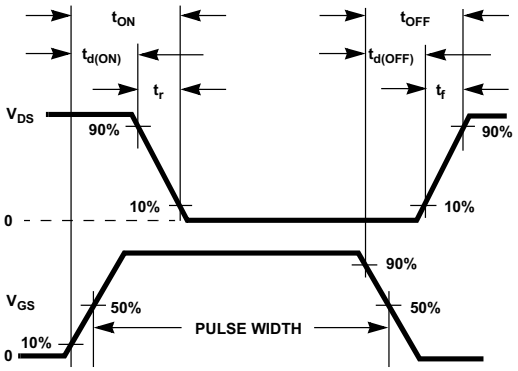
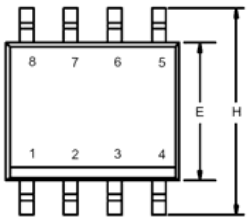
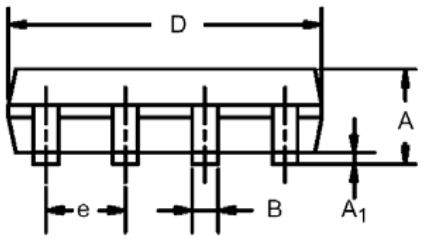


Figure 20. Switching Time Waveforms

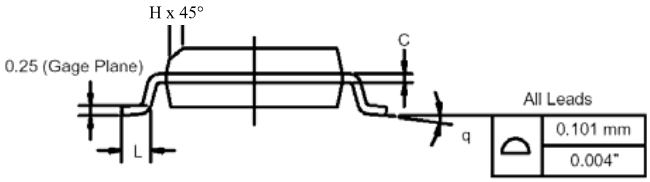
Package Information

SO-8: 8LEAD





Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A_1	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°



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Keep safety first in your circuit designs!

1. MOS-TECH Semiconductor Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
   Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.