## 40V Complementary Power MOSFET

## Features

- N-Channel 40V/3.0A
  R<sub>DS</sub>(ON) = 31m<sub>Ω</sub> (typ) @ VGS = 10V
  R<sub>DS</sub>(ON) = 44m<sub>Ω</sub> (typ) @ VGS = 4.5V
- P-Channel -40V/-2.8A  $R_{DS}$  (ON) = 69m $_{\Omega}$  (typ) @ VGS = -10V  $R_{DS}$  (ON) = 98m $_{\Omega}$  (typ) @ VGS = -4.5V
- RoHS Compliant

## **General Description**

This complementary MOSFET device is produced using Mos-tech's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

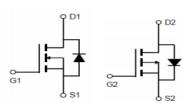
## Applications

- DC-DC converter
- Power management
- LCD backlight inverter

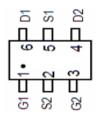
## Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted



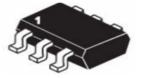
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N-channel P-channel Schematic diagram



Pin Assignment



SOT23-6L top view

Symbol	Parameter		N-CH	P-CH	Units	
V <sub>DSS</sub>	Drain-Source Voltage		40	-40	V	
V <sub>GSS</sub>	Gate-Source Voltage		±20	± 20	V	
ID	Drain Current - Continuous	(Note 1a)	3.0	-2.8		
	- Pulsed		20	-12		
	Power Dissipation for Dual Operation		1.2		w	
<b>_</b>	Power Dissipation for Single Operation (Note 1a) (Note 1b) (Note 1c)		1.5			
P <sub>D</sub>			1.3			
			2.0			
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to	+150	°C	

#### **Thermal Characteristics**

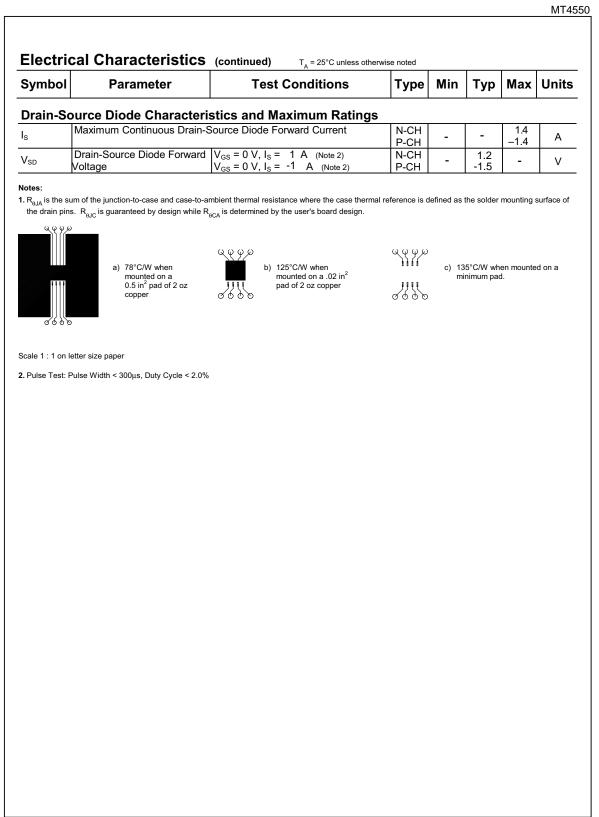
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	80	°C/W
R <sub>0JC</sub>	Thermal Resistance, Junction-to-Case	(Note 1)	55	°C/W

## Package Marking and Ordering Information

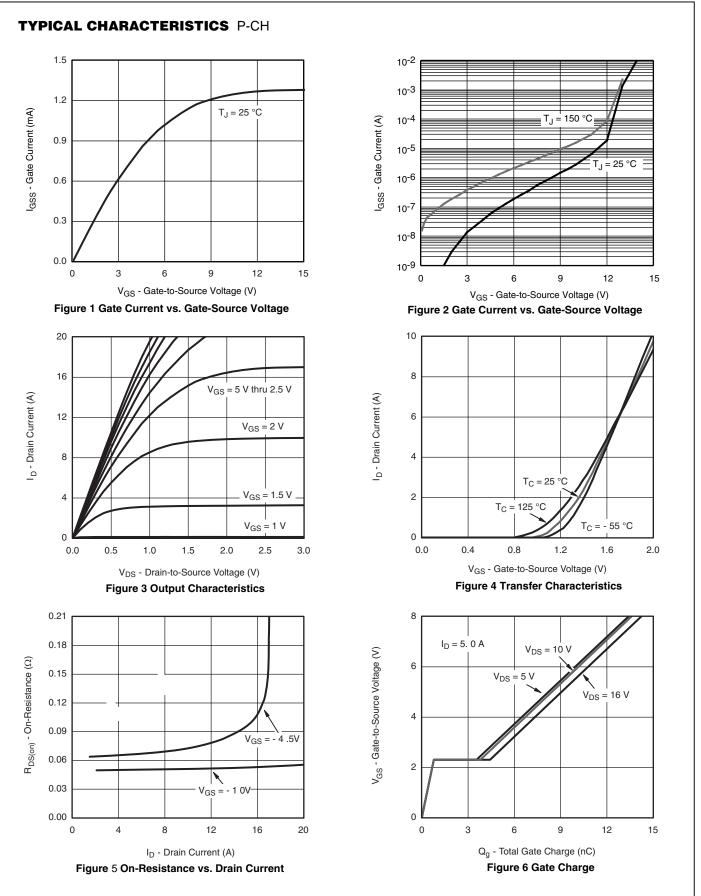
Device Marking	Device	Reel Size	Tape width	Quantity
MT4550	MT4550	Ø180mm	8mm	3000 units

Symbo	I Parameter	Test Conditions	Туре	Min	Тур	Max	Units	
Off Chai	racteristics							
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = 250 \mu A$ $V_{GS} = 0 V, I_D = -250 \mu A$	N-CH P-CH	40 - <b>40</b>	-	-	v	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C $I_D = -250 \ \mu$ A, Referenced to 25°C	N-CH P-CH	-	19 -15	-	mV/°C	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 40V, V_{GS} = 0V V_{DS} = -40V, V_{GS} = 0V$	N-CH P-CH	-	-	1	μA	
I <sub>GSS</sub>	Gate-Body Leakage	$V_{GS} = \pm 20 V, V_{DS} = 0 V$ $V_{GS} = \pm 20 V, V_{DS} = 0 V$	N-CH P-CH	-	-	<u>+</u> 100 +100	nA	
On Char	acteristics (Note 2)		1					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ $V_{DS} = V_{GS}, I_D = -250 \ \mu A$	N-CH P-CH	1 -1	1.7	2.0 -3.0	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to 25°C $I_D = -250 \ \mu\text{A}$ , Referenced to 25°C	N-CH P-CH	-	-3.6 -3.6		mV/°C	
		VGs= 10V,I_D=3.0A VGs=4.5V,I_D=3.0A	N-CH	-	31 44	50 70	mΩ	
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	Vas= -10V,ID=-3.0A Vas=-4.5V,ID=-3.0A	P-CH	-	69 98	85 130		
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$ $V_{GS} = -10 \text{ V}, V_{DS} = -5 \text{ V}$	N-CH P-CH	3.0 -2.5	-	-	A	
<b>g</b> FS	Forward Transconductance	$V_{DS} = 10 V, I_D = 3A$ $V_{DS} = -5 V, I_D = -2.5A$	N-CH P-CH	-	23	-	s	
Dynamic	Characteristics	1.00 1.1,10 2.0,1					1	
C <sub>iss</sub>	Input Capacitance	N-CH V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V,	N-CH P-CH	-	288 268	-	pF	
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz P-CH	N-CH P-CH	-	38 22	-	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance	$V_{DS} = -10 V, V_{GS} = 0 V,$ f = 1.0 MHz	N-CH P-CH	-	72 38	-	pF	
witchine	g Characteristics (Note 2)							
d(on)	Turn-On Delay Time	N-CH V <sub>DD</sub> = 10 V, I <sub>D</sub> = 1 A,	N-CH P-CH	-	3 5	-	ns	
r	Turn-On Rise Time	$V_{GS} = 10 V, R_{GEN} = 1 \Omega$	N-CH P-CH	-	7.5 12	-	ns	
	Turn-Off Delay Time	P-CH V <sub>DD</sub> = -10 V, I <sub>D</sub> = -1 A,	N-CH P-CH	-	20 25	-	ns	
d(off)		$V_{\rm GS} = -10 \text{ V}, \text{ R}_{\rm GEN} = 1 \Omega$	N-CH P-CH	-	6 10	-	ns	
	Turn-Off Fall Time		N-CH P-CH	-	12 10	-	nC	
f	Total Gate Charge	N-CH V <sub>DS</sub> = 10 V, I <sub>D</sub> =3A ,V <sub>GS</sub> = 10 V					nC	
f Qg		V <sub>DS</sub> = 10 V, I <sub>D</sub> =3A ,V <sub>GS</sub> = 10 V P-CH	N-CH P-CH	-	1 0.8	-		
Q <sub>g</sub> Q <sub>gs</sub>	Total Gate Charge	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 3\text{ A}, V_{GS} = 10 \text{ V}$	N-CH	-	1 0.8 2 1.8	-	nC	

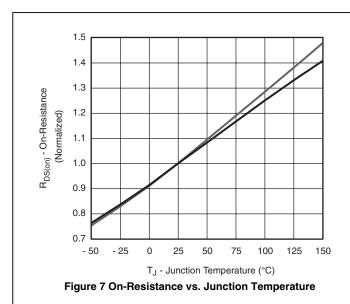
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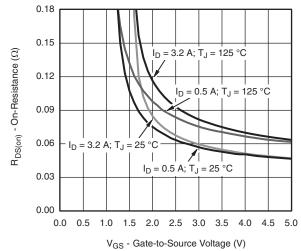
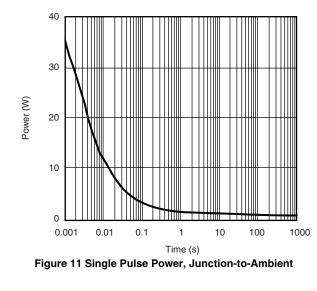
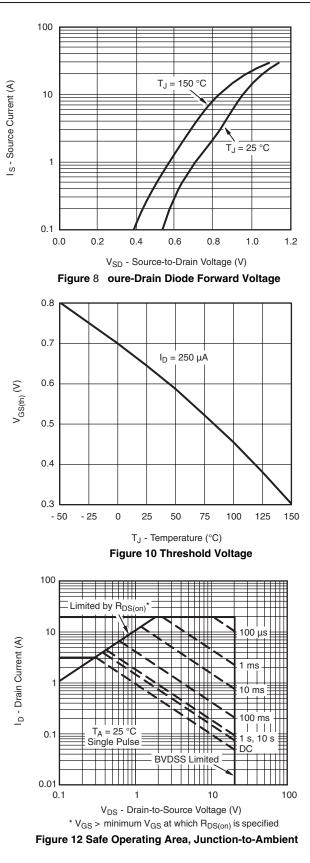
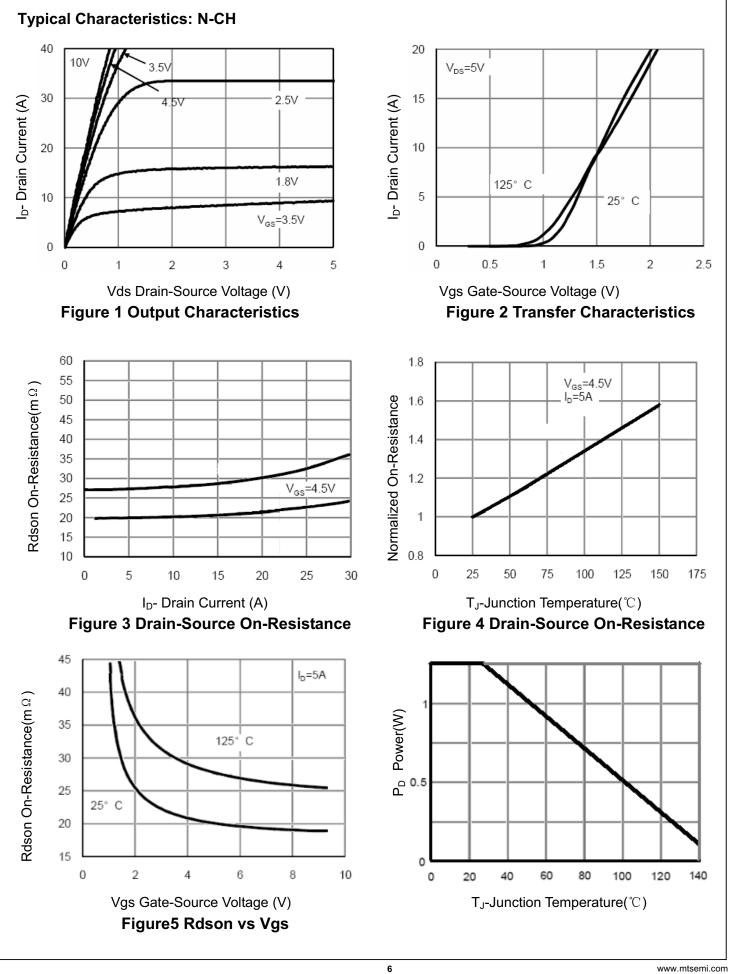


Figure 9 On Resistance VS. Gate-to-Source Voltage

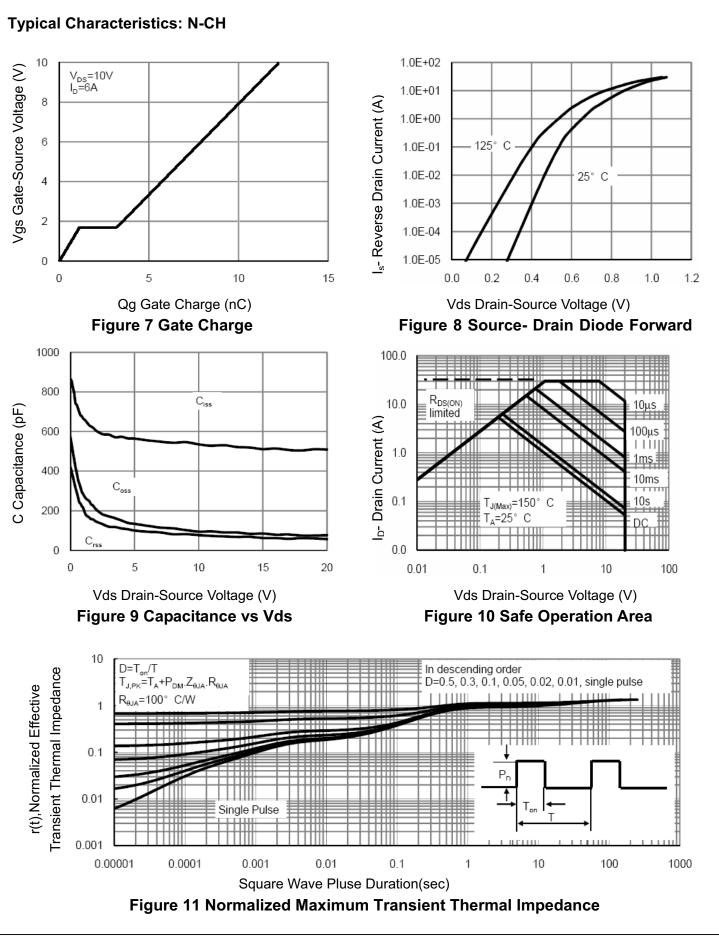


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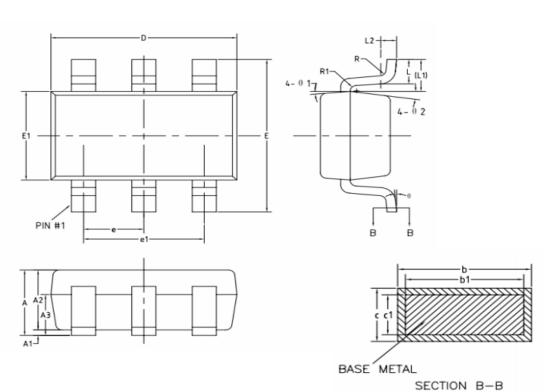








#### SOT23-6L Package Information



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	_	-	1.45
A1	0	-	0.15
A2	0.90	1.10	1.30
A3	0.60	0.65	0.70
b	0.39	-	0.49
b1	0.38	0.40	0.45
с	0.12	-	0.19
c1	0.11	0.13	0.15
D	2.85	2.95	3.05
E	2.60	2.80	3.00
E1	1.55	1.65	1.75
e	0.85	0.95	1.05
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1		0.59REF	
L2		0.25BSC	
R	0.05	-	-
R1	0.05	-	0.20
θ	0.	-	8*
θ 1	8'	10	12
θ2	8'	10'	12

NOTE

1. ALL DIMENSIONS ARE IN MILLMETERS.

2. DIMENSIONS ARE INCLUSIVE OF PLATING.

3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.

MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH. 4. DIMENSION L IS MEASURED IN GAUGE PLANE.

5. CONTROLLING DIMENSION IS MILLIMETER.

CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

MT4550

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