N-Channel Enhancement Mode Field Effect Transistor

Product Summary

PRODUCT S	SUMMARY	
Vdss	Id	$RDS(ON) (m \Omega) Typ$
30V	50A	4.5@ VGS=10V
		7.5@VGS=4.5V

Features

- Super high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement
- TO-252-2L package

Applications

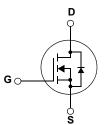
- · DC-DC primary bridge
- DC-DC Synchronous rectification
- Hot swap

Absolute Maximum Ratings (T_A = 25 °C unless otherwise noted)



http://www.mtsemi.com

Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



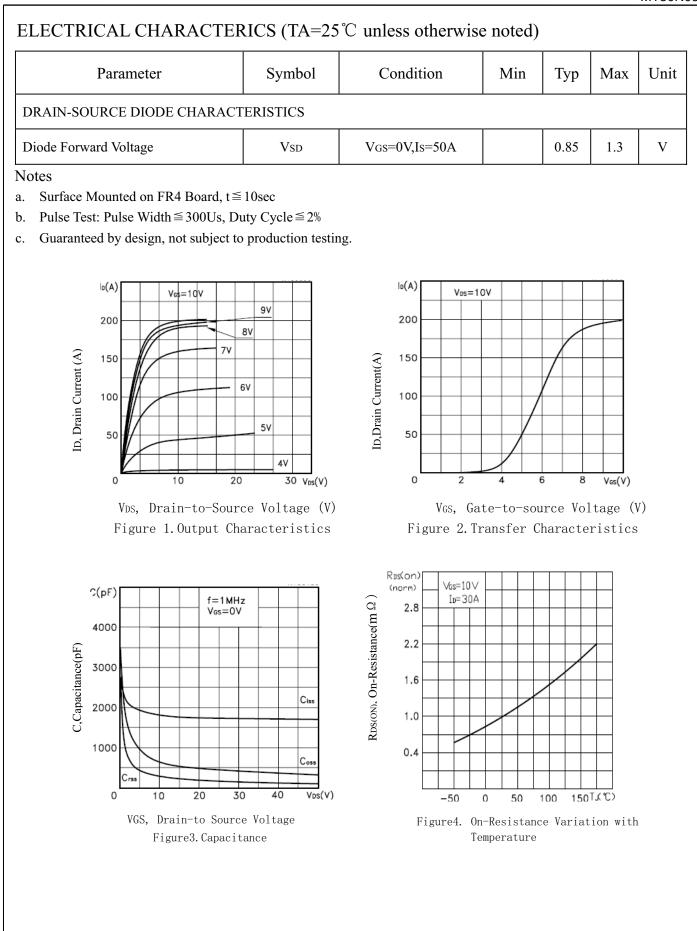
TO-252-2L

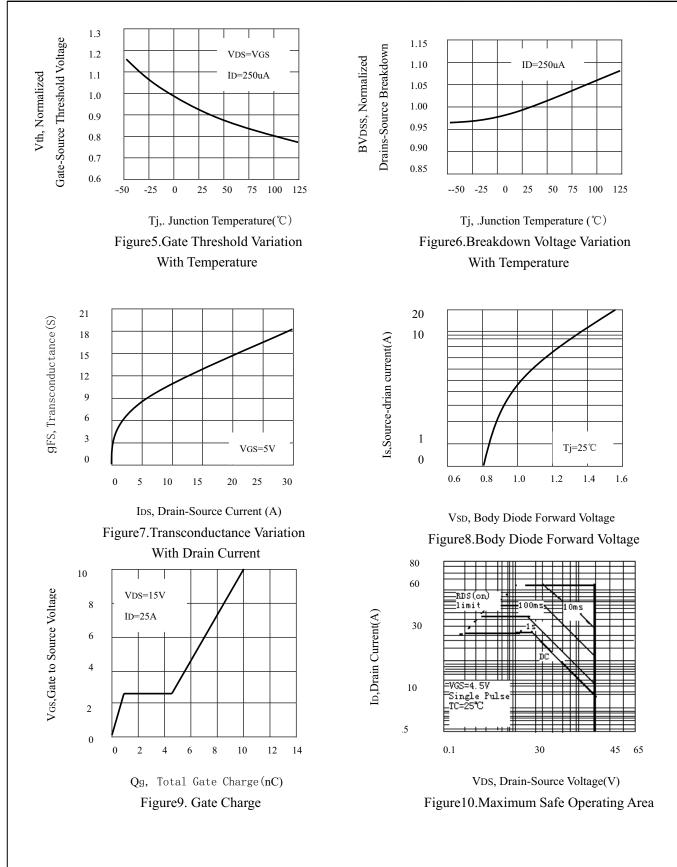
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	30	V	
Gate-Source Voltage	VGS	±20	V	
Drain Current-Continuous ^a @Tj=125°C	ID	50	А	
- Pulse d^b	Idм	350	А	
Drain-source Diode Forward Current ^a	Is	60	А	
Maximum Power Dissipation ^a	Pd	70	W	
Operating Junction and Storage Temperature Range	Tj,Tstg	-55 to 150	°C	

THERMAL CHARACTERISTICS

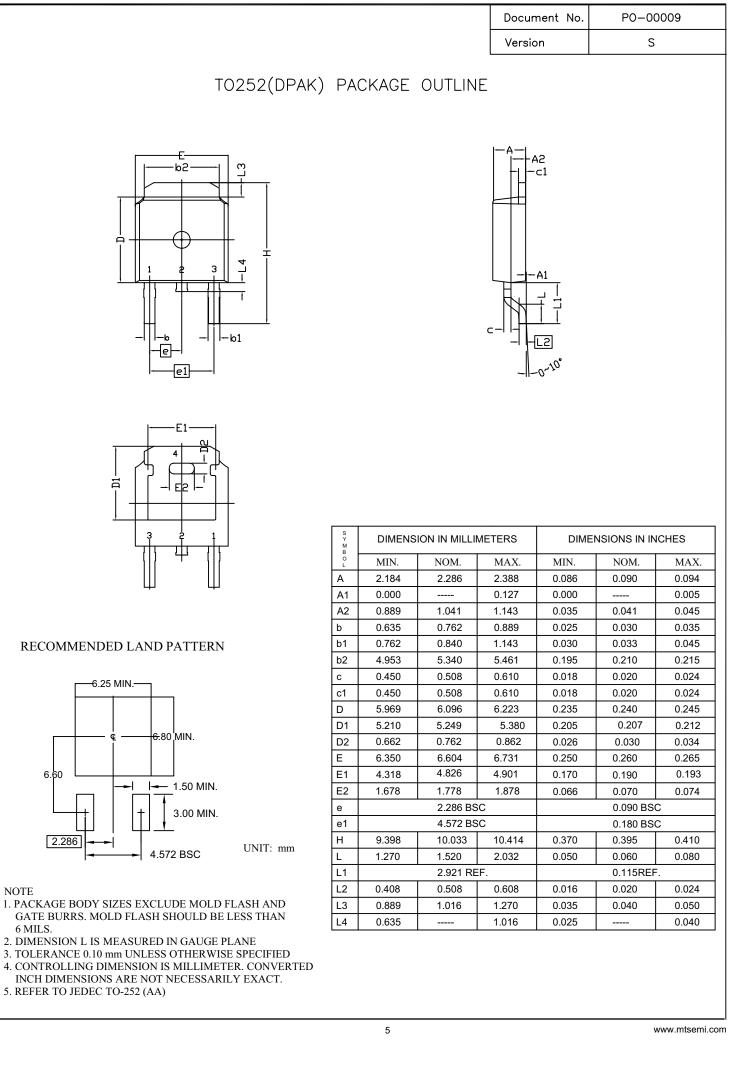
Thermal Resistance, Junction-to Ambient ^a Rth JA40°C/W

Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS				1	I	
Drain-Source Breakdown Voltage	BVDSS	VGS=0V,ID=-250µA	30			V
Zero Gate Voltage Drain Current	Idss	VDS=30V,VGS=0V			1	μA
Gate-Body Leakage	Igss	VGS=±16V,VDS=0V			±100	nA
ON CHARACTERITICS						
Gate Threshold Voltage	VGs(th)	VDS=VGS,ID=-250µA	2		4	V
Drain-Source On-State Resistance	_	VGS=10V,ID=30A		4.5	6	- mΩ
	Rds(on)	Vgs=4.5V,Id=30A		7.5	9	
Forward Transconductance	gfs	VGS=15V,ID=15A		50		S
DAYNAMIC CHARACTERISTICS				1	I	
Input Capacitance	Ciss	VDS=15V,VGS=0V f=1.0MHz		2325.		pF
Output Capacitance	Coss			330		pF
Reverse Transfer Capacitance	Crss	I-1.0MHZ		173		pF
SWITCHING CHARACTERISISTICS						
Turn-On Delay Time	td(on)	VDD=15V ID=1A, VGEN=10V RL=150hm RGEN=60hm		15.3		ns
Rise Time	tr			4		ns
Turn-Off Delay Time	td(off)			45.7		ns
Fall Time	tf			7.6		ns
Total Gate Charge	Qg	Vds=15V,Id=20A Vgs=5V Rgen=4.70hm		17	75	nC
Gate-Source Charge	Qgs			6		nC
Gate-Drain Charge	Qgd			5		nC





4



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