# MT6680

# N-Channel PowerTrench<sup>®</sup> MOSFET 30V, 15A, $9m\Omega$

# **General Description**

This N-Channel MOSFET is produced using Mos-tech Semiconductor's advanced Power mosfet process that has been especially tailored to minimize the on-state resistance. This device is well suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.

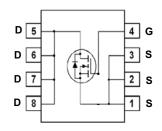
# **Features**

- $R_{DS(on)} = 9m \Omega$ ,  $V_{GS} = 10V$ ,  $I_D = 15A$
- $R_{DS(on)} = 12m \Omega$ ,  $V_{GS} = 4.5V$ ,  $I_D = 12.6A$
- HBM ESD protection level of 3KV typical (note 3)
- High performance trench technology for extremely low RDS(ON)
- · High power and current handling capability
- RoHS compliant



http://www.mtsemi.com

# **Simplified Schematic**



MARKING DIAGRAM & PIN ASSIGNMENT



# **Absolute Maximum Ratings**(T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter		Ratings	Units
V <sub>DS</sub>	Drain to Source Voltage		30	V
V <sub>GS</sub>	Gate to Source Voltage		±20	V
1	Drain Current -Continuous	(Note 1a)	15	^
ID	-Pulsed		60	A
E <sub>AS</sub>	Single Pulse Avalanche Energy	(Note 4)	181	mJ
D	Power Dissipation	(Note 1a)	2.5	W
$P_{D}$	Power Dissipation	(Note 1b)	1.0	VV
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C

# **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case	(Note 1)	25	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1a)	50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	125	

## **Package Marking and Ordering Information**

Device Marking	Device	Reel Size	Tape Width	Quantity
MT6680	MT6680	13"	12mm	2500 units

# **Electrical Characteristics** $T_J = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Parameter Test Conditions				
Off Chara	off Characteristics					
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, referenced to 25°C		20		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V			30	μА
I <sub>GSS</sub>	Gate to Source Leakage Current	$V_{GS} = \pm 20V, \ V_{DS} = 0V$			±100	nA

# On Characteristics (Note 2)

V <sub>GS(th)</sub>	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	1	1.8	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate to Source Threshold Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, referenced to 25°C		-6		mV/°C
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		9	10	
r <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 12.6A		12	13	mΩ
, ,		$V_{GS} = 10V, I_D = 15A T_J = 125^{\circ}C$		10	13	
9 <sub>FS</sub>	Forward Transconductance	$V_{DS} = 5V, I_{D} = 15A$		54		S

# **Dynamic Characteristics**

C <sub>iss</sub>	Input Capacitance	V - 45V V - 0V	1805	2400	pF
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1MHz	335	445	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	1 - 1141112	200	300	pF
$R_g$	Gate Resistance	f = 1MHz	1.4		Ω

# **Switching Characteristics**

t <sub>d(on)</sub>	Turn-On Delay Time		$V_{DD} = 15V, I_D = 15A$ $V_{GS} = 10V, R_{GEN} = 6\Omega$		11	22	ns
t <sub>r</sub>	Rise Time	$V_{DD} = 15V, I_D = 15$			13	26	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GS</sub> = 10V, R <sub>GEN</sub>			25	40	ns
t <sub>f</sub>	Fall Time				7	14	ns
Qg	Total Gate Charge	V <sub>GS</sub> = 0V to 10V	V <sub>DD</sub> = 15V		32	45	nC
Qg	Total Gate Charge	V <sub>GS</sub> = 0V to 5V	I <sub>D</sub> = 15A		17	24	nC
Q <sub>gs</sub>	Gate to Source Charge				6		nC
$Q_{gd}$	Gate to Drain "Miller" Charge				7		nC

# **Drain-Source Diode Characteristics**

$V_{SD}$	Source to Drain Diode Forward Voltage	$V_{GS} = 0V, I_{S} = 2.1A$ (Note 2)	0.8	1.2	V
t <sub>rr</sub>	Reverse Recovery Time	- I <sub>E</sub> = 15A. di/dt = 100A/μs	24	36	ns
Q <sub>rr</sub>	Reverse Recovery Charge	-1 <sub>F</sub> = 15A, αι/αι = 100A/μs	15	23	nC

Notes:

1. R<sub>B,JA</sub> is the sum of the junction-to-case and case-to- ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>B,JC</sub> is guaranteed by design while R<sub>B,JA</sub> is determined by the user's board design.



a) 50°C/W when mounted on a 1in<sup>2</sup> pad of 2 oz copper.



**b)** 125°C/W when mounted on a minimum pad .

- Pulse Test: Pulse Width < 300 us, Duty Cycle < 2%.</li>
   The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.
   Starting T<sub>J</sub> = 25°C, L = 3mH, I<sub>AS</sub> = 11A, V<sub>DD</sub> = 30V, V<sub>GS</sub> = 10V.

# Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

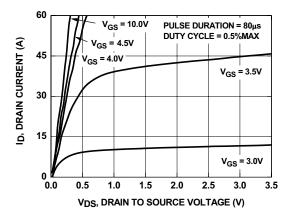


Figure 1. On-Region Characteristics

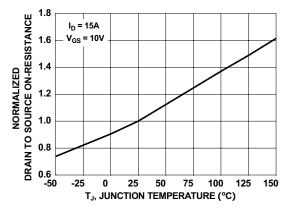


Figure 3. Normalized On-Resistance vs Junction Temperature

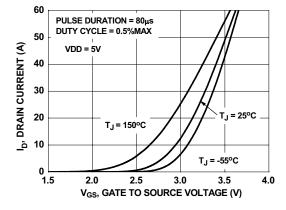


Figure 5. Transfer Characteristics

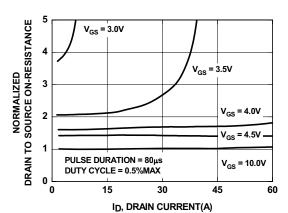


Figure 2. Normalized On-Resistance vs Drain Current and Gate Voltage

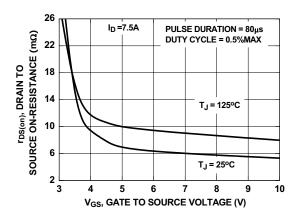


Figure 4. On-Resistance vs Gate to Source Voltage

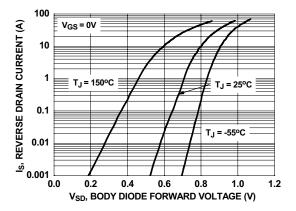


Figure 6. Source to Drain Diode Forward Voltage vs Source Current

# Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

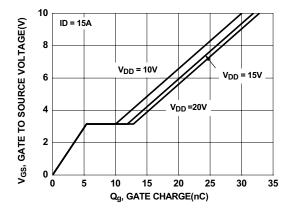


Figure 7. Gate Charge Characteristics

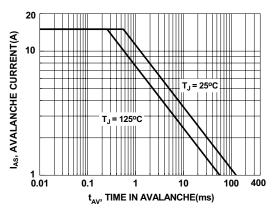


Figure 9. Unclamped Inductive Switching Capability

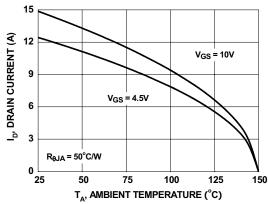


Figure 11. Maximum Continuous Drain Current vs Ambient Temperature

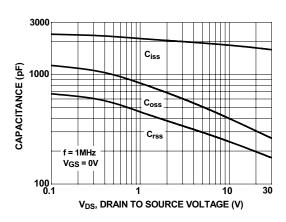


Figure 8. Capacitance vs Drain to Source Voltage

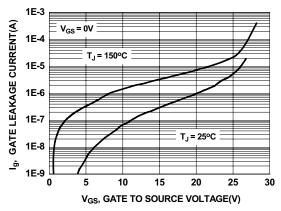


Figure 10. Gate Leakage Current vs Gate to Source Voltage

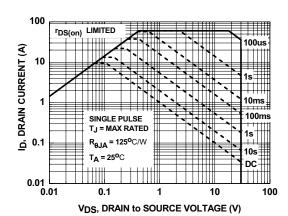


Figure 12. Forward Bias Safe Operating Area

# Typical Characteristics T<sub>J</sub> = 25°C unless otherwise noted

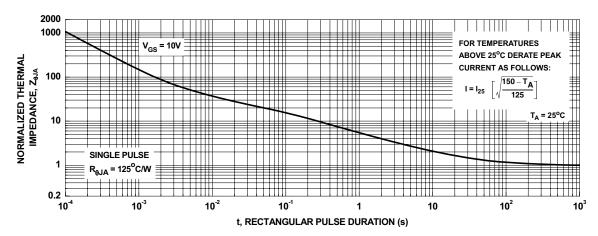


Figure 13. Single Pulse Maximum Power Dissipation

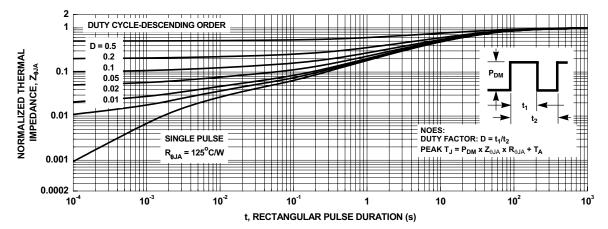
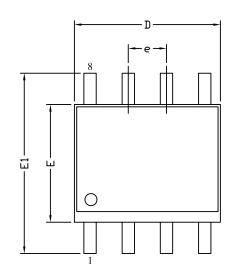
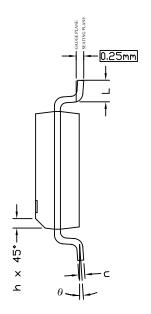


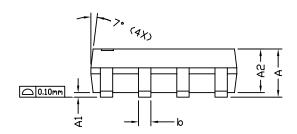
Figure 14. Transient Thermal Response Curve

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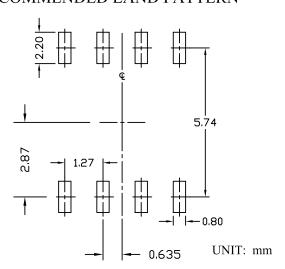
# SO8 PACKAGE OUTLINE







# RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIO	NS IN MILL	IMETERS	DIME	NSIONS IN IN	ICHES
31 MBOLS	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	1.65	1.75	0.053	0.065	0.069
A1	0.10		0.25	0.004		0.010
A2	1.25	1.50	1.65	0.049	0.059	0.065
ь	0.31		0.51	0.012		0.020
С	0.17		0.25	0.007		0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
Е	3.80	3.90	4.00	0.150	0.154	0.157
e	1	.27 BSC		(	0.050 BSC	
E1	5.80	6.00	6.20	0.228	0.236	0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
θ	00		80	00		80

# NOTE

- 1. ALL DIMENSIONS ARE IN MILLMETERS.
- 2. DIMENSIONS ARE INCLUSIVE OF PLATING.
- 3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.

6

- 4. DIMENSION L IS MEASURED IN GAUGE PLANE.
- 5. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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