MT8275N5

N-Channel Enhancement Mode Field **Effect Transistor**

Product Summary

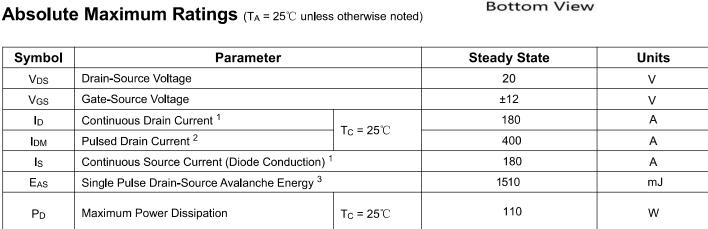
- V_{DS} = 20V
- $I_D = 180A$
- R DS(ON) = $1.4m\Omega$ @V_{GS} = 4.5V
- R DS(ON) = $1.6m\Omega @V_{GS} = 2.5V$

Features

- · Advanced Trench Process Technology.
- · High Density Cell Design for Ultra Low On-Resistance.
- · Lead free product is acquired.
- · RoHS Compliant.
- · PDFN5x6-8L Package

Applications

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



TJ, TSTG

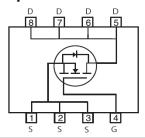
- 1. Surface Mounted on 1" x 1" FR4 Board, t≦10 Sec.
- 2. Pulse width limited by maximum junction temperature.
- 3. The test condition is $T_J = 25^{\circ}C$, $V_{DD} = 30V$, $V_{GS} = 10V$, L = 0.1mH, $R_G = 25\Omega$, $I_{AS} = 50$ A.

Operating Junction and Storage Temperature Range

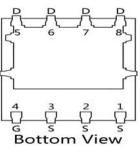


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Simplified Schematic



MARKING DIAGRAM **& PIN ASSIGNMENT**



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 $^{\circ}$ C

-55~150

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	1.15	°C/W	
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Electrical Characteristics (Tc=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	•		•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics						•
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	0.5	0.75	1.2	V
David Carray Car Olata Basistana a	Б	V _{GS} =4.5V, I _D =20A	-	1.4	2.0	
Drain-Source On-State Resistance ^a	R _{DS(ON)}	V _{GS} =2.5V, I _D =15A		1.6	2.4	mΩ
Forward Transconductance	g Fs	V _{DS} =5V,I _D =20A	100	-	-	S
Dynamic Characteristics ^b			•			
Input Capacitance	C _{lss}	V _{DS} =10V,V _{GS} =0V, F=1.0MHz - 5000 1200 900	-	5000	-	PF
Output Capacitance	C _{oss}		-	1200	-	PF
Reverse Transfer Capacitance	C _{rss}		-	PF		
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =10V, I_D =2A, R_L =15 Ω	-	13	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =2.5 Ω	-	45	-	nS
Turn-Off Fall Time	t _f		-	32	-	nS
Total Gate Charge	Qg	\/ -40\/ L -20A	-	70		nC
Gate-Source Charge	Q_{gs}	$V_{DS}=10V,I_{D}=20A,$	-	16		nC
Gate-Drain Charge	Q_{gd}	- V _{GS} =10V	-	20		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =20A	-		1.2	V
Diode Forward Current	Is		-	-	150	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 20A	-	49	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/μs	-	66	-	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negli	gible (turi	n-on is do	ominated b	y LS+LD)
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+L				

Note:

a. Pulse test; pulse width≦300µs, duty cycle≦2%.

b. Guaranteed by design, not subject to production testing.

Typical Electrical and Thermal Characteristics (Curves)

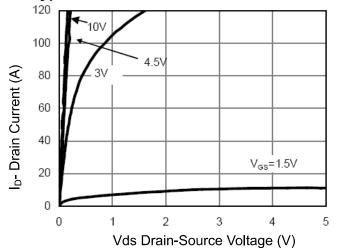


Figure 1 Output Characteristics

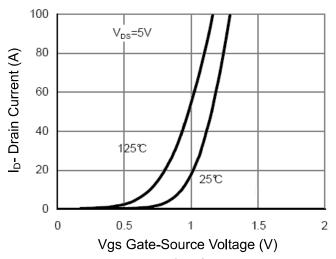


Figure 2 Transfer Characteristics

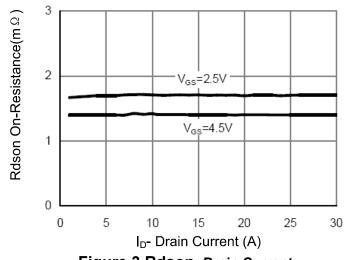


Figure 3 Rdson- Drain Current

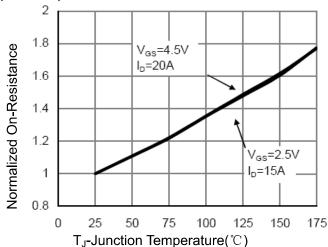


Figure 4 Rdson-JunctionTemperature

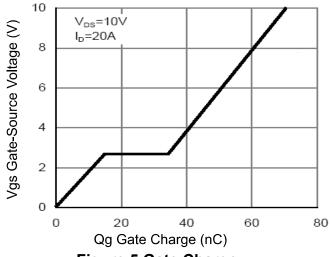


Figure 5 Gate Charge

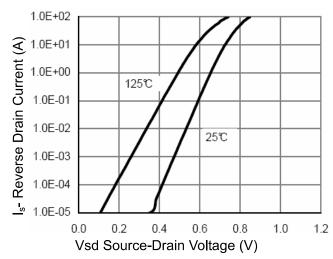
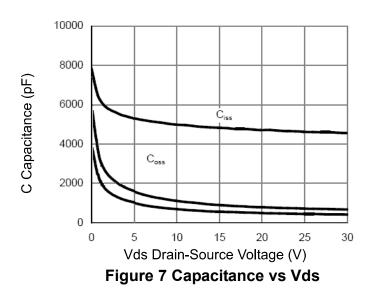
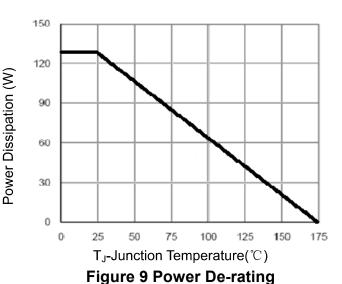
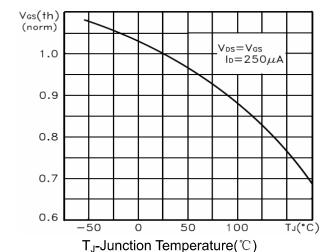


Figure 6 Source- Drain Diode Forward

3





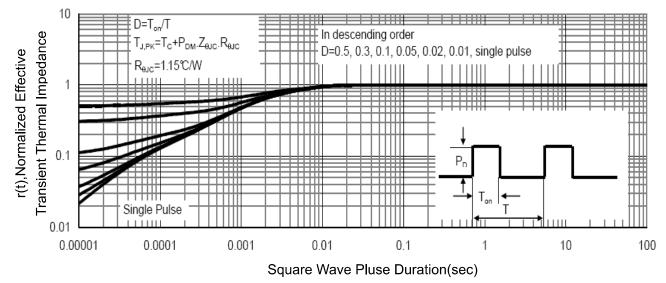


Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

10

Figure 10 V_{GS(th)} vs Junction Temperature

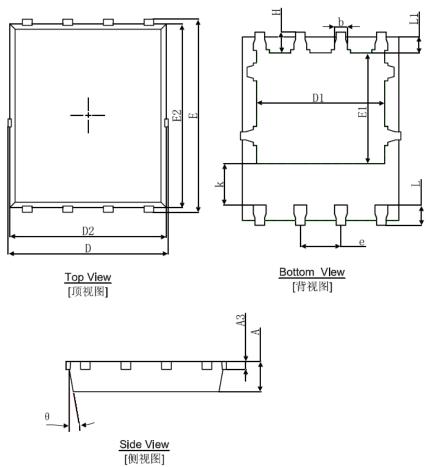


100

Figure 11 Normalized Maximum Transient Thermal Impedance

4

PDFN5X6-8L Package Information



	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	0.900	1.000	0.035	0.039	
A3	0.254	IREF.	0.010REF.		
D	4.944	5.096	0.195	0.201	
Е	5.974	6.126	0.235	0.241	
D1	3.910	4.110	0.154	0.162	
E1	3.375	3.575	0.133	0.141	
D2	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
К	1.190	1.390	0.047	0.055	
b	0.035	0.450	0.014	0.018	
е	1.270(TYP.)		0.050(TYP.)		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	8°	12°	8°	12°	

5

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