MT82P06N3

P-Channel Enhancement Mode Field Effect Transistor

Product Summary

- V_{DS} = -20V
- ID= -55A
- RDS(ON) 5.5m Ω @VGS = -4.5V
- RDS(ON) 7.5 mΩ @VGS= -2.5V

Features

Advanced Trench Process Technology.

Absolute Maximum Ratings (T_A = 25[°]C unless otherwise noted)

- High Density Cell Design for Ultra Low
- On-Resistance.
- · Lead free product is acquired.
- · RoHS Compliant.

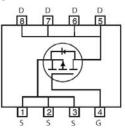
Applications

- Notebook Computer
- Portable Battery Pack

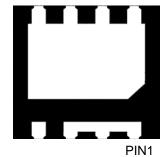
MT Semiconductor®

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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



66

-55 To 150

DFN3X3-8L

 P_{D}

 T_J, T_{STG}

Parameter	Symbol	Limit	
Drain-Source Voltage	Vds	-20	
Gate-Source Voltage	Vgs	±12	
Drain Current-Continuous	I _D	-55	
Drain Current-Pulsed (Note 1)	Или	-220	

Thermal Characteristic

Operating Junction and Storage Temperature Range

Maximum Power Dissipation

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{ extsf{ heta}JA}$	1.9	°C/W

1

Unit V V A A

W

°C

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	· · ·					•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-20	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-	-0.7	-1.0	V
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-10A	-	5.5	7.0	mΩ
	R _{DS(ON)}	V_{GS} =-2.5V, I _D = -5A	-	7.5	10	mΩ
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	N/ 401/11/ 01/	-	3460	-	PF
Output Capacitance	C _{oss}	V _{DS} =-10V,V _{GS} =0V, F=1.0MHz	_	545	-	PF
Reverse Transfer Capacitance	C _{rss}		_	490	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		_	11	-	nS
Turn-on Rise Time	tr	V_{DD} =-10V, ,RL=2 Ω	_	110	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =3 Ω	_	157	_	nS
Turn-Off Fall Time	t _f		_	160	_	nS
Total Gate Charge	Qg		_	56	_	nS
Gate-Source Charge	Q _{gs}	V _{DS} =-4.5V,I _D =-15A, V _{GS} =-4.5V	_	8	_	nS
Gate-Drain Charge	Q _{gd}		_	16	-	nS
Drain-Source Diode Characteristics	· · ·					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-30A	-	-0.7	-1.3	V
Diode Forward Current (Note 2)	Is		-	-	-55	A

Electrical Characteristics (T_A=25 $^{\circ}$ Cunless otherwise noted)

Notes

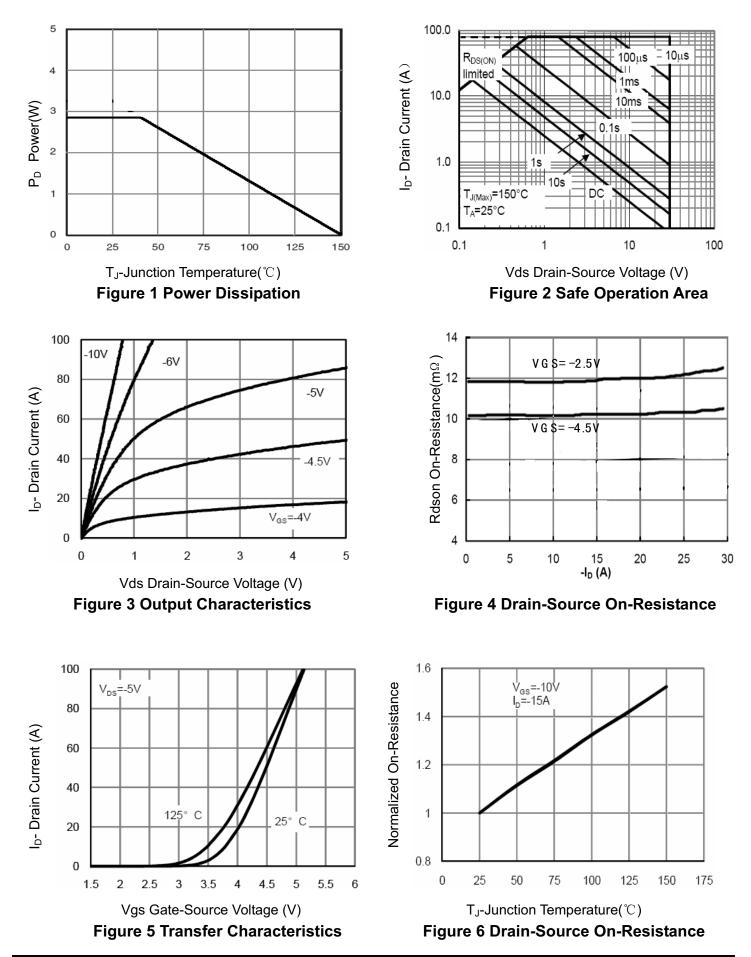
1. Repetitive Rating: Pulse width limited by maximum junction temperature.

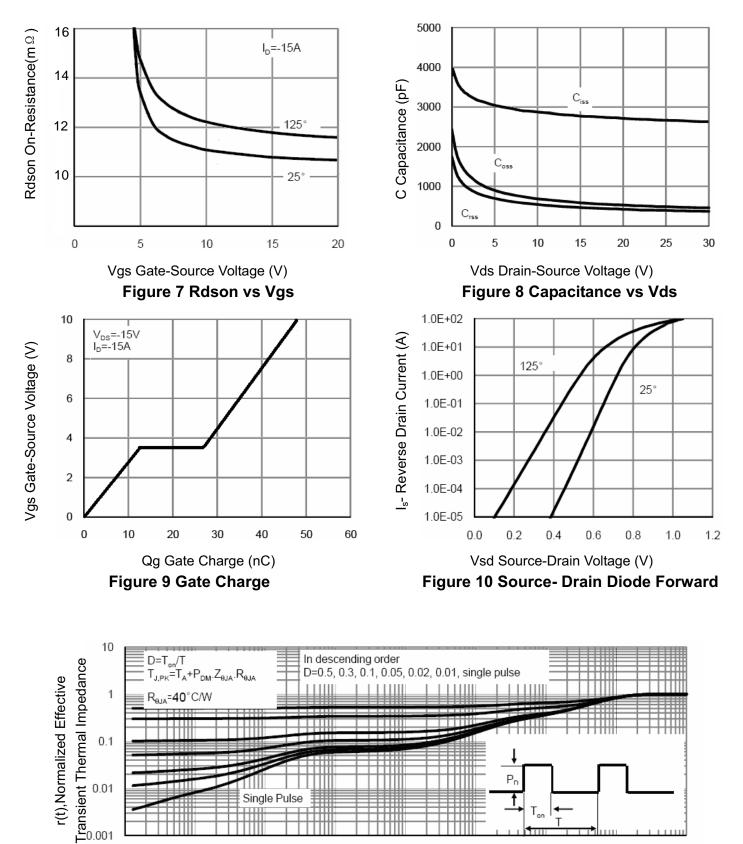
2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

Characteristics Curve (TA=25°C, unless otherwise noted)





Characteristics Curve (TA=25°C, unless otherwise noted)

0.00001

0.0001

0.001



0.01

0.1

Square Wave Pluse Duration(sec)

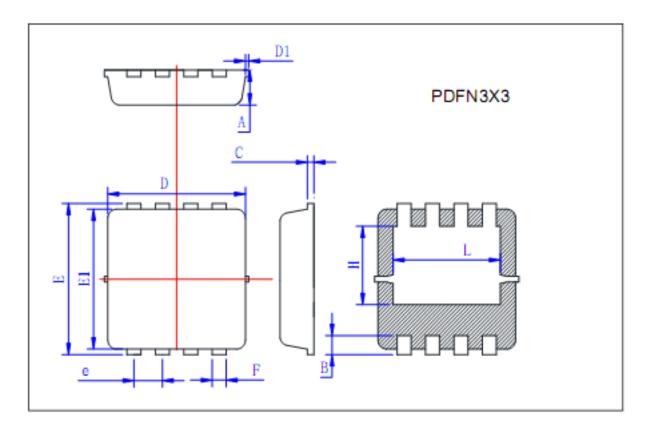
1

10

100

1000

PACKAGE OUTLINE DIMENSIONS



Symbol	Min	Тур	Max
Α	0.725	0.775	0.825
В	0.28	0.38	0.48
С	0.13	0.15	0.20
D	3.05	3.15	3.25
D1			0.10
Е	3.25	3.35	3.45
El	3.0	3.1	3.2
e	0.60	0.65	0.70
F	0.27	0.32	0.37
Н	1.63	1.73	1.83
L	2.35	2.45	2.55

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