

MT8318M5

N-Channel Enhancement Mode Power MOSFET



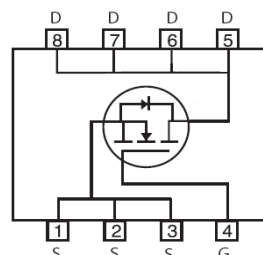
MT Semiconductor®

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Feature Description

- 30V/180A
 $R_{DS(ON)} = 1.3m\Omega(\text{typ.}) @ V_{GS} = 10V$
 $R_{DS(ON)} = 1.6m\Omega(\text{typ.}) @ V_{GS} = 4.5V$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen- Free Devices Available

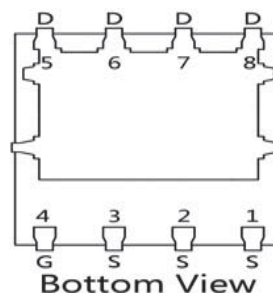
Simplified Schematic



Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Power Tool Application
- Networking DC-DC Power System

MARKING DIAGRAM & PIN ASSIGNMENT



Absolute Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
Common Ratings (T_c=25°C Unless Otherwise Noted)			
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±20	V
T _J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
I _S	Source Current-Continuous(Body Diode)	T _c =25°C 180	A
Mounted on Large Heat Sink			
I _{DM}	Pulsed Drain Current *	T _c =25°C 720	A
I _D	Continuous Drain Current	T _c =25°C 180	A
		T _c =100°C 114	A
P _D	Maximum Power Dissipation	T _c =25°C 104	W
		T _c =100°C 42	W
R _{θJC}	Thermal Resistance, Junction-to-Case	1.2	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient **	62	°C/W
E _{AS}	Single Pulsed-Avalanche Energy ***	L=0.1mH 287.2	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
 ** Surface mounted on FR-4 board.
 *** Limited by T_{Jmax} , starting $T_J=25^{\circ}C$, $L = 0.1mH$, $R_G = 25\Omega$, $V_{GS} = 10V$.

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
MT8318M5	MT8318M5	DFN5X6-8L	-	-	-

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
Common Ratings ($T_c=25^{\circ}C$ Unless Otherwise Noted)				
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$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		62	$^{\circ}C/W$
E_{AS}	SinglePulsed-Avalanche Energy ***	$L=0.1mH$	287.2	mJ

Electrical Characteristics ($T_c = 25^{\circ}C$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit
			Min	Typ.	Max	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
I_{DSS}	Drain-to-Source Leakage Current	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
		$T_J=55^{\circ}C$	-	-	5	μA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1	1.5	3	V
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)*}$	Drain-Source On-State Resistance	$V_{GS}=10V, I_{DS}=20A$	-	1.3	1.6	m Ω
		$V_{GS}=4.5V, I_{DS}=20A$	-	1.6	2.0	
Diode Characteristics						
V_{SD}^*	Diode Forward Voltage	$I_{SD}=20A, V_{GS}=0V$	-	0.7	1.0	V
t_{rr}	Reverse Recovery Time	$I_{SD}=20A, dI_{SD}/dt=100A/\mu s$	-	32	-	ns
Q_{rr}	Reverse Recovery Charge		-	70	-	nC

Electrical Characteristics (Cont.) (T_c =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1 MHz	-	0.9	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =25V, Frequency=1.0MHz	-	4265	-	pF
C _{oss}	Output Capacitance					
C _{rss}	Reverse Transfer Capacitance					
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V, R _G =3.3Ω, I _{DS} =20A, V _{GS} =10V	-	30	-	ns
T _r	Turn-on Rise Time					
t _{d(OFF)}	Turn-off Delay Time					
T _f	Turn-off Fall Time					
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =24V, V _{GS} =10V, I _D =20A	-	70	-	nC
Q _{gs}	Gate-Source Charge					
Q _{gd}	Gate-Drain Charge					

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

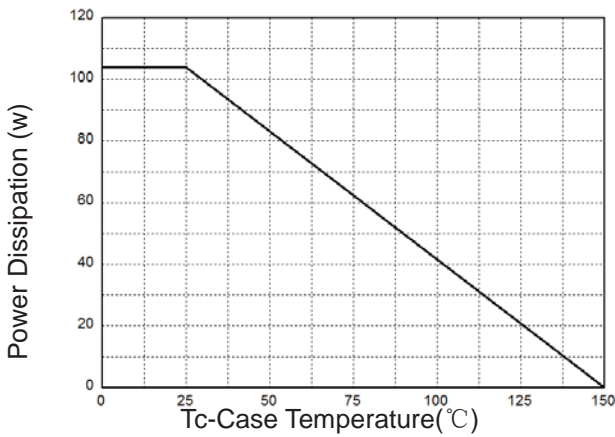


Figure 2: Drain Current

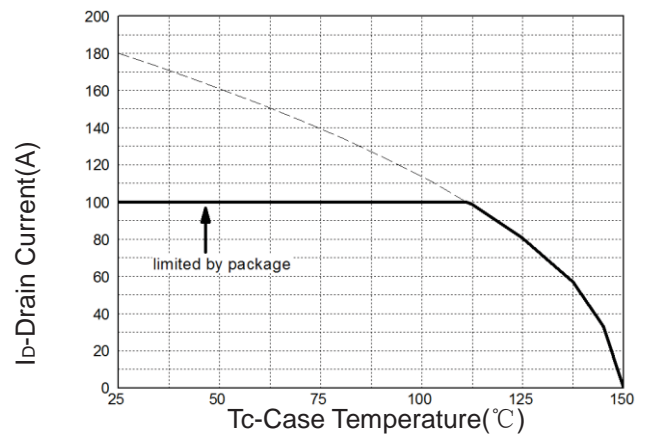


Figure 3: Safe Operation Area

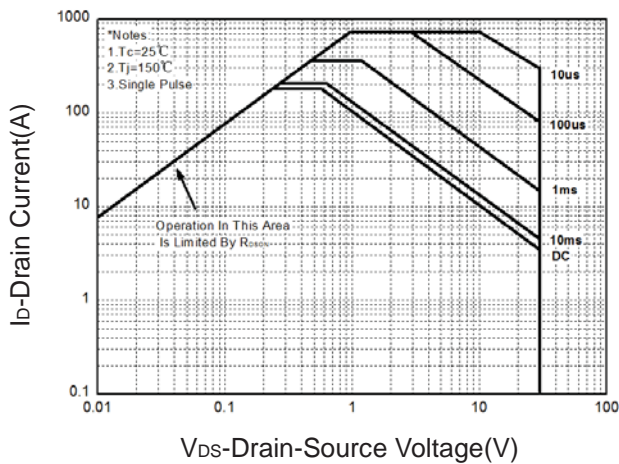


Figure 4: Thermal Transient Impedance

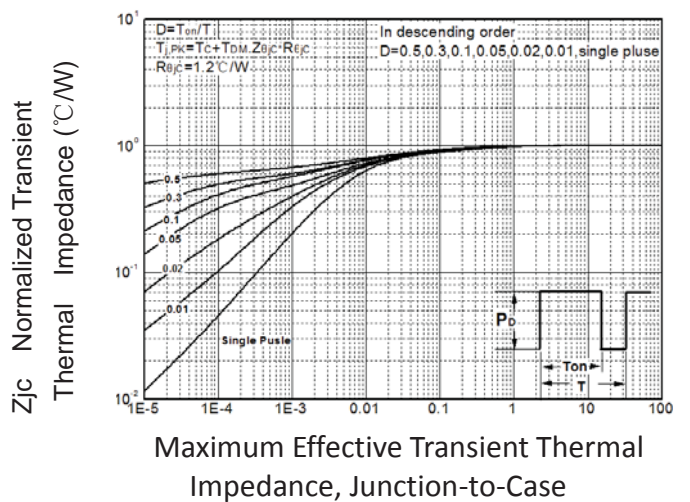


Figure 5: Output Characteristics

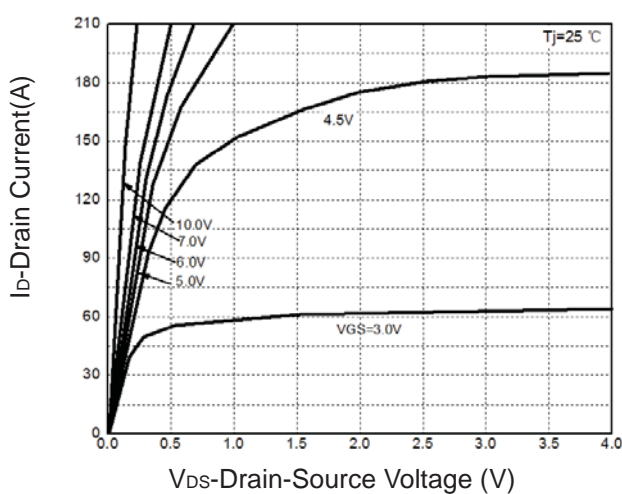
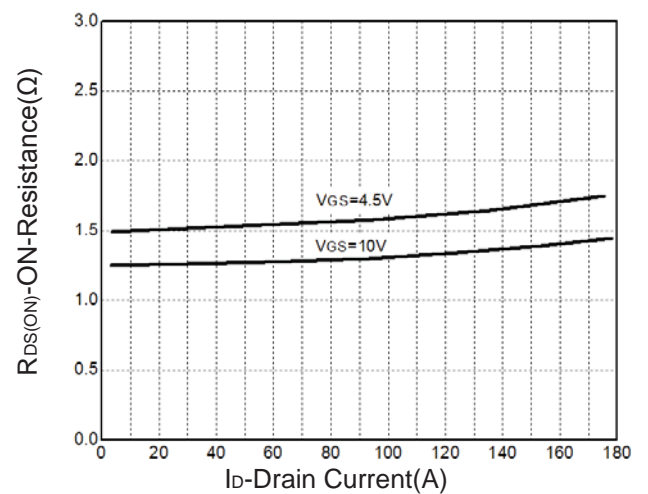


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

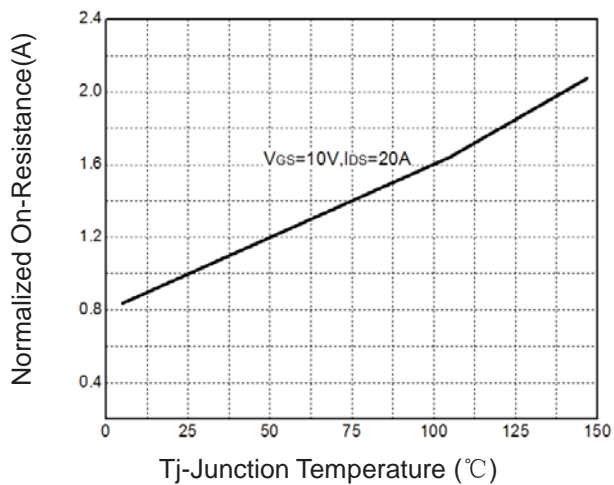


Figure 8: Source-Drain Diode Forward

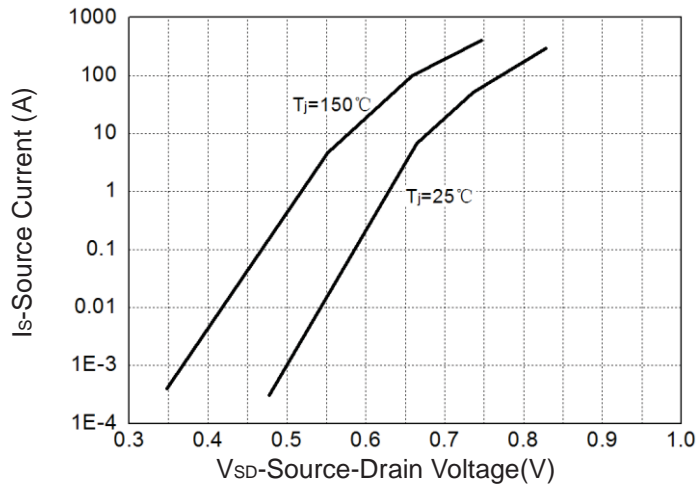


Figure 9: Capacitance Characteristics

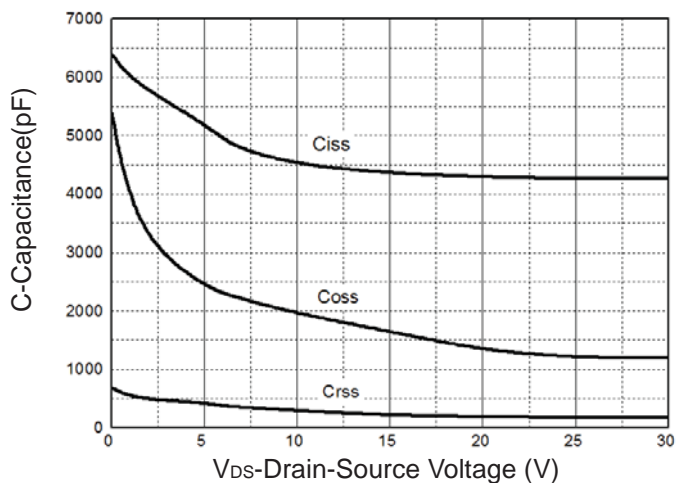
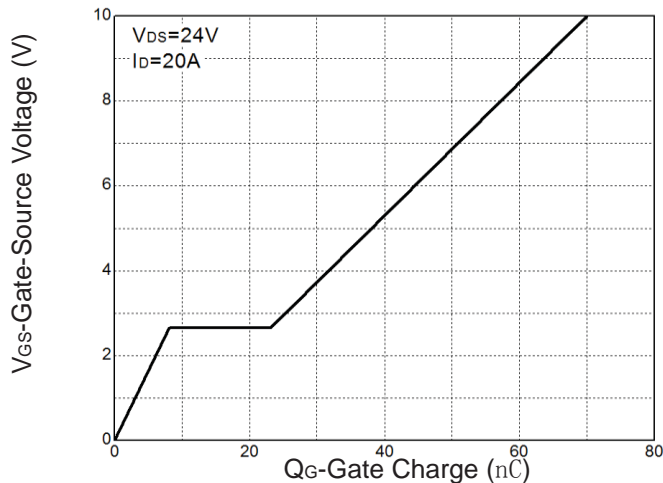
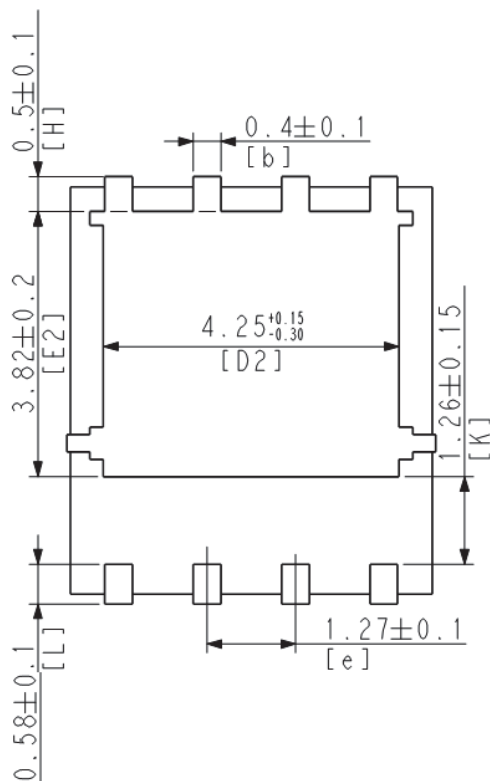
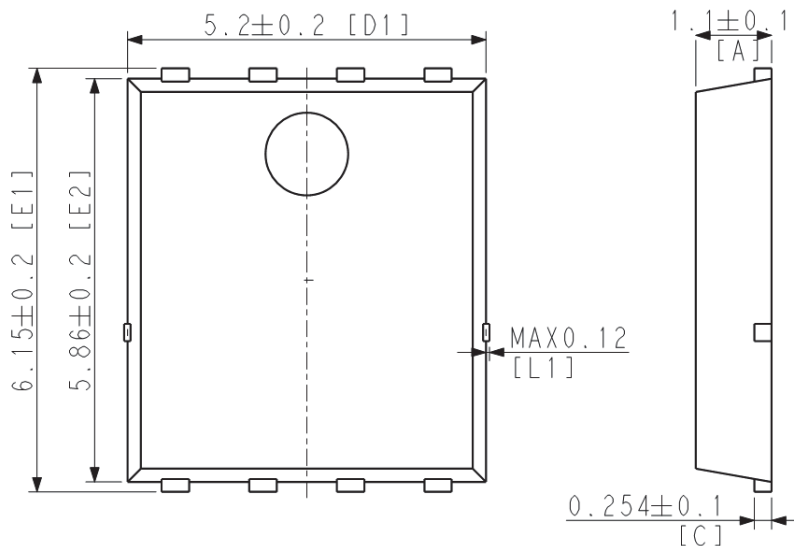


Figure 10: Gate Charge Characteristics



Package Information

PDFN5*6-8L



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